

### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			$V_{DSS}$	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> =10V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	260 200	mA
Continuous Drain Current (Note 5) V <sub>GS</sub> = 5V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	220 160	mA
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	800	mA		

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 5)	$P_{D}$	450	mW
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	281	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to 150	°C

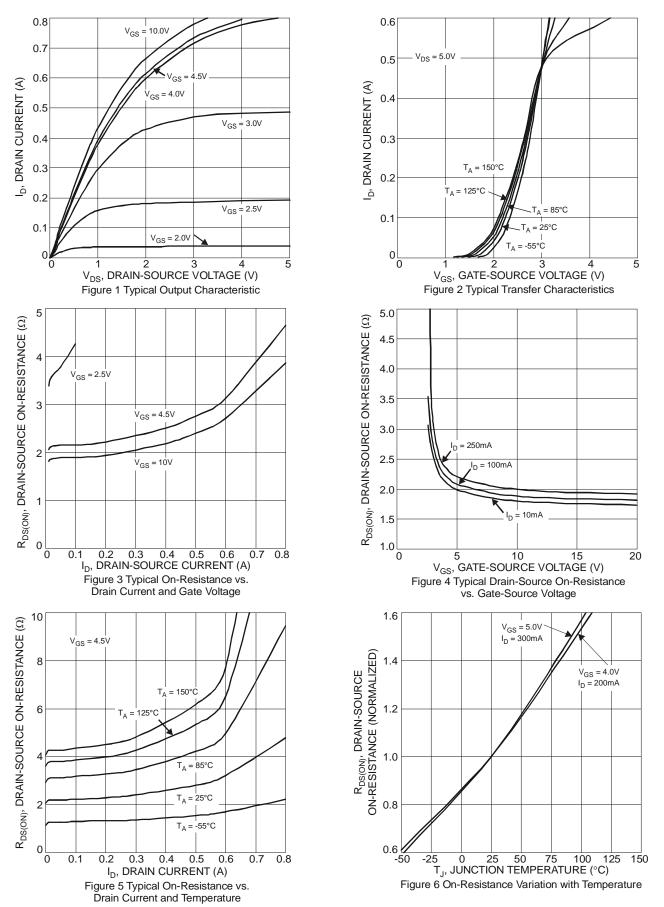
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>			±10.0	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(th)}$	8.0	_	1.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
		_	_	2.8		$V_{GS} = 10.0V, I_D = 250mA$	
		_	_	3.8		$V_{GS} = 5.0V, I_D = 250mA$	
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)			4.2	Ω	$V_{GS} = 4.5V, I_D = 250mA$	
		_	_	4.5		$V_{GS} = 4.0V, I_D = 250mA$	
		_	_	13		$V_{GS} = 2.5V, I_D = 10mA$	
Forward Transconductance	g <sub>FS</sub>	80		_	mS	$V_{DS} = 10V, I_D = 0.115A$	
Diode Forward Voltage	V <sub>SD</sub>	-	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	22.0			$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	
Output Capacitance	Coss	_	3.2		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.0	_			
Gate Resistance	$R_{G}$	_	79.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge V <sub>GS</sub> = 10V	Qg	_	0.87	_			
Total Gate Charge V <sub>GS</sub> = 4.5V	Qg	_	0.43	_	nC	$V_{GS} = 10V, V_{DS} = 30V,$ $I_{D} = 150mA$	
Gate-Source Charge	Q <sub>gs</sub>	_	0.11	_	nC		
Gate-Drain Charge	$Q_{qd}$		0.11	_			
Turn-On Delay Time	t <sub>D(on)</sub>		3.3	_		V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.115A, V <sub>GEN</sub> = 10V.	
Turn-On Rise Time	t <sub>r</sub>		3.2	_	nS		
Turn-Off Delay Time	t <sub>D(off)</sub>		12.0	_	no	$R_{GEN} = 25\Omega$	
Turn-Off Fall Time	t <sub>f</sub>	_	6.3	_			

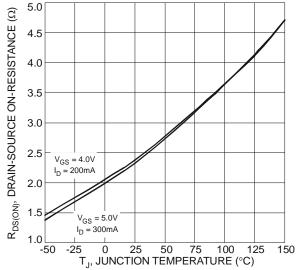
Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6 .Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to production testing.

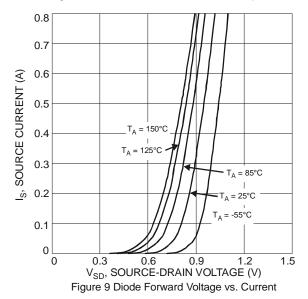












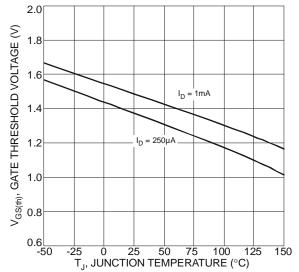
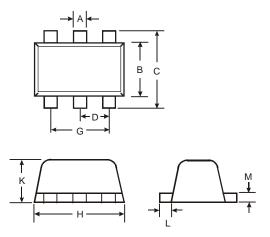


Figure 8 Gate Threshold Variation vs. Ambient Temperature



## **Package Outline Dimensions**

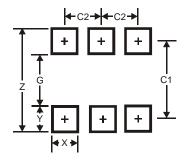
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT563					
Dim	Min Max		Тур		
Α	0.15	0.30	0.20		
В	1.10	1.25	1.20		
С	1.55	1.70	1.60		
D	-	-	0.50		
G	0.90	1.10	1.00		
Н	1.50	1.70	1.60		
K	0.55	0.60	0.60		
L	0.10	0.30	0.20		
M	0.10	0.18	0.11		
All	All Dimensions in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)				
Z	2.2				
G	1.2				
Х	0.375				
Υ	0.5				
C1	1.7				
C2	0.5				



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