June 2018



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Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Dusin Courset (Alata 7) V 40V	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	10.3 8.3	А
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +100$ °C	ΙD	45 28	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	58.3	Α	
Maximum Continuous Body Diode Forward Current (Note 7)		Is	3	Α
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	33.3	Α
Avalanche Energy, L = 0.1mH		Eas	56.8	mJ

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		$P_D$	1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		123	°C/W
	t < 10s	$R_{\theta JA}$	69	
Total Power Dissipation (Note 7)		$P_D$	2.1	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{ hetaJA}$	60	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	t < 10s		34	
Total Power Dissipation (Note 7)		$P_D$	40	W
Thermal Resistance, Junction to Case (Note 7)		$R_{ heta JC}$	3.2	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

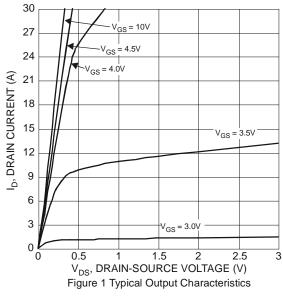
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current, T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	1.8	3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	9.3	13	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialii-Source Off-Resistance	R <sub>DS(ON)</sub>	_	12.3	18		$V_{GS} = 4.5V, I_D = 8A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.7A$	
DYNAMIC CHARACTERISTICS (Note 9)						•	
Input Capacitance	C <sub>iss</sub>	_	2577	_	pF	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output Capacitance	Coss	_	162	_	pF	$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	132	_	pF		
Gate Resistance	Rg	_	0.9	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	26.6	_	nC		
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	55.4	_	nC	7, 20,4 40,4	
Gate-Source Charge	Qgs	_	9.3	_	nC	$V_{DS} = 30V, I_{D} = 10A$	
Gate-Drain Charge	$Q_{gd}$	_	12.6	_	nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.2	_	ns	$V_{GS} = 10V, V_{DS} = 30V,$ $R_{G} = 3\Omega, I_{D} = 10A$	
Turn-On Rise Time	t <sub>R</sub>	_	9.9	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	27.6	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	11.7	_	ns		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	9.4	_	ns	-I <sub>F</sub> = 10A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	$Q_{RR}$	_	18.6	_	nC		

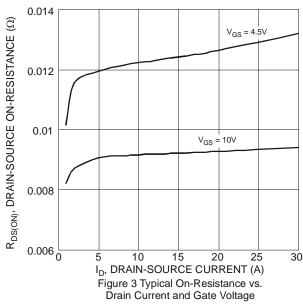
<sup>6.</sup> Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

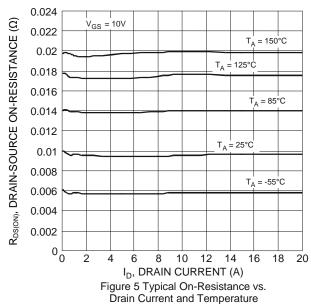
<sup>8.</sup> Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

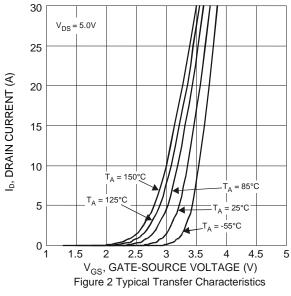


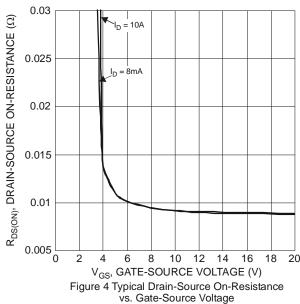












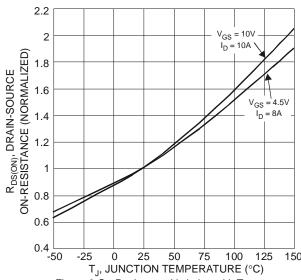
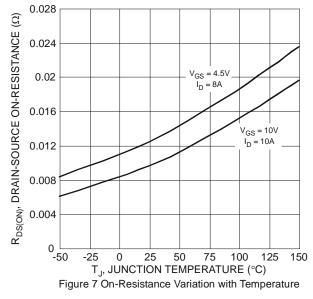
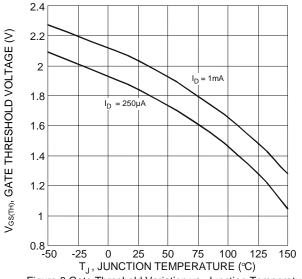


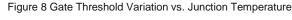
Figure 6 On-Resistance Variation with Temperature

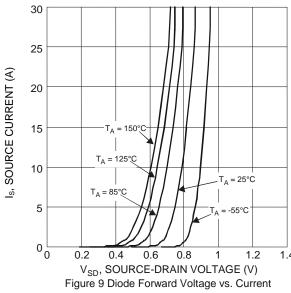


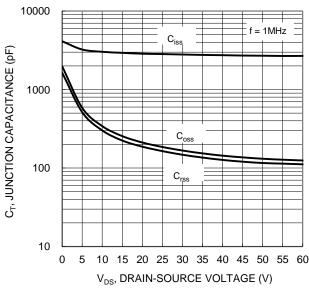


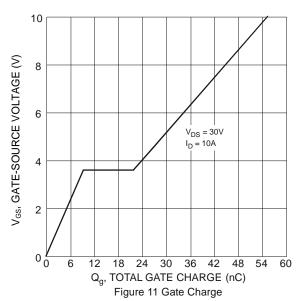


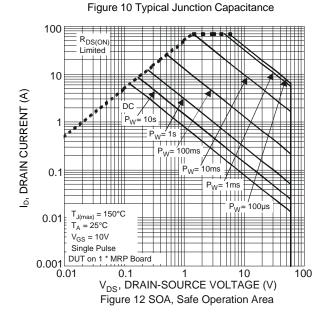




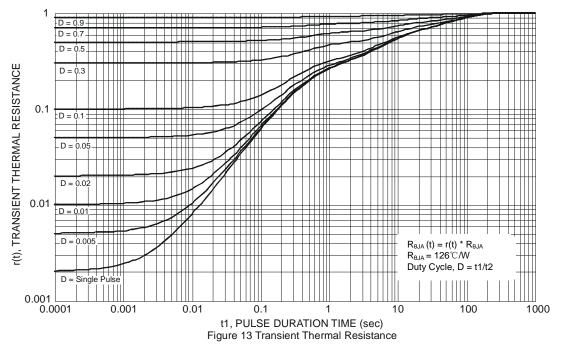










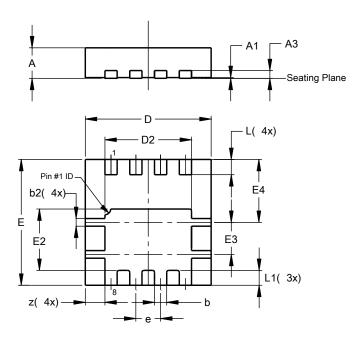




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8

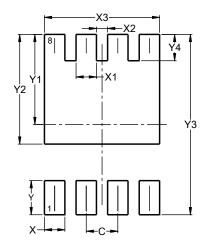


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	_	-	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
Е	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
e	I	-	0.65		
L	0.35	0.45	0.40		
L1	-	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
VΔ	0.540



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