

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 5) $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		6 5	А
Pulsed Drain Current (Note 6)	I _{DM}	24	A
Body-Diode Continuous Current (Note 5)	Is	2.25	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 5) t ≤10s	$R_{ heta}$ JA	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	۵°

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Turn	Мах	Unit	Test Condition	
	Symbol	WIIN	Тур	wax	Unit	Test Condition	
STATIC PARAMETERS		1	1	1			
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
5	+25°C +55°C	_	—	1 5	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage Current	I _{GSS}		—	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$	
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	2.1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance (Note 7)	R _{DS(ON)}	_	25 36	30 40	mΩ	V _{GS} = 10V, I _D = 6A V _{GS} = 4.5V, I _D = 5A	
Forward Transconductance (Note 7)	g fs	—	5	—	S	$V_{DS} = 10V, I_D = 8A$	
Diode Forward Voltage (Note 7)	V _{SD}	—	0.7	1.1	V	I _S = 2.25A, V _{GS} = 0V	
DYNAMIC PARAMETERS (Note 8)							
Total Gate Charge	Qg		10.5	_	nC	$V_{GS} = 5V, V_{DS} = 15V, I_D = 6A$	
Gate-Source Charge	Q _{gs}	—	3.8	_	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$	
Gate-Drain Charge	Q _{gd}	—	2.9	_	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$	
Turn-On Delay Time	t _{D(ON)}		11	_	ns		
Turn-On Rise Time	t _R		7	—	ns	$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}		63	_	ns	$R_D = 1.8\Omega, R_G = 6\Omega$	
Turn-Off Fall Time	t _F	—	30	_	ns		
Input Capacitance	C _{iss}	_	755	_	pF		
Output Capacitance	Coss	_	136	_	pF	− V _{DS} = 10V, V _{GS} = 0V − f = 1.0MHz	
Reverse Transfer Capacitance	Crss		108		pF		

Notes: 5. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t \leq 10s.

6. Repetitive Rating, pulse width limited by junction temperature.

7. Test pulse width t = 300ms.

8. Guaranteed by design. Not subject to production testing.



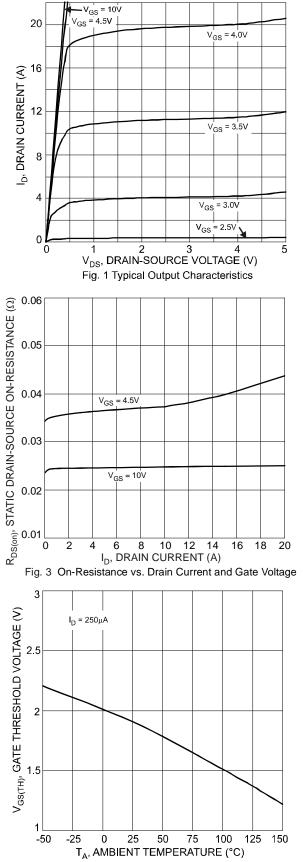
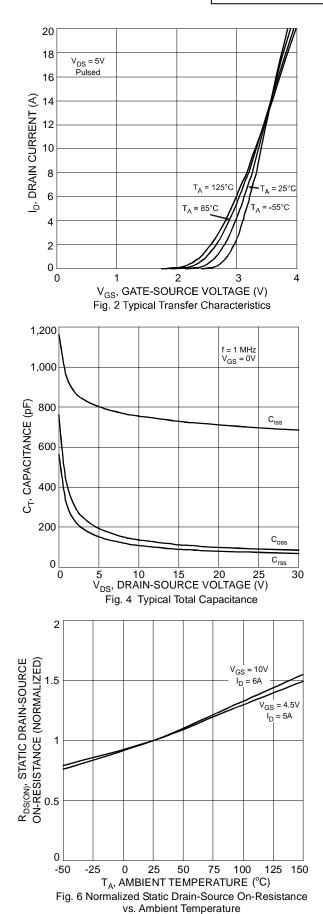
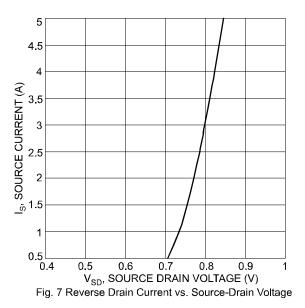


Fig. 5 Gate Threshold Voltage vs. Ambient Temperature

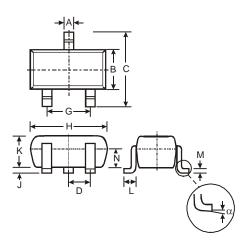






Package Outline Dimensions

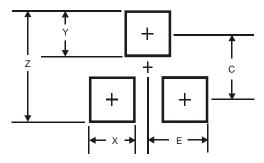
Please see http://www.diodes.com/package-outlines.html for the latest version.



SC59				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	-	0.95	
G	-	-	1.90	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
Κ	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
Ν	0.70	0.80	0.75	
α	0°	8°	-	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
E	1.35

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