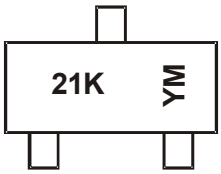
Marking Information



21K = Product Type Marking Code
YM = Date Code Marking
Y or $\overline{Y}$= Year (ex: D = 2016)
M = Month (ex: 9 = September)

Date Code Key

Year	2016	……	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	D	……	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	-2.4 -1.9	A
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	-1.12	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	-8	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	0.84	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	150	°C/W
Total Power Dissipation (Note 6)		P_D	1.40	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	91	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	V_{GS} = 0V, I_D = -250μA
Zero Gate Voltage Drain Current (T_J = +25°C)	I_{DSS}	—	—	-10	μA	V_{DS} = -16V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	μA	V_{GS} = ±10V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.3	-0.6	-1.0	V	V_{DS} = V_{GS}, I_D = -250A
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	136	160	mΩ	V_{GS} = -4.5V, I_D = -1.0A
			183	210		V_{GS} = -2.5V, I_D = -1.0A
			229	298		V_{GS} = -1.8V, I_D = -0.2A
Diode Forward Voltage	V_{SD}	—	-0.8	-1.2	V	V_{GS} = 0V, I_S = -1.0A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	156	—	pF	V_{DS} = -6V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	36	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	28	—	pF	
Gate Resistance	R_g	—	41	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	1.6	—	nC	V_{DS} = -6V, I_D = -2.2A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	3.4	—	nC	
Gate-Source Charge	Q_{gs}	—	0.3	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.4	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	3.2	—	ns	V_{DS} = -6V, V_{GS} = -4.5V, R_{GEN} = 6 Ω, I_D = -1A
Turn-On Rise Time	t_R	—	7.4	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	11.0	—	ns	
Turn-Off Fall Time	t_F	—	10.5	—	ns	
Reverse Recovery Time	t_{RR}	—	6.5	—	ns	I_F = -1.0A, di/dt = 100A/μs
Reverse Recovery Charge	Q_{RR}	—	0.8	—	nC	

Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

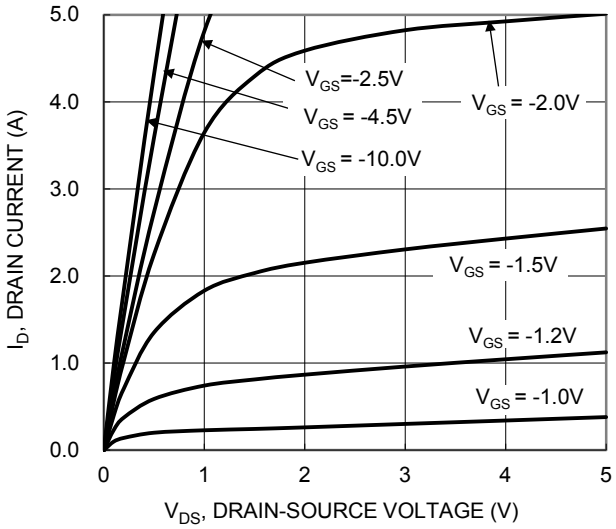


Figure 1. Typical Output Characteristic

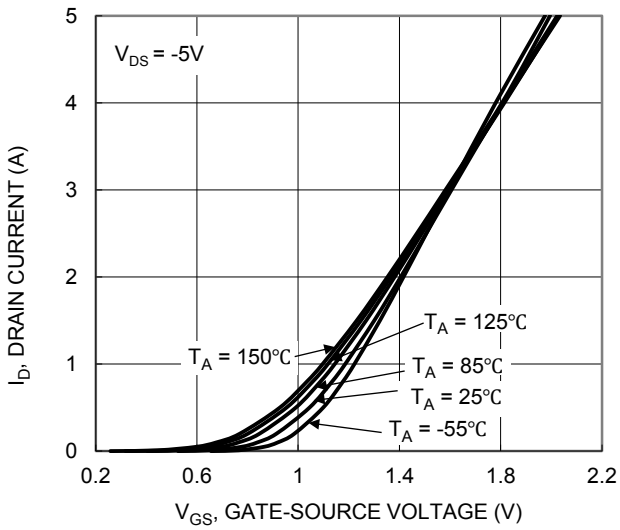


Figure 2. Typical Transfer Characteristic

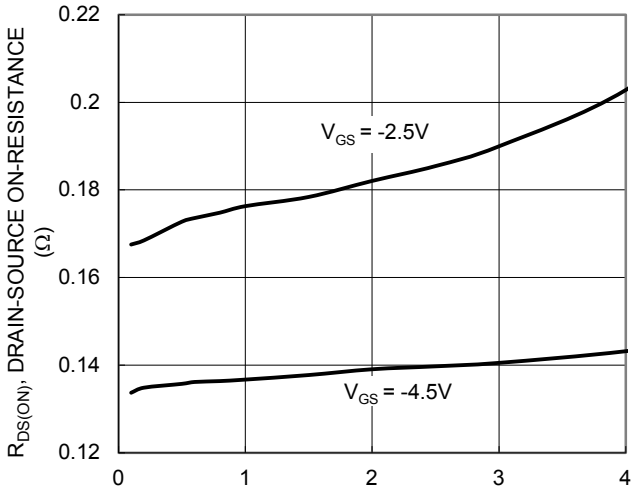


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

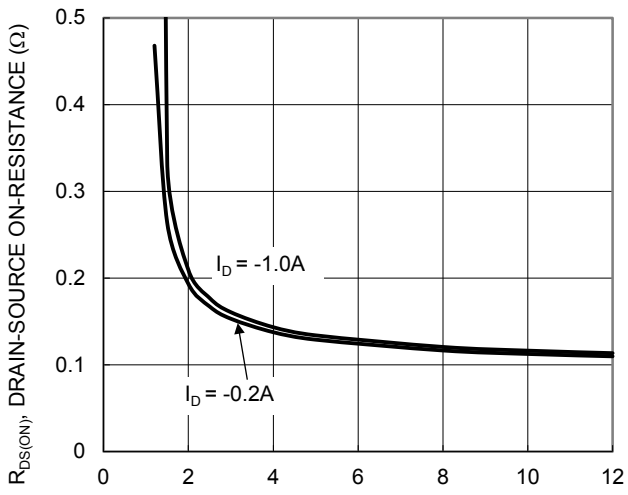


Figure 4. Typical Transfer Characteristic

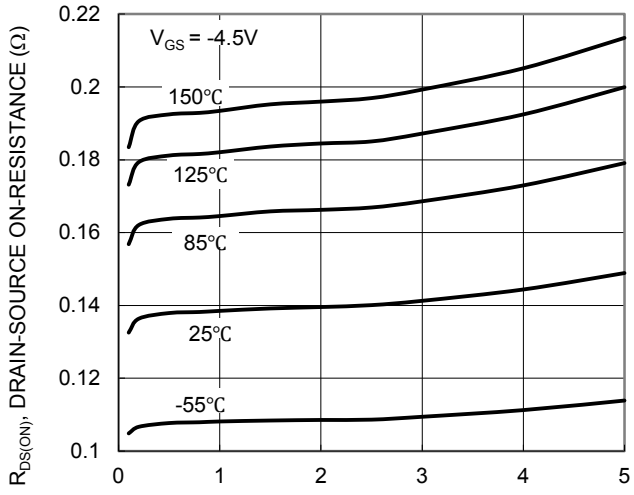


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

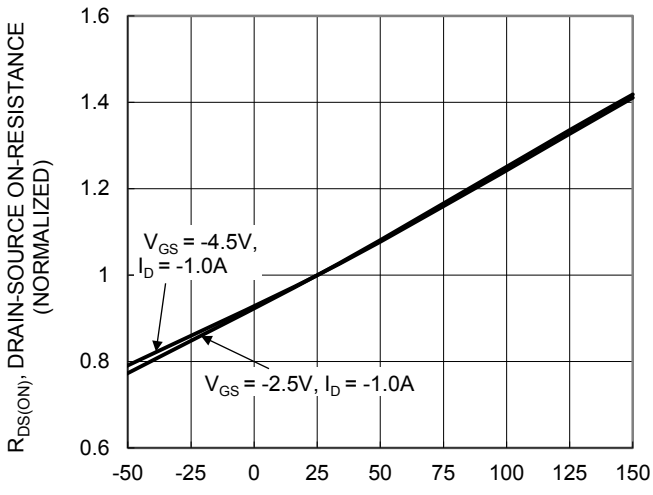


Figure 6. On-Resistance Variation with Junction Temperature

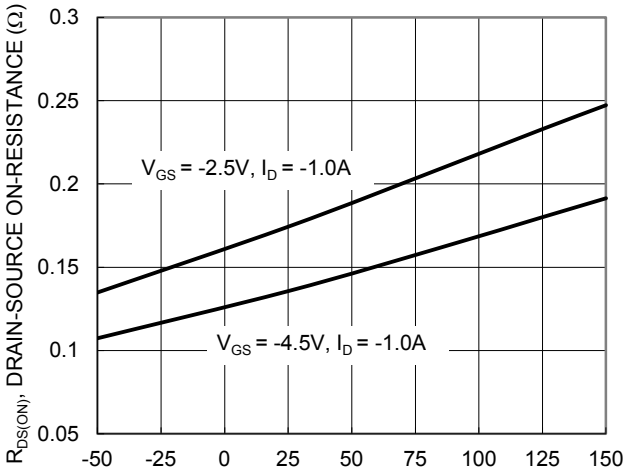


Figure 7. On-Resistance Variation with Junction Temperature

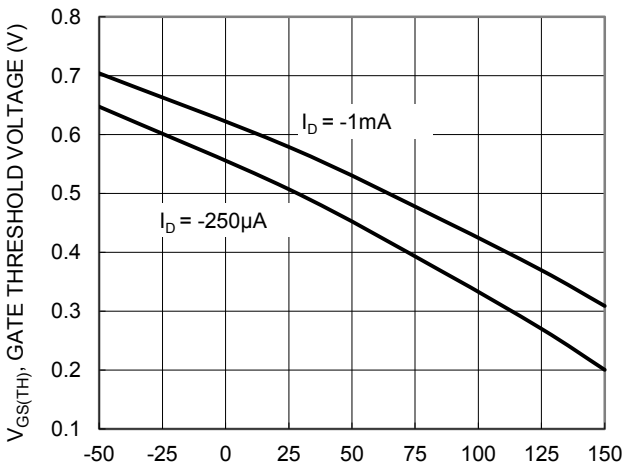


Figure 8. Gate Threshold Variation vs. Junction Temperature

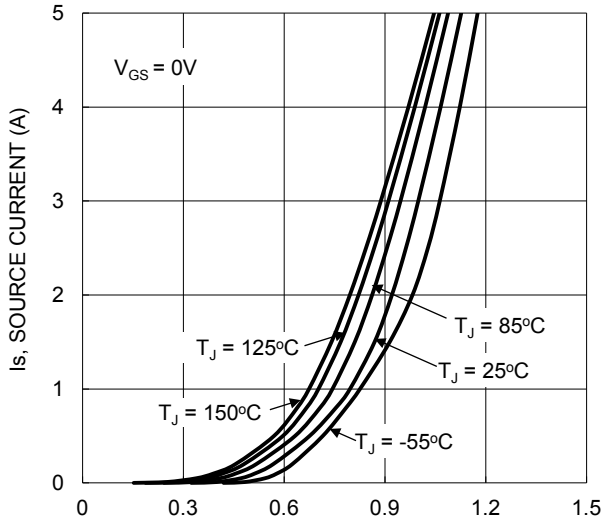


Figure 9. Diode Forward Voltage vs. Current

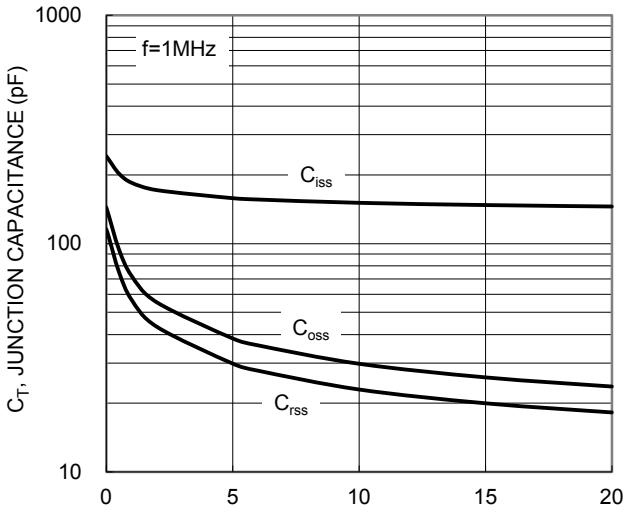


Figure 10. Typical Junction Capacitance

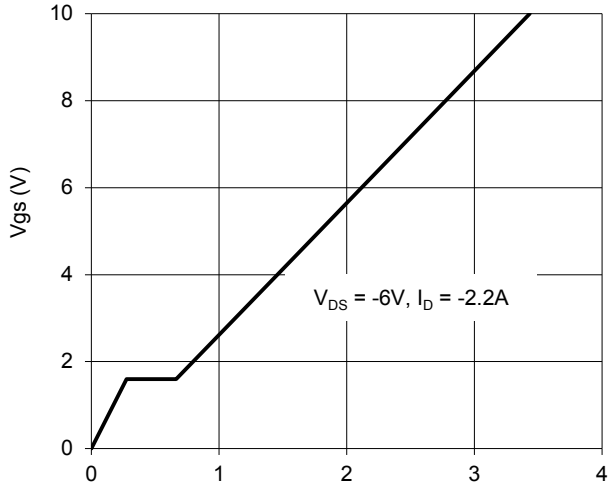


Figure 11. Gate Charge

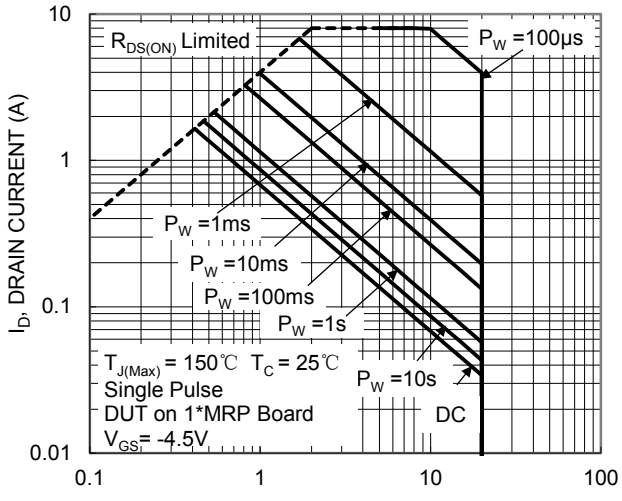


Figure 12. SOA, Safe Operation Area

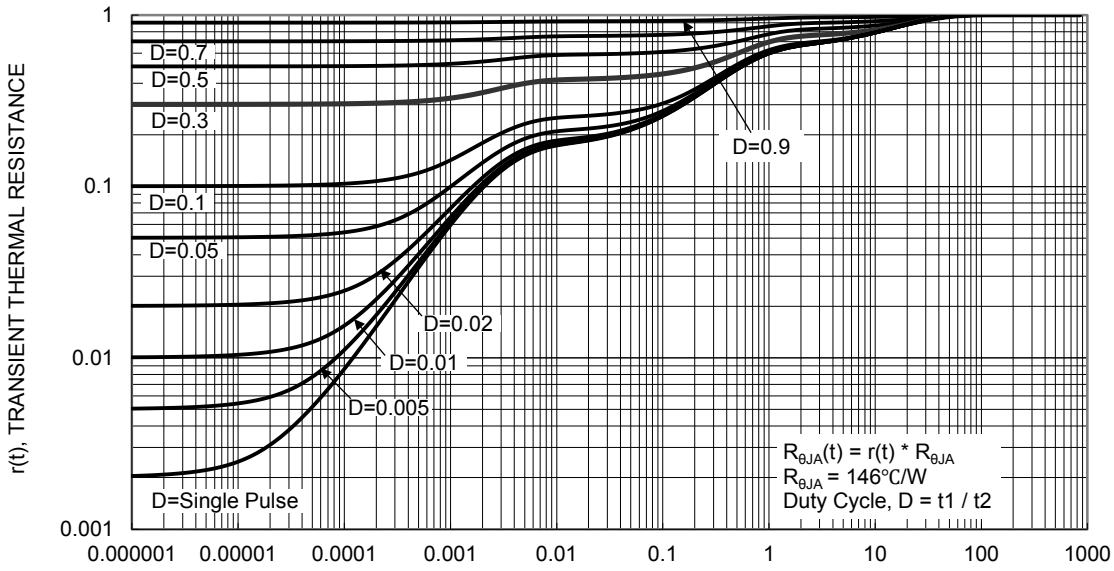
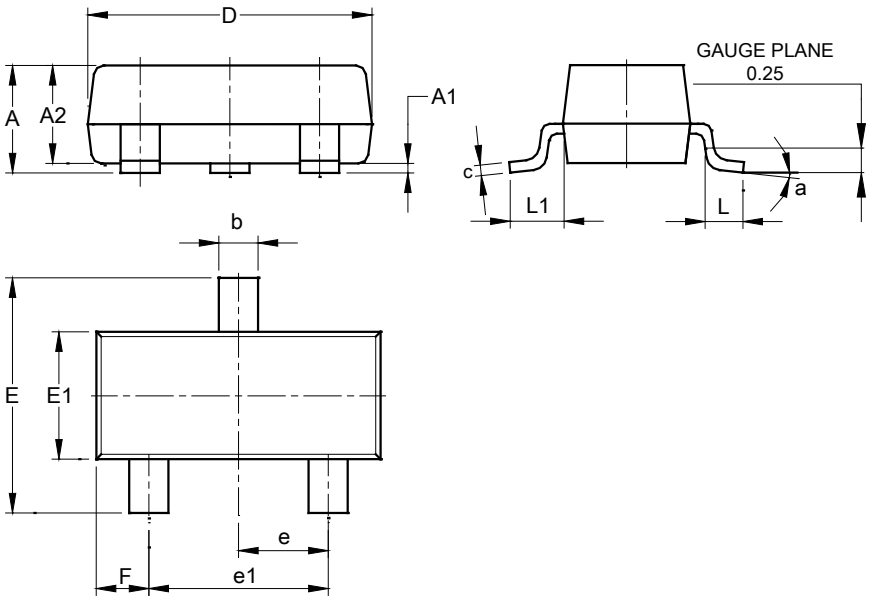


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)

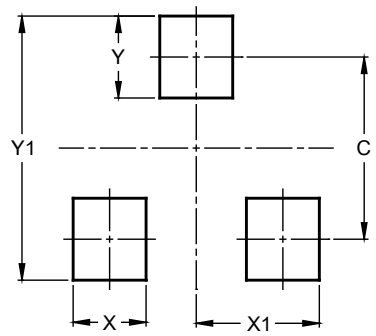


SOT23 (Standard)			
Dim	**Min**	**Max**	**Typ**
A	0.90	1.15	1.025
A1	0.00	0.10	0.05
A2	0.85	1.10	0.975
b	0.30	0.51	0.40
c	0.080	0.202	0.11
D	2.80	3.00	2.90
E	2.25	2.55	2.40
E1	1.20	1.40	1.30
e	0.89	1.03	0.915
e1	1.78	2.05	1.83
F	0.40	0.60	0.535
L1	0.45	0.61	0.55
L	0.25	0.55	0.40
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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