

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value_Q1	Value_Q2	Units
Drain-Source Voltage			V _{DSS}	30	-30	V
Gate-Source Voltage			V _{GSS}	±20	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C	I _D	0.65	-0.45	A
		T _A = +70°C		0.50	-0.36	
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	0.4	-0.35	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	4	-3	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P _D	0.31	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	406	°C/W
Total Power Dissipation (Note 6)		P _D	0.39	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	319	°C/W
Thermal Resistance, Junction to Case		R _{θJC}	126	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics – N Channel – Q1 (@T_A = +25°C, unless otherwise specified.)

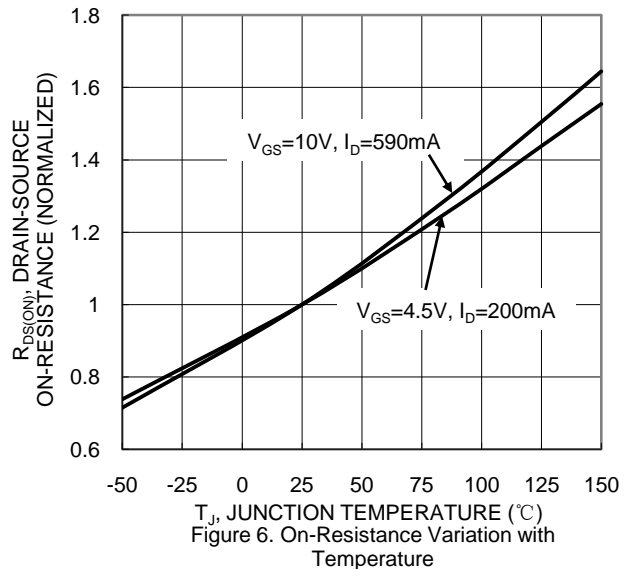
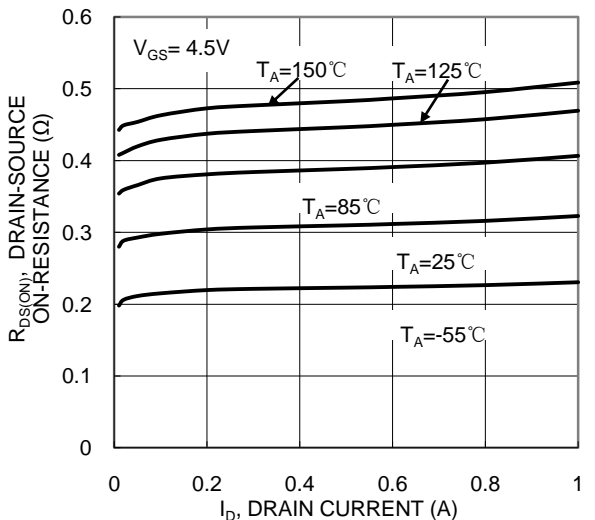
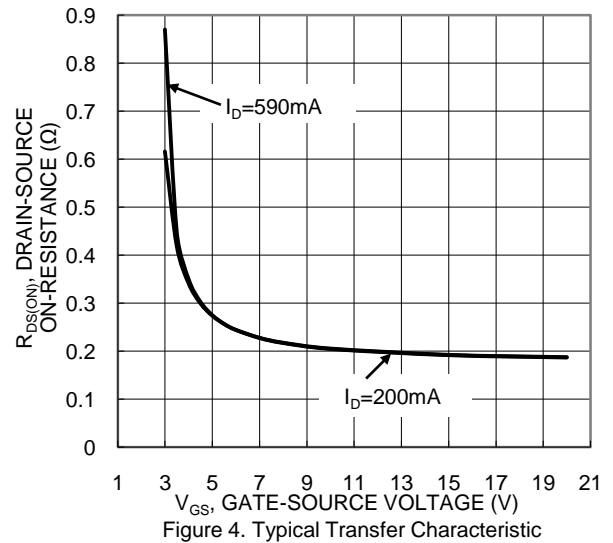
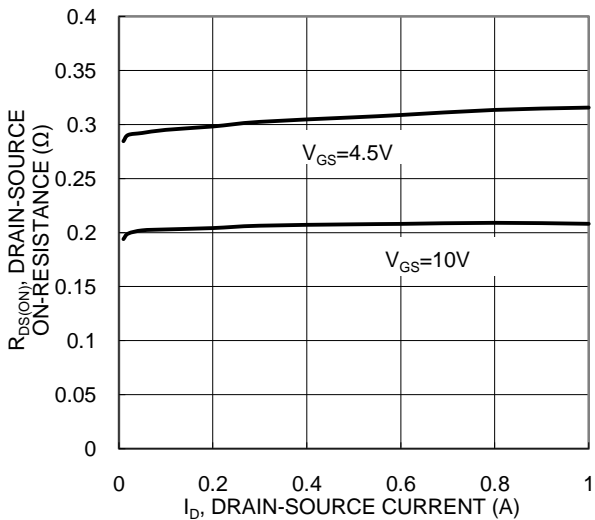
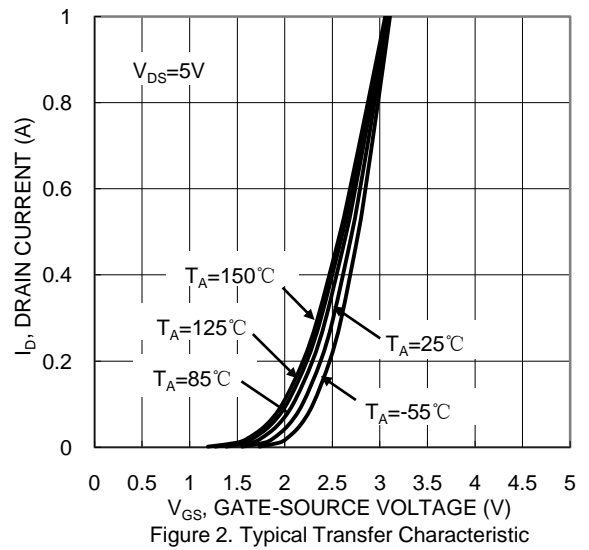
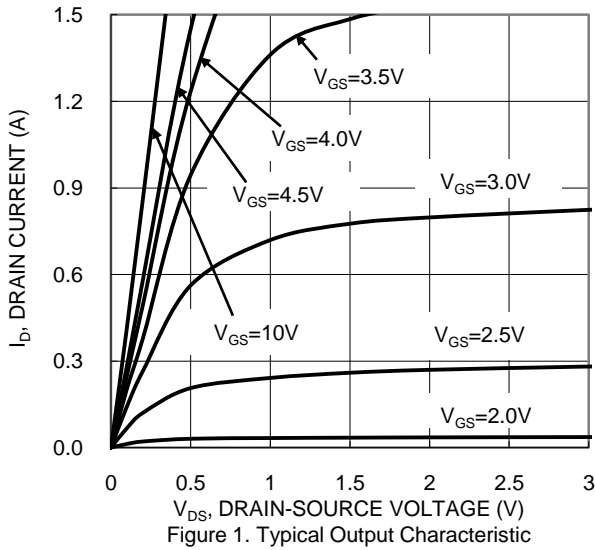
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	µA	V _{DS} = 24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	µA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.8	-	1.6	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.2	0.4	Ω	V _{GS} = 10V, I _D = 0.59A
		-	0.3	0.7		V _{GS} = 4.5V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	-	0.8	1.2	V	V _{GS} = 0V, I _S = 0.23A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	55	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	8.5	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	6.5	-	pF	
Gate Resistance	R _g	-	92	-	Ω	V _{DS} = V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	-	0.6	-	nC	V _{DS} = 10V, I _D = 250mA
Total Gate Charge (V _{GS} = 10V)	Q _g	-	1.4	-	nC	
Gate-Source Charge	Q _{gs}	-	0.2	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.1	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	3.8	-	ns	V _{GS} = 10V, V _{DS} = 30V, I _D = 100mA, R _G = 1Ω
Turn-On Rise Time	t _R	-	3.5	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	25.2	-	ns	
Turn-Off Fall Time	t _F	-	18.8	-	ns	

Electrical Characteristics – P Channel – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	-1	μA	V _{DS} = -24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1	-	-2.6	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.36	0.9	Ω	V _{GS} = -10V, I _D = -0.42A
		-	0.57	1.7		V _{GS} = -4.5V, I _D = -0.2A
Diode Forward Voltage	V _{SD}	-	-0.8	-1.2	V	V _{GS} = 0V, I _S = -0.23A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	54	-	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	10	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	8.3	-	pF	
Gate Resistance	R _g	-	240	-	Ω	V _{DS} = V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	-	0.6	-	nC	V _{DS} = -10V, I _D = -0.24A
Total Gate Charge (V _{GS} = -10V)	Q _g	-	1.3	-	nC	
Gate-Source Charge	Q _{gs}	-	0.2	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.2	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	5.7	-	ns	V _{GS} = -10V, V _{DD} = -15V, I _D = -0.5A, R _G = 1Ω
Turn-On Rise Time	t _R	-	8.8	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	35	-	ns	
Turn-Off Fall Time	t _F	-	19	-	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

Typical Characteristics - N-CHANNEL



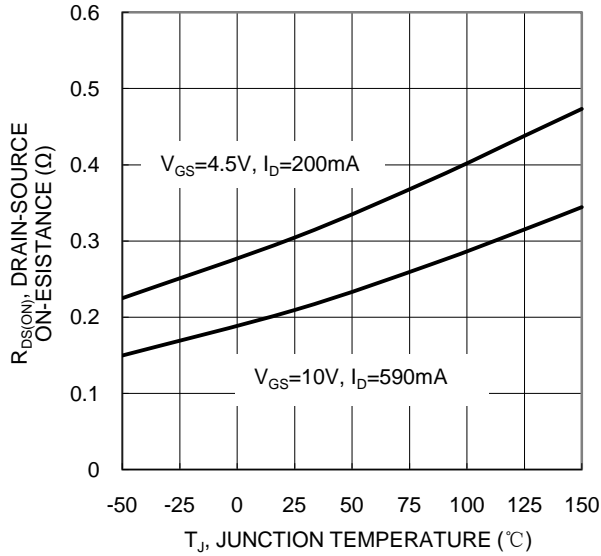


Figure 7. On-Resistance Variation with Temperature

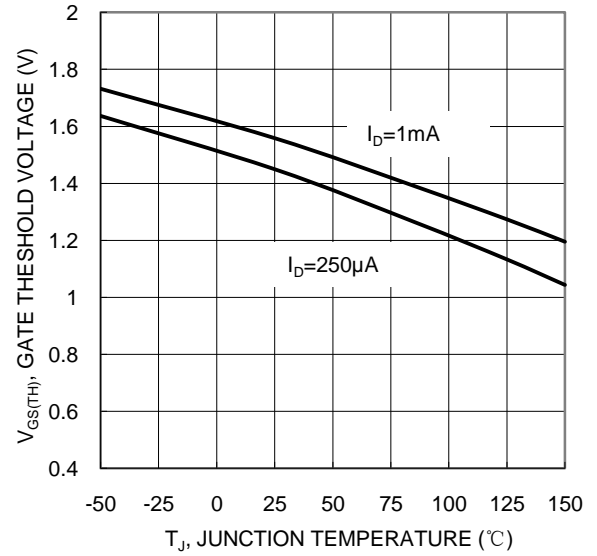


Figure 8. Gate Threshold Variation vs. Junction Temperature

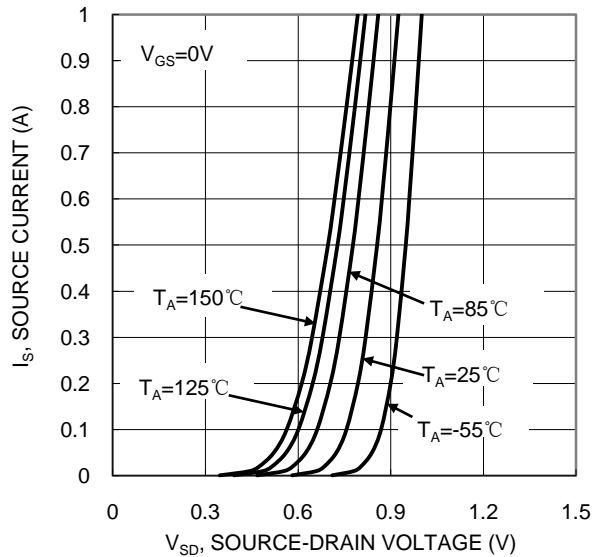


Figure 9. Diode Forward Voltage vs. Current

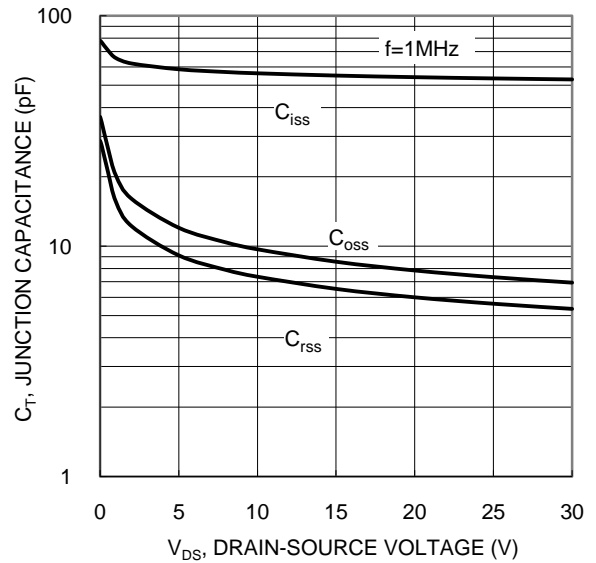


Figure 10. Typical Junction Capacitance

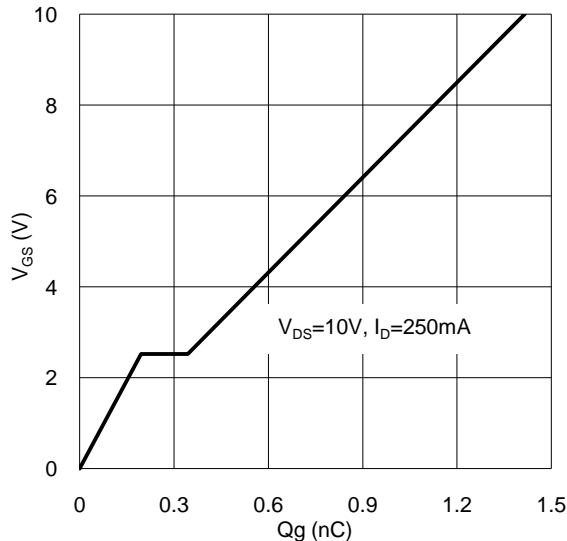


Figure 11. Gate Charge

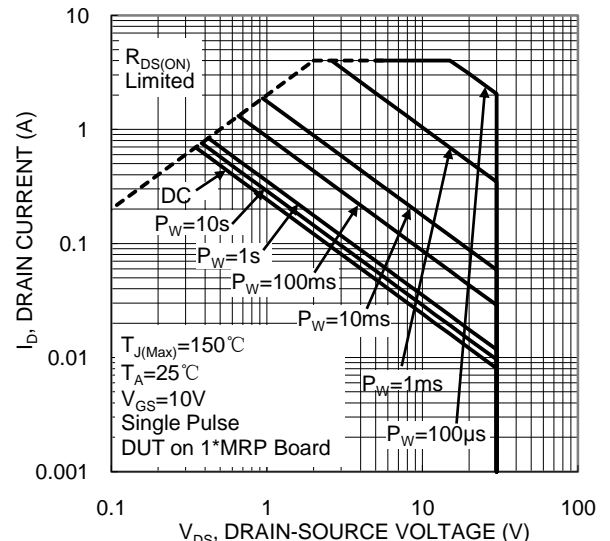
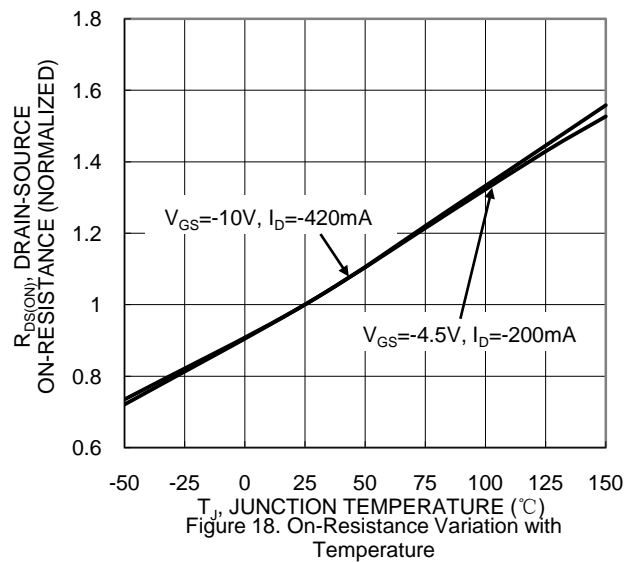
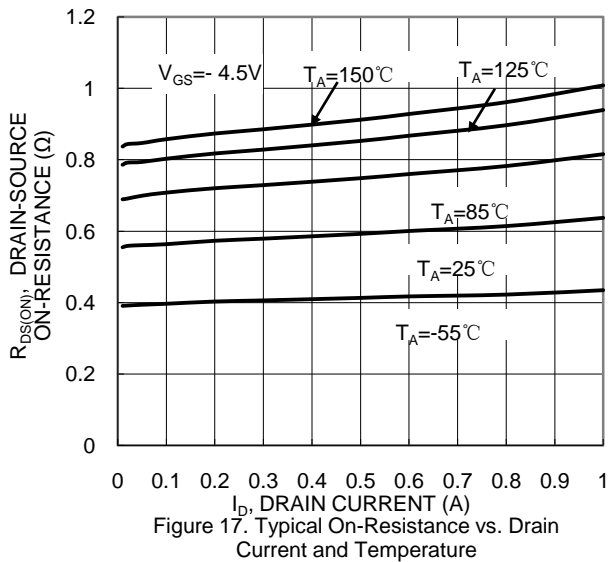
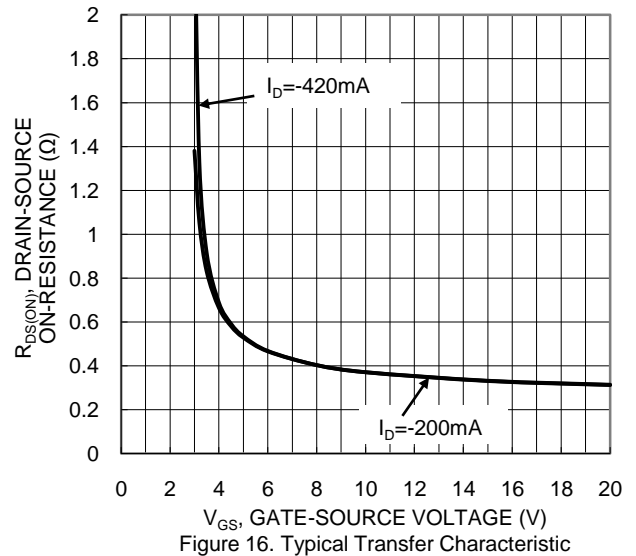
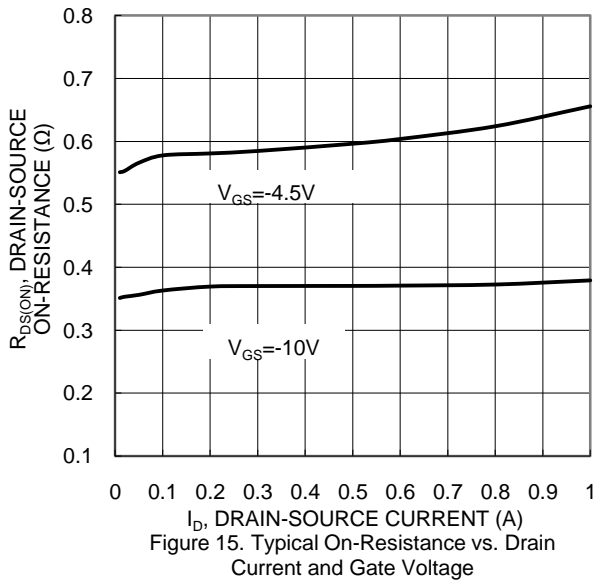
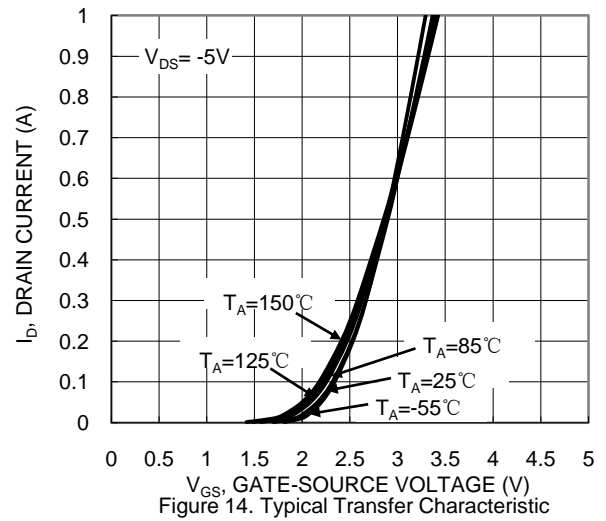
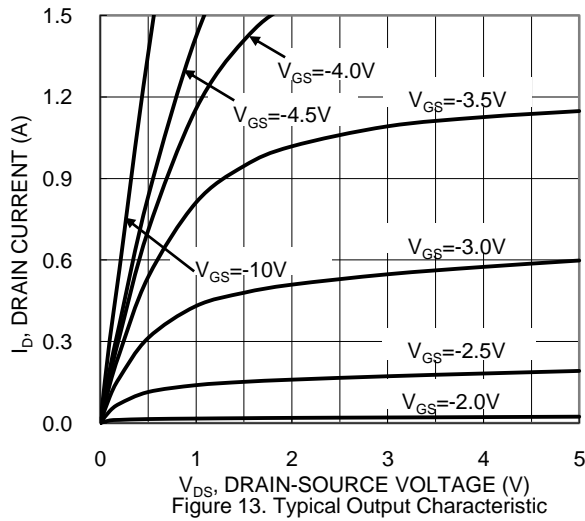


Figure 12. SOA, Safe Operation Area

Typical Characteristics - P-CHANNEL



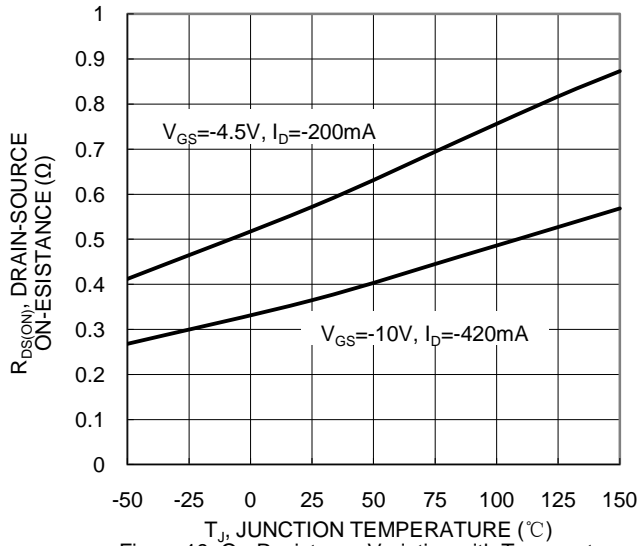


Figure 19. On-Resistance Variation with Temperature

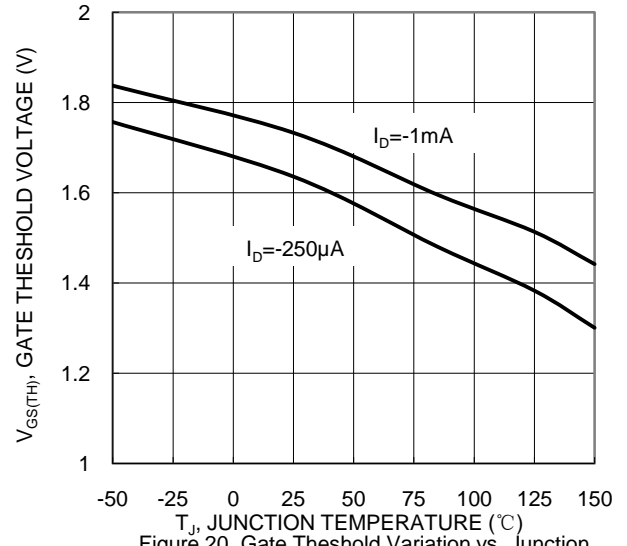


Figure 20. Gate Threshold Variation vs. Junction Temperature

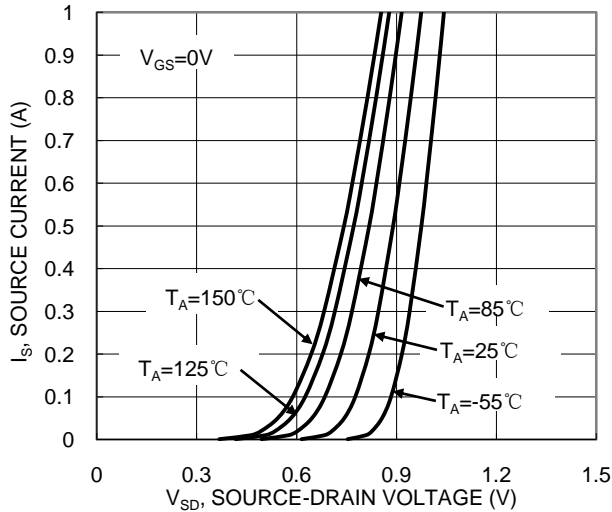


Figure 21. Diode Forward Voltage vs. Current

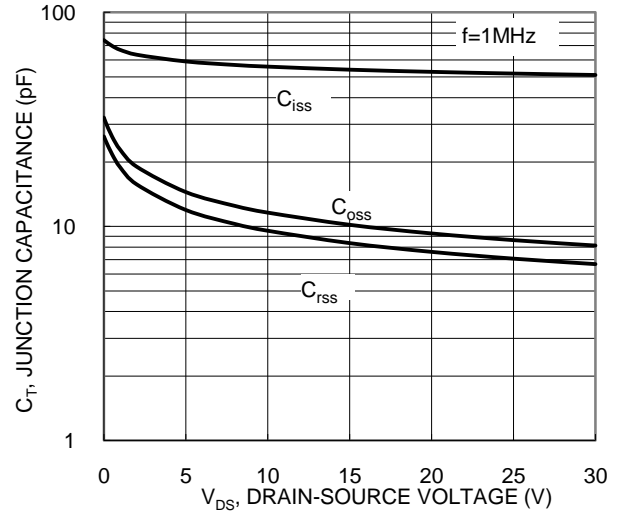


Figure 22. Typical Junction Capacitance

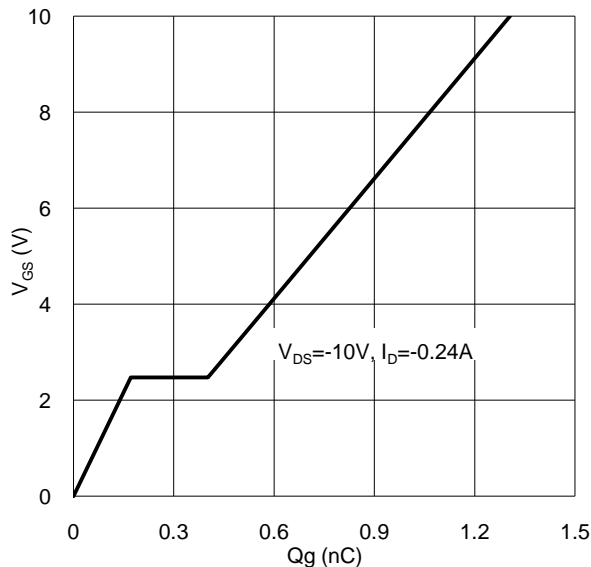


Figure 23. Gate Charge

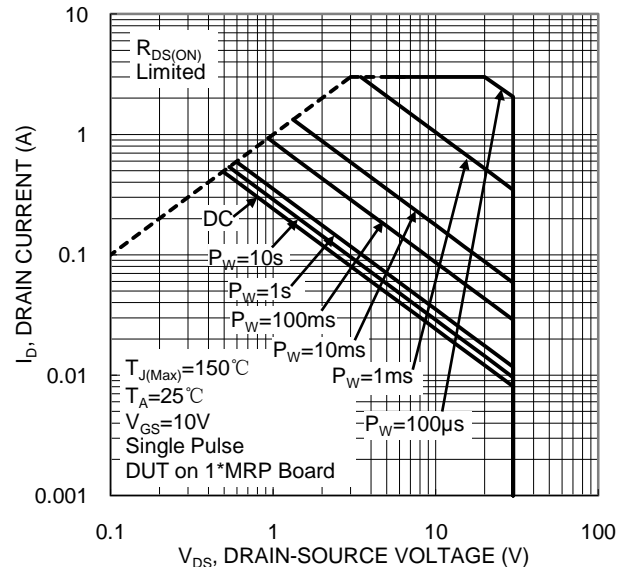


Figure 24. SOA, Safe Operation Area

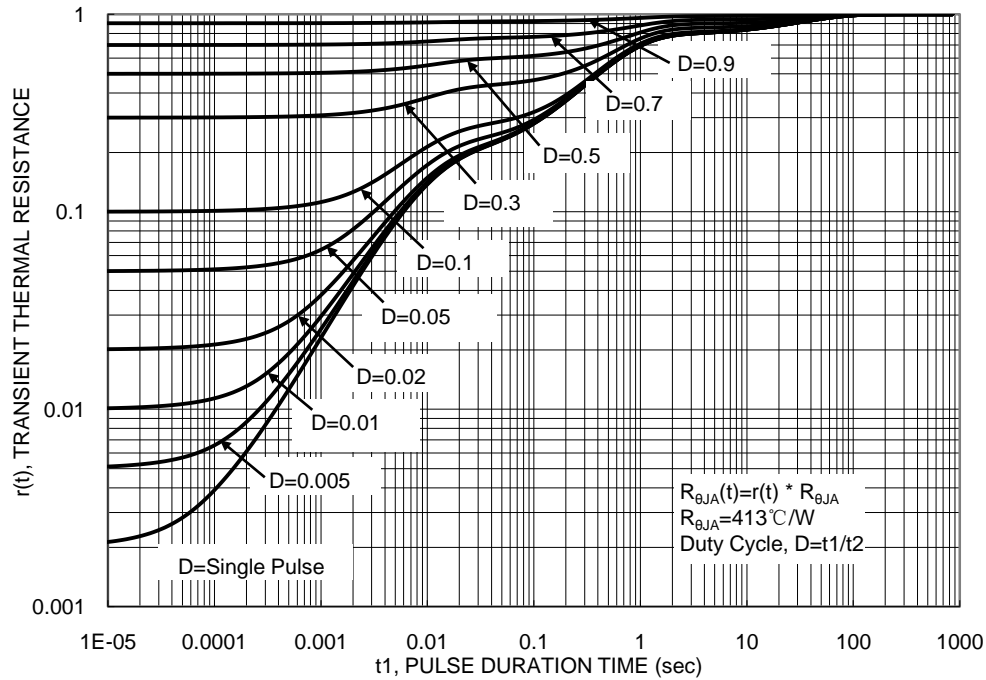
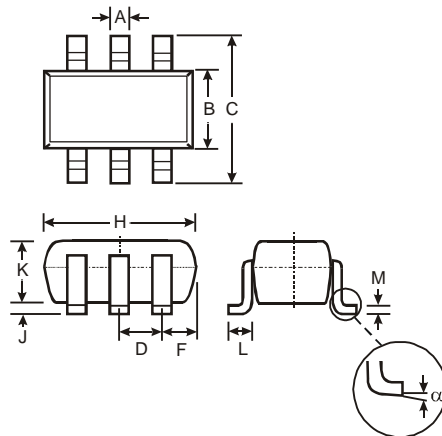


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

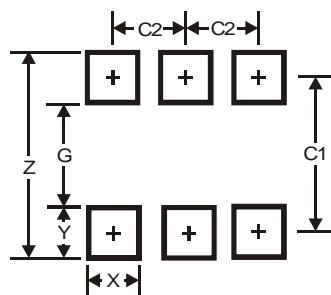
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> the for latest version.



SOT363			
Dim	Min	Max	Typ
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> the for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

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