

# **Maximum Ratings** $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Forward Current	I <sub>F(AV)</sub>	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	40	А

### **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{\theta JA}$	73	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 7)	R <sub>0JS</sub>		13	°C/W
Operating Temperature Range	TJ	-65 to +125		°C
Storage Temperature Range	T <sub>STG</sub>	-65 to +150		°C

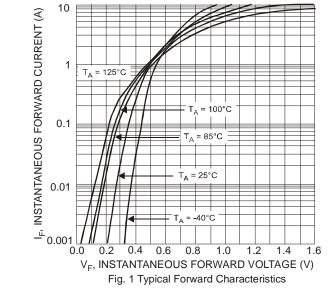
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

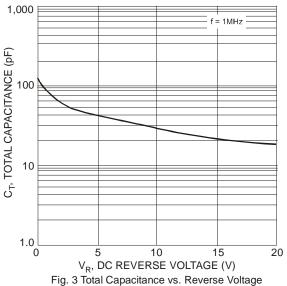
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	40		_	V	$I_R = 20\mu A$
Forward Voltage	V <sub>F</sub>	_	0.52	0.58	· · · · · · · · · · · · · · · · · · ·	I <sub>F</sub> = 1.0A
Polward Vollage		_	0.65	0.7		$I_F = 2.0A$
Leakage Current (Note 8)	I <sub>R</sub>	_	_	20		$V_R = 40V, T_A = +25$ °C
Leakage Current (Note 8)		_		6.0	mA	$V_R = 40V, T_A = +100$ °C
Total Capacitance	Ст	_	28	_	pF	$V_R = 10V, f = 1.0MHz$

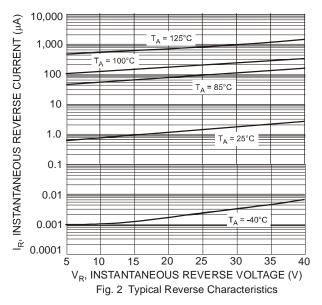
Notes:

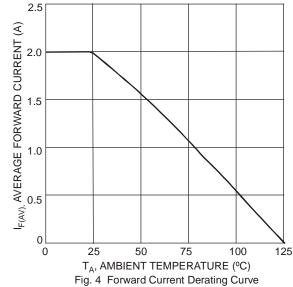
- 6. Part mounted on Polymide board with 2 oz., copper,  $74\text{mm}^2$  pad layout.  $T_A = +25^{\circ}\text{C}$
- $7. \ \ Theoretical \ Re \ \ \ \ calculated \ from \ the \ top \ center \ of \ the \ die \ straight \ down \ to \ the \ PCB/cathode \ tab \ solder \ junction.$
- 8. Short duration pulse test used to minimize self-heating effect.









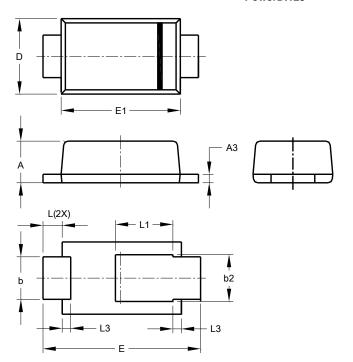




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI123

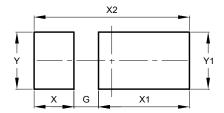


PowerDI123					
Dim	Min	Max	Тур		
Α	0.93	1.00	0.98		
A3	0.15	0.25	0.20		
b	0.85	1.25	1.00		
b2	1.025	1.125	1.10		
D	1.63	1.93	1.78		
Е	3.50	3.90	3.70		
E1	2.60	3.00	2.80		
١	0.40	0.50	0.45		
L1	1.25	1.40	1.35		
L3	0.125	0.275	0.20		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI123



Dimensions	Value		
Dillielisions	(in mm)		
G	0.65		
Х	1.05		
X1	2.40		
X2	4.10		
Y	1.50		
Y1	1.50		



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