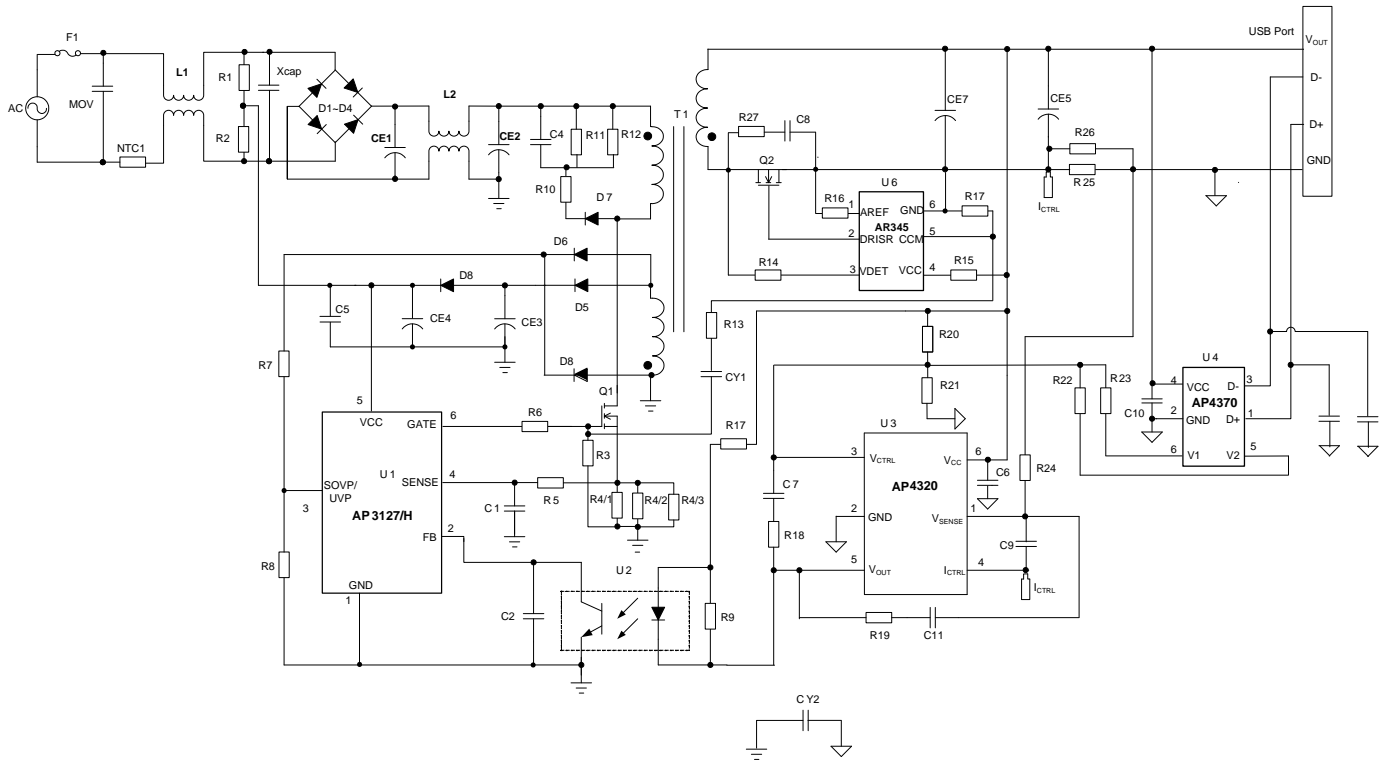


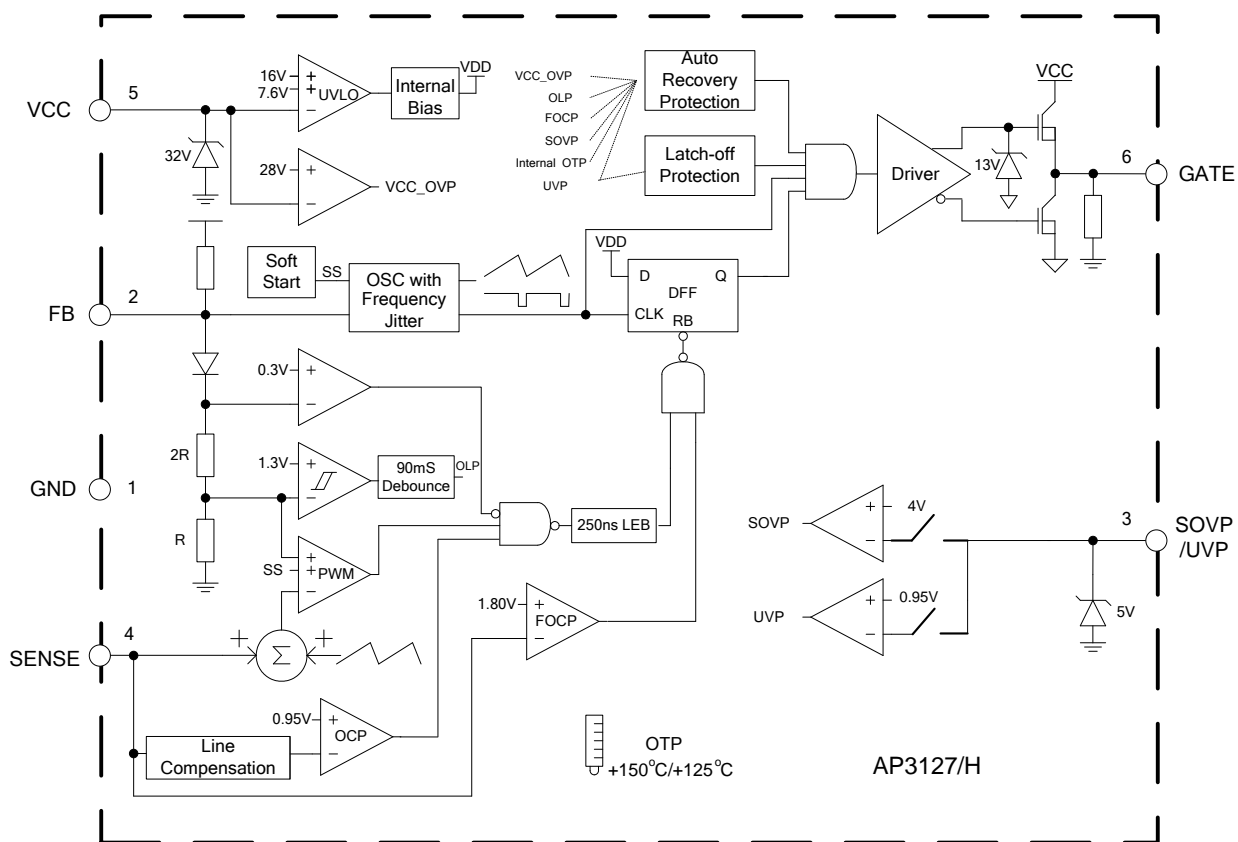
Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	Function
1	GND	Signal ground. Current return for driver and control circuits
2	FB	Feedback. Directly connected to the opto-coupler
3	SOVP/UVF	Sense pin for secondary side OVP and UVP
4	SENSE	Current Sense
5	VCC	Supply voltage of driver and control circuits
6	GATE	Gate driver output

Functional Block Diagram



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating	Unit
V_{CC}	Power Supply Voltage	33	V
I_O	Gate Output Current	350	mA
$V_{FB}, V_{SENSE}, V_{SOVP/UVLP}$	Input Voltage to FB, SENSE, SOVP/UVLP	-0.3 to 7	V
θ_{JA}	Thermal Resistance (Junction to Ambient)	250	°C/W
P_D	Power Dissipation at $T_A < +25^\circ\text{C}$	500	mW
T_J	Operating Junction Temperature	-40 to +150	°C
T_{STG}	Storage Temperature Range	+150	°C
–	ESD (Human Body Model)	3000	V
–	ESD (Machine Model)	300	V

Note 4: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply Voltage	10	28	V

Electrical Characteristics (@T_A = +25°C, V_{CC} = 16V, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply Voltage (VCC Pin)						
I _{STARTUP}	Startup Current	–	–	1	5	μA
I _{CC}	Operating Supply Current	V _{FB} =0V, C _L =1nF	0.5	0.7	1	mA
		V _{FB} =3V, C _L =0nF	0.6	1.2	2.0	
–	UVLO (on)	–	14.5	15.8	16.5	V
–	V _{CC} Maintain	–	8.1	8.6	9.1	V
–	UVLO (off)	–	7.1	7.6	8	V
–	V _{CC} OVP	–	29	30	31	V
PWM Section/Oscillator Section						
–	Maximum Duty Cycle	–	70	75	80	%
–	Oscillation Frequency	AP3127,Central Frequency	60	65	70	kHz
–	Oscillation Frequency	AP3127H,Central Frequency	93	100	107	kHz
–	Green Mode Frequency	–	20	–	30	kHz
–	Frequency Temperature Stability	-20°C to +125°C (Note 5)	–	–	5	%
–	Frequency Voltage Stability	V _{CC} =12V to 30V	–	–	3	%
–	Frequency Dithering	–	±4	±6	±8	%
Current Sense Section (SENSE Pin)						
V _{CS}	Maximum SENSE Voltage	V _{FB} =4.5V	0.9	0.95	0.98	V
–	FOCP Voltage	–	1.5	1.7	1.9	V
–	LEB Time of SENSE	–	150	250	350	ns
–	Delay to Output (Note 5)	–	–	100	–	ns
–	Soft-start Time	–	3	5	8	ms
Feedback Input Section (FB Pin)						
–	The Ratio of Input Voltage to Current Sense Voltage	–	2.5	3	3.5	V/V
–	Input Impedance	–	12	15	18	kΩ
–	Source Current	V _{FB} =0V	-0.2	-0.27	-0.34	mA
–	Green Mode Threshold	–	–	2.3	–	V
–	Input Voltage for Zero Duty	–	1.3	1.55	1.8	V
Output Section (GATE Pin)						
–	Output Low Level	I _O =20mA, V _{CC} =12V	–	–	1	V
–	Output High Level	I _O =20mA, V _{CC} =12V	8	–	–	V
–	Output Clamping Voltage	–	11	13	15	V
–	Rising Time	C _L =1nF, V _{CC} =13V	–	220	320	ns
–	Falling Time	C _L =1nF, V _{CC} =13V	–	50	100	ns

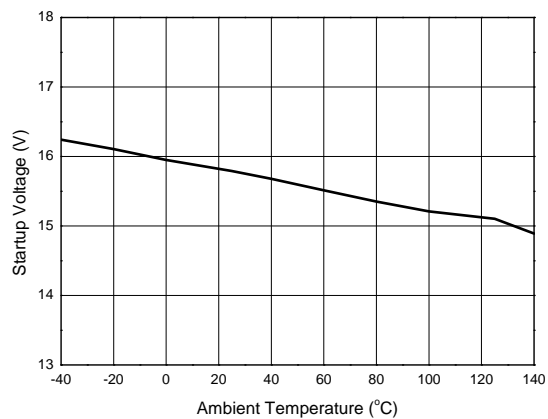
Electrical Characteristics (Cont.) (@T_A = +25°C, V_{CC} = 16V, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Protection Section (SOVP/UVF Pin)						
–	OVP Reference Voltage	–	3.8	4	4.2	V
–	UVF Reference Voltage	–	0.925	0.95	0.975	V
Delay Time Section						
–	Delay of Short Circuit Protection	–	70	90	110	ms
–	Delay of Hiccup Protection (Note 5)	VCC OVP	–	6	–	Cycles
Internal OTP Section						
–	OTP Enter (Note 5)	–	–	+150	–	°C
–	OTP Exit (Note 5)	–	–	+125	–	°C

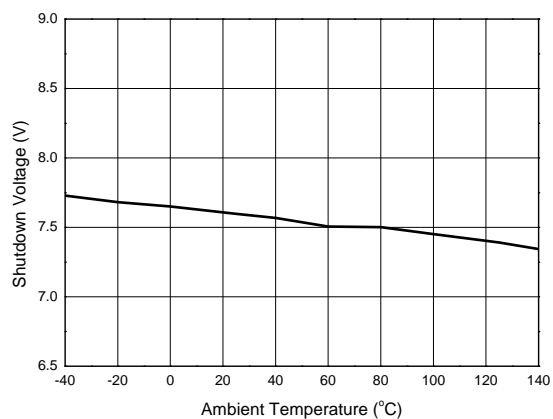
Note 5: Guaranteed by design.

Performance Characteristics

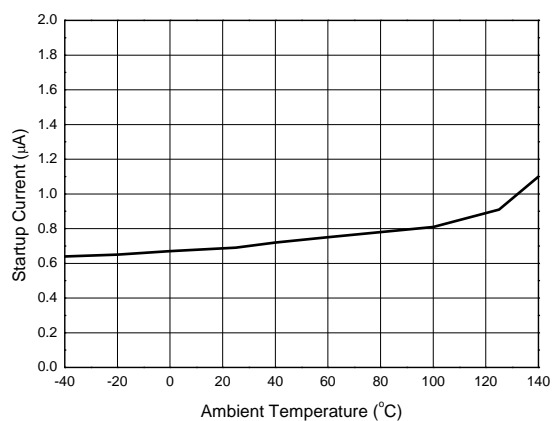
Startup Voltage vs. Ambient Temperature



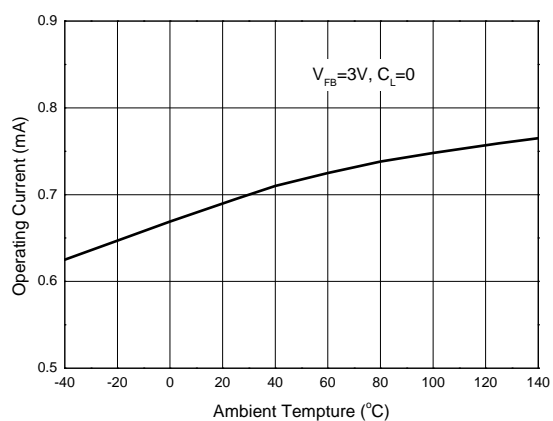
Shutdown Voltage vs. Ambient Temperature



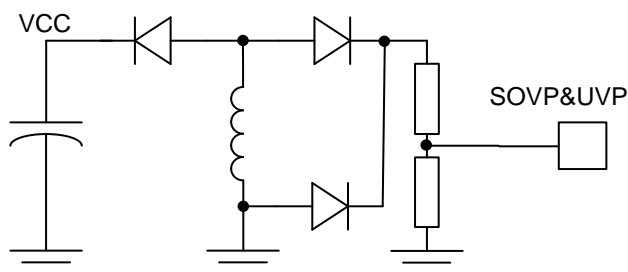
Startup Current vs. Ambient Temperature



Operating Current vs. Ambient Temperature



PIN3 Utilization for SOVP/UDP



SOVP and UDP

Operation Description

The AP3127/H is specifically designed for off-line AC-DC power supply used in LCD monitor, notebook adapter and battery charger applications. It offers a cost effective solution with a versatile protection function.

Start-up Current and UVLO

The start-up current of AP3127/H is optimized to realize ultra low current (1μA typical) so that VCC capacitor can be charged more quickly. The direct benefit of low start-up current is the availability of using large start-up resistor, which minimizes the resistor power loss for high voltage AC input.

An UVLO comparator is included in AP3127/H to detect the voltage on VCC pin. It ensures that AP3127/H can draw adequate energy from hold-up capacitor during power-on. The turn-on threshold is 16V and the turn-off threshold is 7.6V.

Current Sense Comparator and PWM Latch

The AP3127/H operates as a current mode controller, the output switch conduction is initiated by every oscillator cycle and is terminated when the peak inductor current reaches the threshold level established by the FB pin. The inductor current signal is converted to a voltage signal by inserting a reference sense resistor R_S . The inductor current under normal operating conditions is controlled by the voltage at FB pin. The relation between peak inductor current (I_{PK}) and V_{FB} is:

$$I_{PK} = (V_{FB} - 0.8) / 3R_S$$

Moreover, FOCV with 1.8V threshold is only about 100ns delay, which can avoid some catastrophic damages such as secondary rectifier short test. Few drive cycles can alleviate the destruction range and get better protection.

Leading-edge Blanking

A narrow spike on the leading edge of the current waveform can usually be observed when the power MOSFET is turned on. A 250ns leading-edge blank is built-in to prevent the false-triggering caused by the turn-on spike. During this period, the current limit comparator is disabled and the gate driver can not be switched off.

At the time of turning off the MOSFET, a negative undershoot (maybe larger than -0.3V) can occur on the SENSE pin. So it is strongly recommended to add a small RC filter or at least connect a resistor "R" on this pin to protect the IC (Shown as Figure 1).

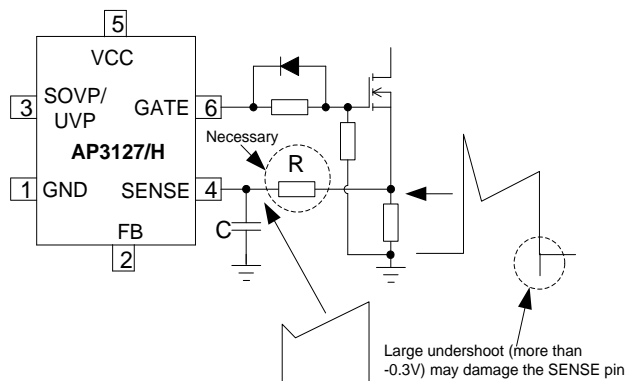


Figure 1

Built-in Slope Compensation

It is well known that a continuous current mode SMPS may become unstable when the duty cycle exceeds 50%. The built-in slope compensation can improve the stability, so there is no need for design engineer to spend much time on that.

FB Pin and Short Circuit Protection

This pin is normally connected to the opto-coupler and always paralleled with a capacitor for loop compensation. When the voltage at this pin is greater than 4.5V and lasts for about 90ms, the IC will enter the protection mode. For AP3127/H, the system will enter hiccup mode to wait the V_{CC} decreasing to low UVLO level, then the IC will try to restart until the failure removed. And when this voltage is less than 1.55V, the IC will stop the drive pulse immediately. Therefore, this feature can be used for short circuit protection, which makes the system immune from damage. Normally, output short makes the V_{FB} value to the maximum because the opto-coupler is cut off.

Operation Description (Cont.)

V_{CC} Maintain Mode

During light load or step load, V_{FB} will drop and be lower than 1.55V, thus the PWM drive signal will be stopped, and there is no more new energy transferred due to no switching. Therefore, the IC supply voltage may reduce to the shutdown threshold voltage and system may enter the unexpected restart mode. To avoid this, the AP3127/H holds a so-called V_{CC} maintain mode which can supply energy to V_{CC}.

When V_{CC} decreases to a setting threshold, the V_{CC} maintain comparator will output some drive signal to make the system switch and provide a proper energy to V_{CC} pin. The V_{CC} maintain function will cooperate with the PWM and burst mode loop which can make the output voltage variation be within the regulation. This mode is very useful for reducing startup resistor loss and achieving a better standby performance with a low value V_{CC} capacitor. The V_{CC} is not easy to touch the shutdown threshold during the startup process and step load. This will also simplify the system design. The minimum V_{CC} voltage is suggested to be designed a little higher than V_{CC} maintain threshold thus can achieve the best balance between the standby and step load performance.

System Protection and Pin Fault Protection

The AP3127/H provides versatile system and pin fault protections. The OCP comparator realizes the cycle-by-cycle current limiting (OCP). In universal input line voltage, the IC realizes the constant over load protection (OLP). V_{CC} over voltage protection can be applied as the primary OVP or opto-coupler broken protection. The AP3127/H also has pin fault connection protection including floating and short connection. The floating pin protection includes the SENSE, FB, etc. The short pin protection includes the SOVP/UVP pin short protection. When these pins are floated or SOVP/UVP pin is shorted to ground, PWM switching will be disabled, thus protecting the power system.

SOVP/UVP Protection Function

For some applications, the system requires the output over voltage and under voltage protection function. The SOVP/UVP pin compares the divided voltage from the V_{CC} winding with the inner threshold, when the voltage between R1 and R2 (as in Figure 2) is higher than 4V or lower than 0.95V in switch turning off duration, AP3127/H will trigger SOVP or UVP function and the system will enter the Auto-recovery protection mode. Since the value of V_{CC} winding's waveform reflects the output voltage precisely, the output OVP and UVP can be realized by this function.

D2 in Figure 2 is adopted to clamp the negative signal from V_{CC} winding as a noise immunity solution.

Internal OTP Protection Function

The AP3127/H integrates an internal temperature sensor. It has a trigger window of entering OTP mode at +150°C and exiting at +125°C. The internal OTP protection mode is auto-recovery mode.

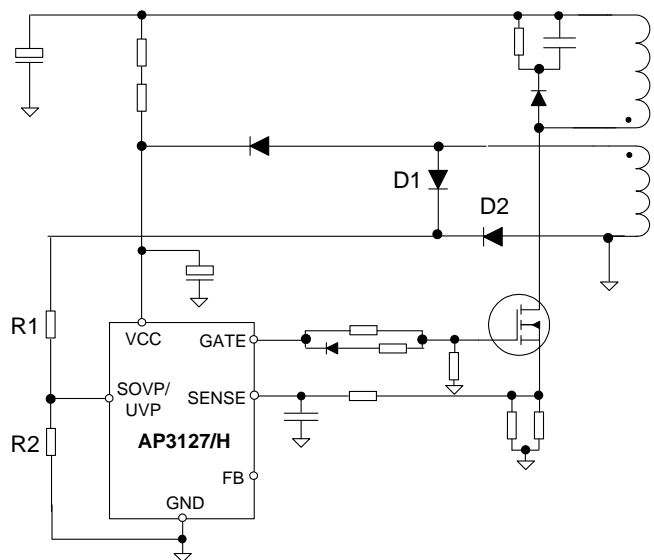
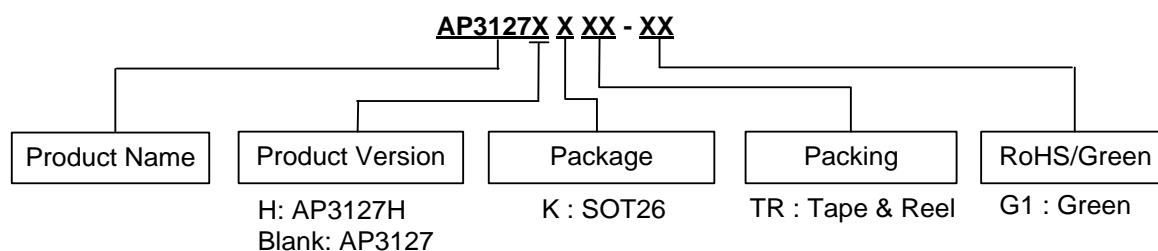


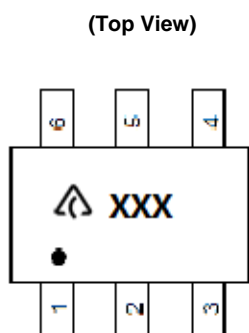
Figure 2


Ordering Information



Package	Part Number	Marking ID	Packing
SOT26	AP3127KTR-G1	GPH	3000/Tape & Reel
	AP3127HKTR-G1	GSH	3000/Tape & Reel

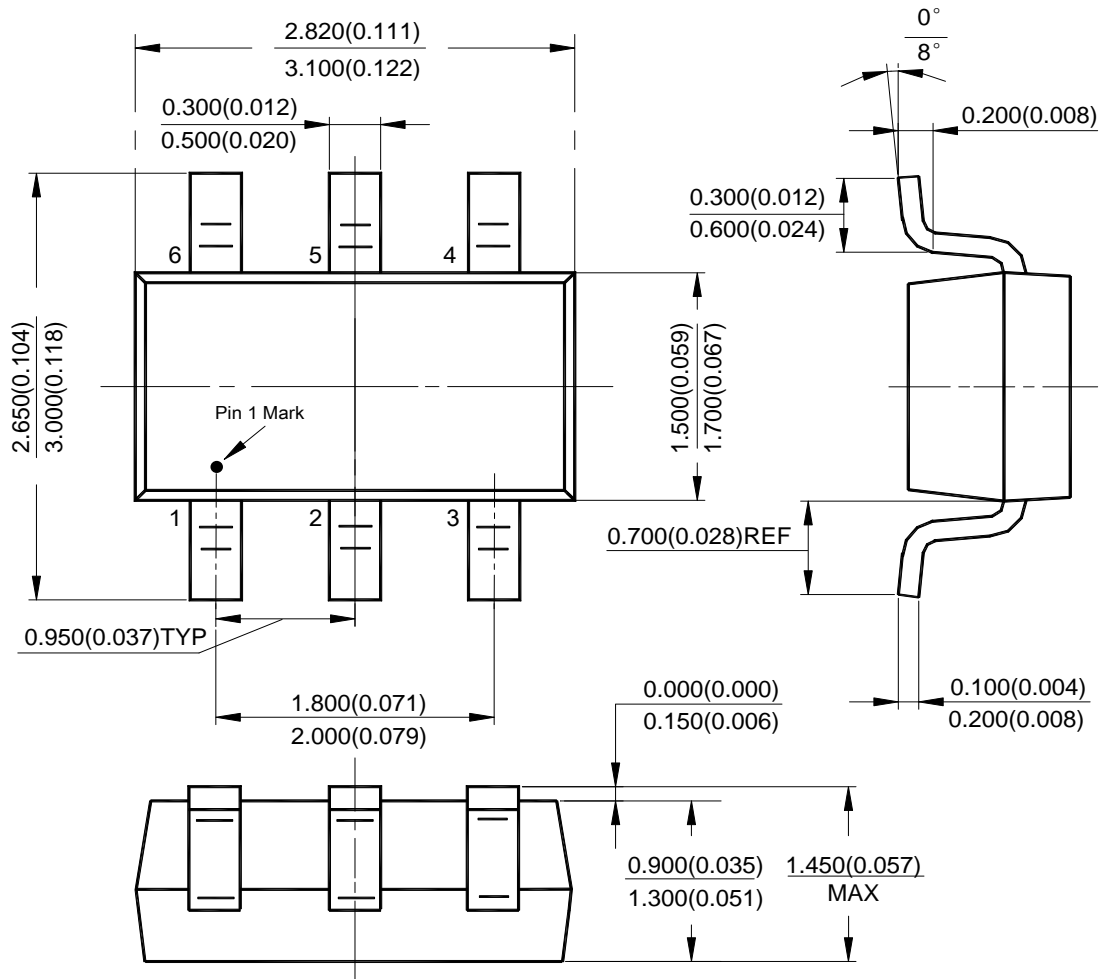
Marking Information



 : Logo
 XXX: Marking ID (See Ordering Information)

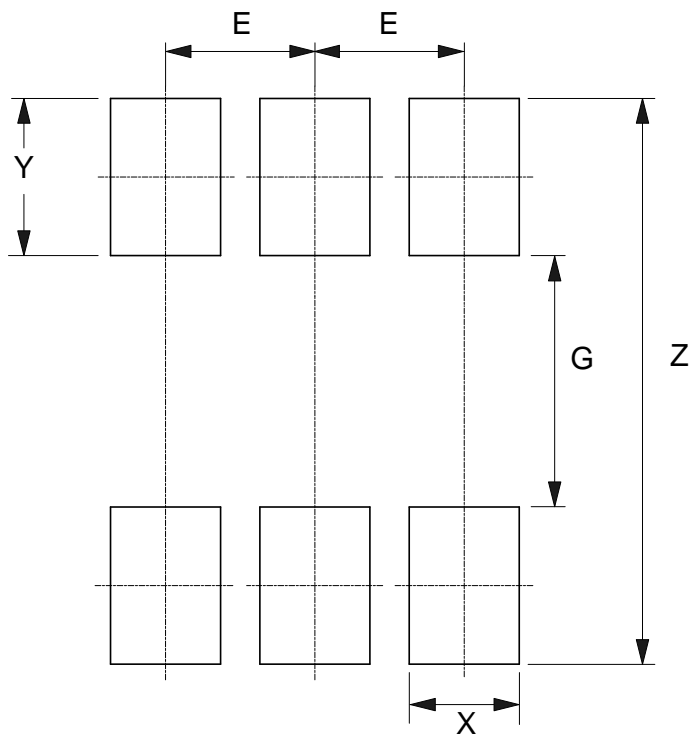
Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: SOT26



Suggested Pad Layout

(1) Package Type: SOT26



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037

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