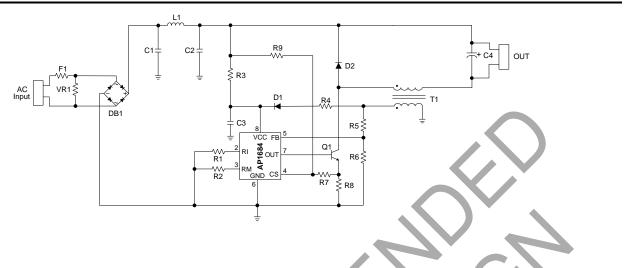


Typical Applications Circuit



Pin Descriptions

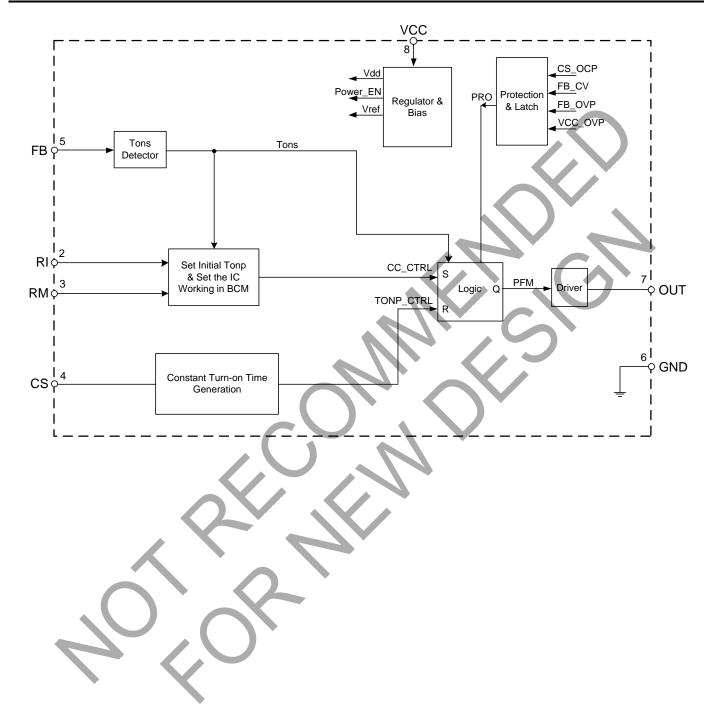
Pin Number	Pin Name	Function
1	NC	No connection
2	RI	The initial Tonp tuning resistor
3	RM	Set the operating mode
4	CS	Primary current sensing
5	FB	The feedback voltage sensing from the auxiliary winding
6	GND	Ground
7	OUT	Gate driver output
8	VCC	Supply voltage of gate driver and control circuits of the IC





AP1684

Functional Block Diagram





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified. Note 4)

Symbol	Parameter	Rating	Unit
Vcc	Power Supply Voltage	-0.3 to 35	V
Ιουτ	Driver Output Current	150	mA
Vcs	Voltage at CS to GND	-0.3 to +7	V
VFB	FB Input Voltage	-40 to +10	V
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C
TLEAD	Lead Temperature (Soldering, 10 sec)	+300	°C
PD	Power Dissipation ($T_A = +50^{\circ}C$)	0.65	w
θја	Thermal Resistance (Junction to Ambient)	160	°C/W
_	ESD (Human Body Model)	2000	V
_	ESD (Charged-device Model)	±1000	V

Note 4: Stresses greater than those listed under Absolute Maximum Ratings can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

Recommended Operating Conditions

5

Symbol	Parameter	Min	Мах	Unit
Vcc	Power Supply Voltage	7	25	V
Та	Ambient Temperature	-40	+105	°C



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

UVLO Section Image: constraint of the section VTH (ST) Start-up Threshold - 18 19 20 VOPR (Min) Minimum Operating Voltage After turn on 5.5 6.5 7.5 VCC_OVP VCC OVP Voltage - 30 32 34 - VCC Delatch Voltage (Note 5) - 3 4 5 Standby Current Section Isr Start-up Current Vcc = VTH (ST)-0.5V, Before start up - 20 1 Isr Start-up Current Static - 900 1300 1 Drive Output Section - Static - 900 1300 1 Iour Output Current (Note 5) VCS_PEAK = 1V - - 60 r Vos UVLO Saturation Voltage Vcc = 0 to Vold-on, Isplik = 10mA - 1.1 - Vcs_REF Current Sense Reference - 1 - 1.1 - Vcs_C_CLAMP Clamp Clamp - 1000 <t< th=""><th></th><th></th><th>0</th><th></th><th>-</th><th></th><th></th></t<>			0		-		
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VFB_OVP FB OVP Threshold 4.5 6 7.5 Output Current 4.5 6 7.5 Output Current - System Output Current on Final Test Board - - +2 Over Temperature Protection Section - - - +2	IFB		VFB = 2V	_		4	μA
Output Current	V _{FB_CV}	FB CV Threshold	-	3.8	4	4.2	V
System Output Current on Final Test Board±2 Over Temperature Protection Section	Vfb_ovp	FB OVP Threshold		4.5	6	7.5	V
Test Board Image: Constraint of the section section	Output Current						
	_			_	_	±2	%
_ Shutdown Temperature (Note 5) _ +150							
		Shutdown Temperature (Note 5)	_	+150	_	_	°C
_ Temperature Hysteresis (Note 5) _ +20 _ +20 _	-	Temperature Hysteresis (Note 5)	 _		+20	_	°C

Note 5: These parameters, although guaranteed by design, are not 100% tested in production.

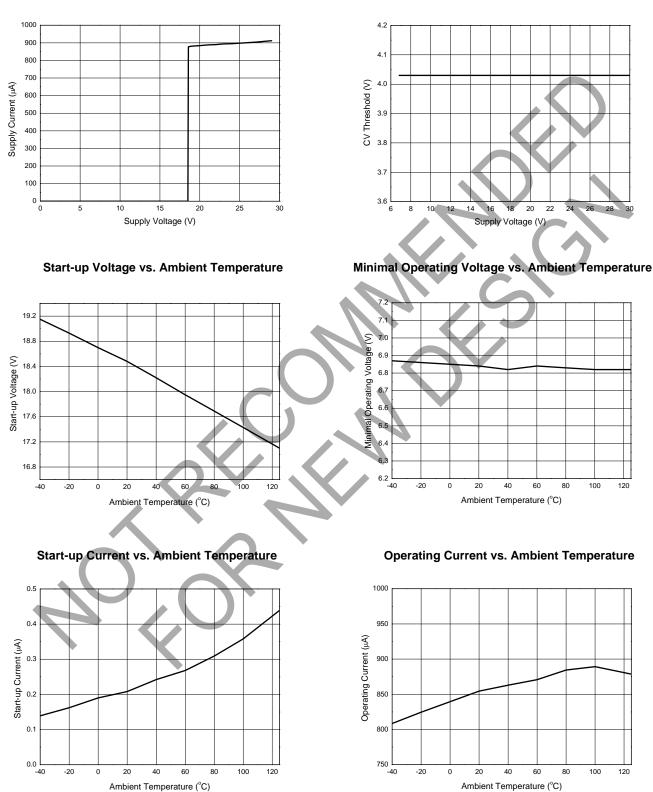
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AP1684

Performance Characteristics

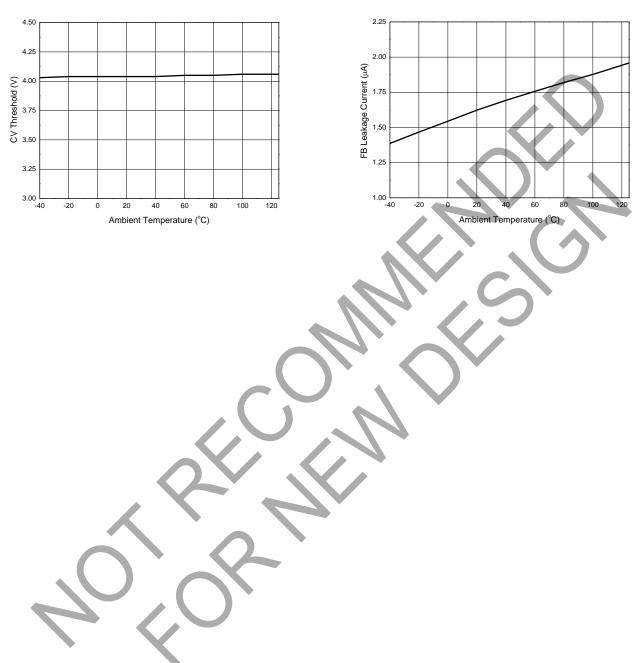
Supply Current vs. Supply Voltage



CV Threshold vs. Supply Voltage



Performance Characteristics (continued)



CV Threshold vs. Ambient Temperature

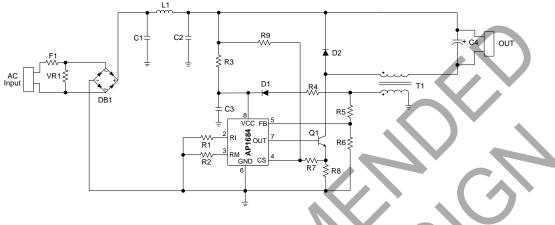
FB Leakage Current vs. Ambient Temperature

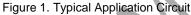


AP1684

Application Information

The AP1684 is designed for single voltage application, and it features high power factor correction (PFC), low total harmonic distortion (THD), low BOM cost and good EMI performance. The device can be widely used in non-dimmable LED application such as GU10, bulb lamps, down lamp, etc. The AP1684 adopts constant on time control method within one AC cycle to achieve the high power factor and low THD. The control scheme is very simple, the power factor correction effectiveness is obvious, and the constant current control is also good enough.





Design Parameters

Setting the Current Sense Resistor R8

As the AP1684 adopts constant on time control method, the current of the inductance will follow the input voltage to get a sinusoidal wave. The current sense pin CS of the AP1684 will sense the peak current of the inductance by sensing the voltage dropped on the current sense resistor R8, and the constant current control is realized by controlling the peak current. In buck structure, when the V_o is higher than V_{in}, no energy will be transferred from input to output which is called dead zone, and considering the dead zone of buck structure, the output current can be calculated as below:

$$I_{o_mean} = k \cdot \frac{1}{\pi} \cdot \frac{V_{cs_ref}}{R8}$$

Where,

Vcs_ref is the reference of the current sense, and the typical value is 1V.

K is the current modification coefficient, and the value of k is approximate to be 0.7.

So, the current sense resistor R8 is determined:

$$R8 = k \cdot \frac{V_{es_ref}}{\pi \cdot I_{o_mean}}$$

Transformer Selection (T1)

The non-isolated buck circuit in Figure 1 is usually selected, and the system is operating at boundary conduction mode. The system's operating frequency does not keep constant, and considering the limit of the BJT's operating frequency, the minimum switching frequency at the crest is set as f_{min}, and then the buck inductance value L can be got:

$$L = \frac{(\sqrt{2} \cdot V_{in_rms} - V_o) \cdot R8 \cdot V_o}{V_{cs_ref} \cdot \sqrt{2} \cdot V_{in_rms} \cdot f_{\min}}$$

Where,

Vo is the output voltage.

Vin_rms is the RMS value of the input voltage.



Application Information (continued)

The next step is determining the transformer's winding turns number, the worst case operation condition of transformer is at the peak voltage area of sine waveform input voltage where the current of across the inductance is the maximum value. The transformer design should be based on the worst case operation condition to guarantee that the transformer is not saturated. According to Ferrari's law of electromagnetic induction, the winding turns number of the buck inductance N_L is:

$$N_{L} = \frac{L \cdot I_{pk}}{A_{e} \cdot B_{m}} = \frac{L \cdot V_{cs_ref}}{A_{e} \cdot B_{m} \cdot R8}$$

Where,

 A_e is the core effective area.

B_m is the maximum magnetic flux density.

The auxiliary winding is power supply for Vcc, the winding turns number Naux is:

$$N_{aux} = N_L \cdot \frac{V_{cc}}{V_o + V_d}$$

Where,

Vcc is the power supply voltage for IC from auxiliary winding.

Vd is the voltage drop of the freewheel diode.

Setting the Initial On Time

As the AP1684 adopts constant on-time control method, the AP1684 will generate an initial on time to start a working cycle. If the initial on time is longer than the rated on time, overshoot will happen which could damage the LED. And a good system performance does not permit overshoot, so the appropriate initial on time should be guaranteed. And initial on time is determined by resister R1 shown in Figure 1.

According to initial on time generation mechanism, the ton_initial is

 $t_{on_initial} = 80 \cdot R1 \cdot 10^{-12} s$

To guarantee the system with no overshoot phenomenon, the resistor is selected

O

$$R1 = \frac{1.25 \cdot L}{R8 \cdot \sqrt{2}U_{in} rms} \cdot 10^{14}$$

The system operation mode is determined by R2, to guarantee the system working at BCM mode, resistance R2 is generally selected as R2≤R1.

Valley on Control Method

The valley on function can provide low turn-on switching losses for buck converter. The voltage across the collector and emitter of the BJT is reflected by the auxiliary winding of the buck transformer. The voltage is sensed by the FB pin.

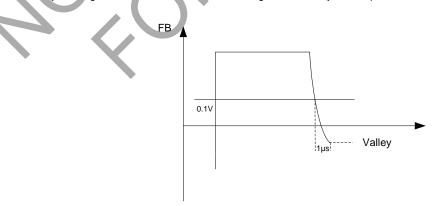


Figure 2. Valley on Control

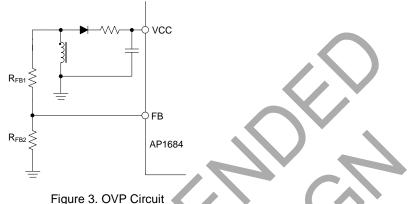


Application Information (continued)

According to Figure 2, when the falling edge of 0.1V is sensed by the FB pin, the AP1684 will see the t_{OFF} time is over and delay 1µs to start a new operating cycle. In this way we can realize valley on function.

Fault Protection

Over Voltage Protection and Output Open Protection



The output voltage is sensed by the auxiliary winding voltage of the buck transformer, the VCC pin and FB pin provide over voltage protection function. When the output is open or large transient happens, the output voltage will exceed the rated value. When the voltage of V_{CC} cap exceeds V_{CC_ovp} or V_{FB_CV}, the over voltage is triggered and the IC will discharge V_{CC}. When the V_{CC} is below the UVLO threshold voltage, the IC will start a new work cycle and the V_{CC} cap is charged again by start resistance. If the over voltage condition still exists, the system will work in hiccup mode.

Output Short Protection

When the output is shorted, the output voltage will be clamped at 0. At this condition, V_{CC} will drop down without auxiliary winding for power supply. And the V_{CC} will drop to UVLO threshold voltage, the IC will shut down and restart a new operating cycle, and the V_{CC} is charged by startup resistance. When the V_{CC} is higher than V_{cc_start} voltage, the IC will output a bunch of pulse to control BJT on and off, which will consume the energy stored in the V_{CC} cap, because of no V_{CC} supply from the auxiliary winding, the V_{CC} will drop down to V_{CC} UVLO threshold voltage again. If output short condition still exists, the system will operate in hiccup mode.

Over Temperature Protection

The AP1684 has two kinds of over temperature protection processes. First, the system is operating normally, the ambient temperature is changed to +170°C suddenly, the IC will trigger over temperature protection which leads to a latch work mode. Second, if the system starts, the over temperature protection will be triggered when the ambient temperature is higher than +150°C. So the AP1684 can startup successfully when the ambient temperature is less than +150°C.

Recommended Applications

The AP1684 is designed to drive BJT as the power switch, because of the BJT's current limit, the maximum output current is limited. In buck structure, the output voltage has some limitation because of the dead zone. The device is designed for single voltage application, so the recommended application is given in the table below.

AC Power Input	Output Voltage Range	Max Output Current
Low Mains Input	20V to 70V	200mA (13005)
High Mains Input	20V to 120V	200mA (13005)

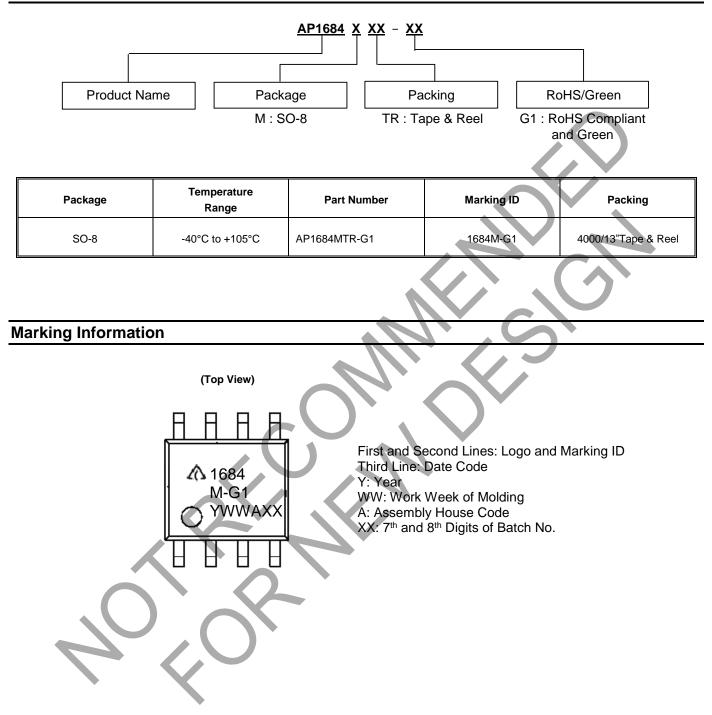
Components Selection Guide

If the system's output spec is changed, please refer to the design sheet of the AP1684 and select the compatible system parameter. When the system needs to be adjusted slightly, please refer to the table below and adjust the value of the related component.

Item	Description	Related Components
lo	LED current	R8
Output Current Ripple	Small current ripple is good for LED life	C4
ton_initial	System initial on time, used to start up the system	R1
Output Open Voltage	Setting the output voltage when the LED is open	R5, R6
Line Compensation	To get a good line regulation	R7, R9
Startup Time	System startup time	R3, C3, T1
EMI	Pass EN 55022 class B with 6DB margin	L1, C1, C2



Ordering Information

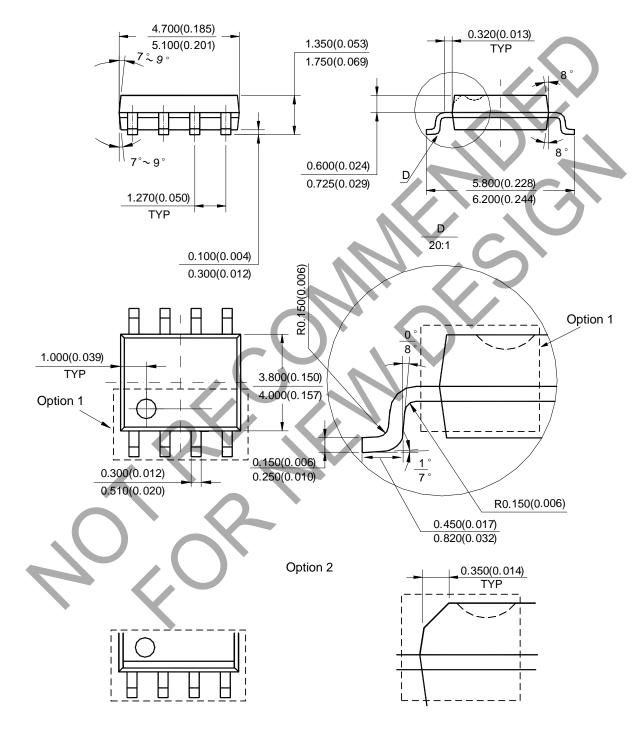




Package Outline Dimensions (All dimensions in mm(inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8



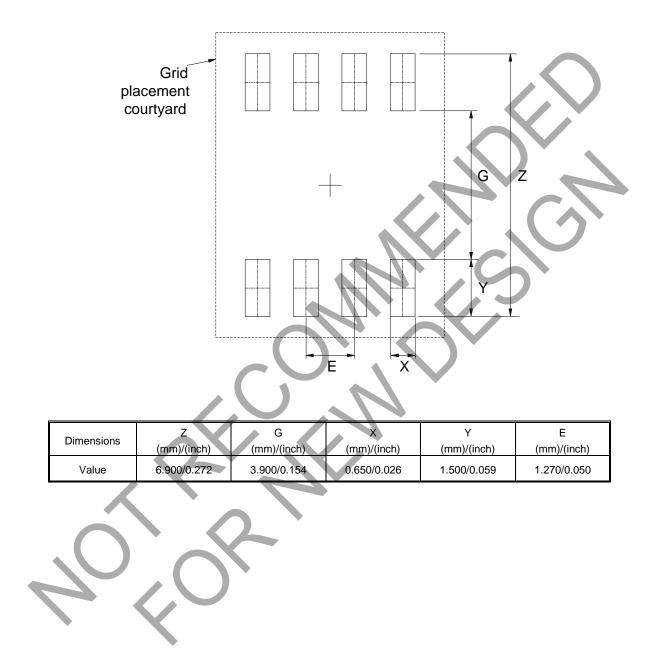
Note: Eject hole, oriented hole and mold mark is optional.



Suggested Pad Layout

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(1) Package Type: SO-8





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