

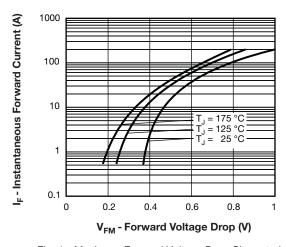
ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 141 °C, rectangular waveform		80		
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	4600	A	
		10 ms sine or 6 ms rect. pulse		790		
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25$ °C, $I_{AS} = 8$ A, L = 1.7 mH		54	mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		8	Α	

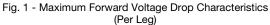
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES		UNITS		
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	T <sub>J</sub> = 25 °C	0.60	V	
		80 A		0.74		
		40 A	T <sub>J</sub> = 125 °C	0.54		
		80 A		0.66		
Maximum reverse leakage current per leg See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5	- mA	
		T <sub>J</sub> = 125 °C		45		
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2600	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.5		5.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/		V/µs		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and sto temperature range	rage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	
Maximum thermal resistan junction to case per leg	ction to case per leg		DC operation See fig. 4	0.85	°C/W	
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	0.42		
Typical thermal resistance, case to heatsink	,	R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Aitil-t				7.8	g	
Approximate weight				0.28	OZ.	
Mounting torque —	minimum			40 (35)	kgf · cm	
	maximum			58 (50)	(lbf · in)	
				81CN0	Q035A	
Marking device			Case style D-61	81CN0	81CNQ040A	
				81CNQ045A		
		•		81CNQ0	035ASM	
			Case style D-61-8-SM	81CNQ0	81CNQ040ASM	
				81CNQ0	81CNQ045ASM	
			Case style D-61-8-SL	81CNQ	81CNQ035ASL	
				81CNQ	81CNQ040ASL	
				81CNQ	045ASL	





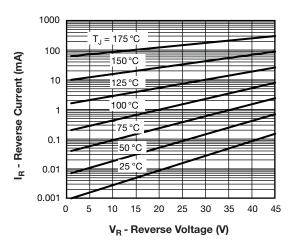


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

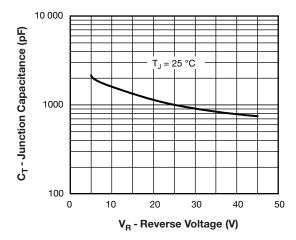


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

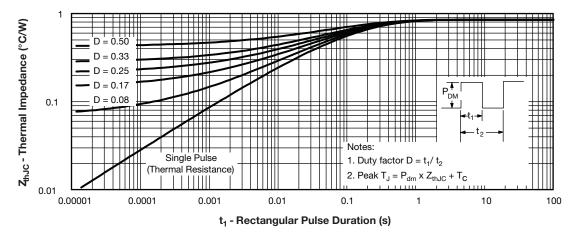


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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## Vishay Semiconductors

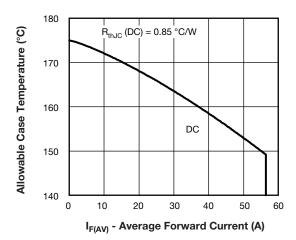


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

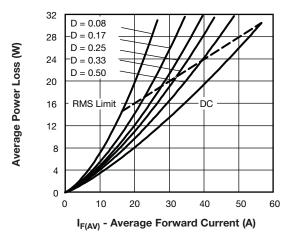


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

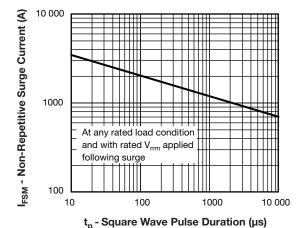


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

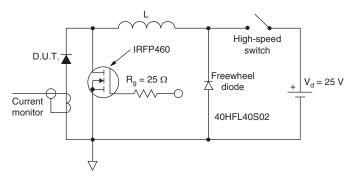
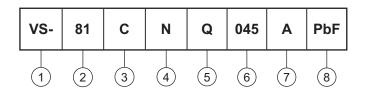


Fig. 8 - Unclamped Inductive Test Circuit



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (80 A)

3 - Circuit configuration:

C = common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

035 = 35 V

- Voltage ratings

040 = 40 V 045 = 45 V

Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = standard production

• PbF = lead (Pb)-free

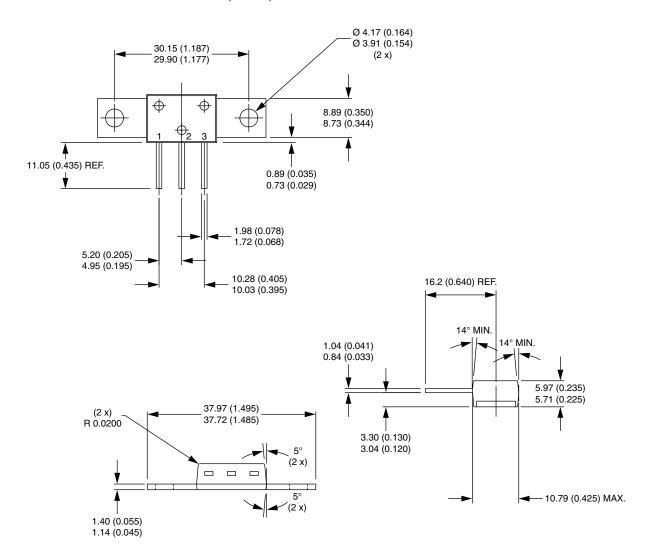
Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95354		
Part marking information	www.vishay.com/doc?95356		



# D-61-8, D-61-8-SM, D-61-8-SL

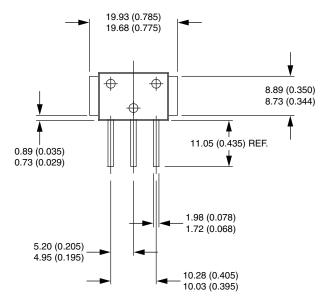
#### **DIMENSIONS - D-61-8** in millimeters (inches)

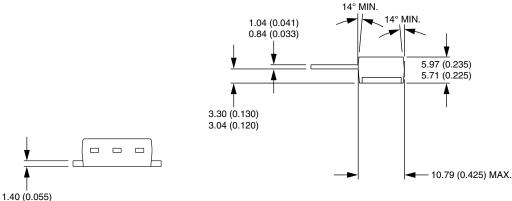




#### **DIMENSIONS - D-61-8-SM** in millimeters (inches)

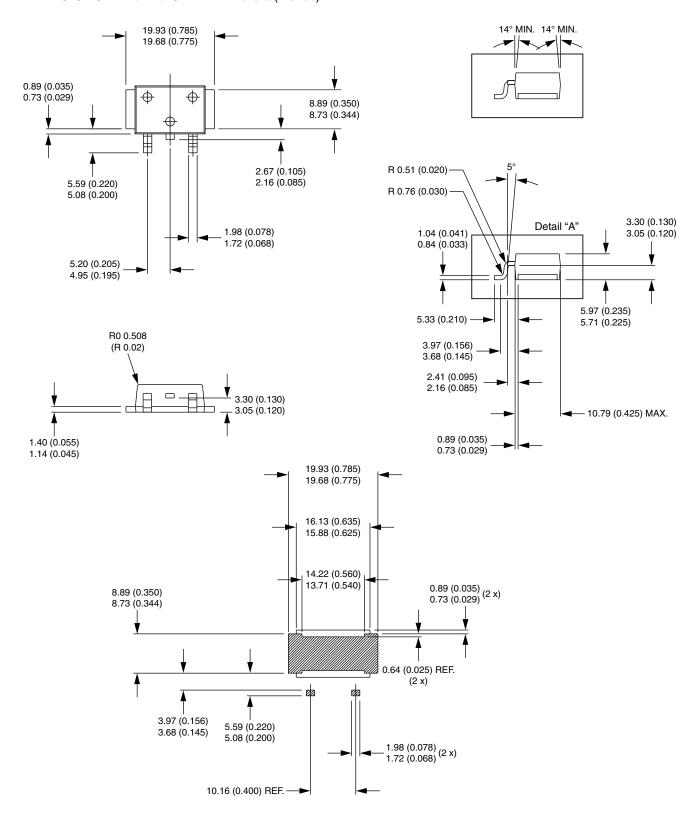
1.14 (0.045)







#### **DIMENSIONS - D-61-8-SL** in millimeters (inches)



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