

Wide Range of Support

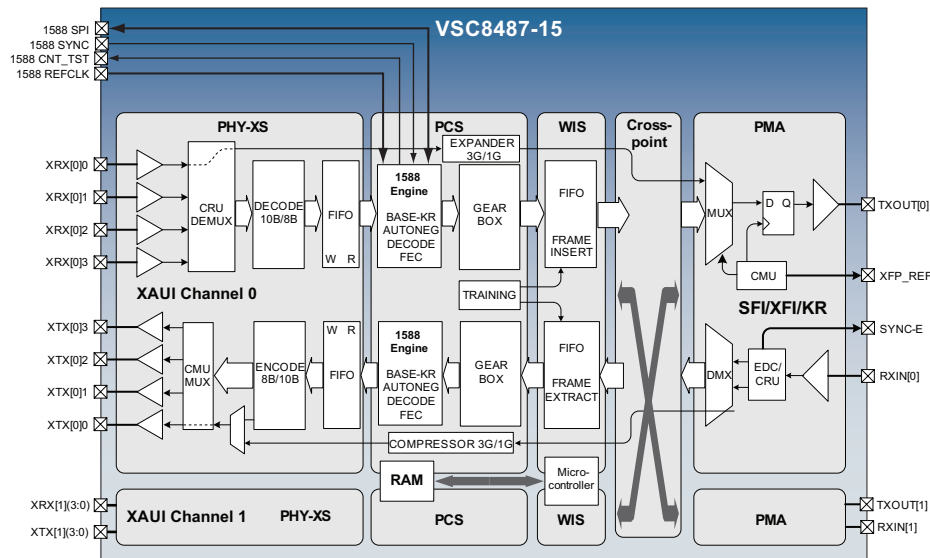
- IEEE1588v2/1731 OAM precision timing support
- Compliant to IEEE 802.3ae and SFF8431 electrical (SFI) specifications
- 9.95 Gbps, 10.3 Gbps, and 10.5 Gbps operation, as well as 1.25 Gbps and 3.125 Gbps legacy support
- Supports SFP+/QSFP+ and XFP SR/LR/LRM applications
- Support for 10GBASE-KR (IEEE 802.3ap) for 10 Gbps backplanes, including 1 Gbps, 3 Gbps, and 10 Gbps auto-negotiation
- Adaptive FFE/DFE receiver with programmable, multitap transmit pre-emphasis
- Synchronous Ethernet integrated

Power Specifications

- 1 W typical for each bidirectional channel
- 1.8 V and 1.2 V core power supplies (1.2 V to 3.3 V TTL supply)

Flexibility

- 1588v2 supports widest variety of protocol encapsulations in the industry
- Signal quality monitor for optimum eye opening and lower BER
- Failover switching and lane ordering
- VScope input signal monitoring integrated circuit technology provides a more accurate, in-line view of the post-equalization waveform, which can be remotely monitored and shared through network connections
- Host-side and line-side loopbacks with BIST functions
- I/O programmability for lane swap, invert, amplitude, slew, pre-emphasis, and equalization
- Optional forward error correction (FEC)
- Flexible clocking options enable Layer 1 support for Synchronous Ethernet
- MDIO, two-wire serial slave, and two-wire serial master interfaces
- Passive copper cable support for minimum transmission cost



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