

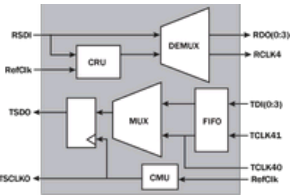
PRODUCTS

Legacy

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Signal Integrity
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Multirate 4-Bit with CRU and CMU

The VSC8145 is an extended multi-rate transceiver with integrated clock recovery unit (CRU) and clock multiplier unit (CMU) for use in SONET/SDH, DWDM, and other optical transport systems. The device performs all required serialization and deserialization functions for OC-48, OC- 12, OC-3, FEC, Gigabit Ethernet, Fibre Channel and FDDI/Fast Ethernet data rates. The integrated CRU Phase Locked Loop (PLL) recovers the high-speed clock from the input Non-Return to Zero (NRZ) data signal. The integrated CMU PLL multiplies a low-speed reference clock to provide the high-speed serial line clock for internal logic and output retiming. This device is available in commercial and industrial temperature ranges.



Product Variants	Description
VSC8145VQ-03	[Discontinued] 156-pin 21 x 21 mm TBGA, -5 °C to 70 °C
VSC8145XVQ-03	[Discontinued] 156-pin 21 x 21 mm TBGA lead-free second-level interconnect, -5 °C to 70 °C
VSC8145VQ-04	[Discontinued] 156-pin 21 x 21 mm TBGA, -40 °C to 85 °C
VSC8145XVQ-04	[Discontinued] 156-pin 21 x 21 mm TBGA lead-free second-level interconnect, -40 °C to 85 °C

— Key Features

- OC-48/-12/-3, FEC, GbE, Fibre channel and other data rates
- Integrated clock recovery and clock multiplier units
- Wide ranging PLLs with bypass capabilities
- Low-speed bit order swap (MSB/LSB)

+ Applications

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ADDITIONAL RESOURCES

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