Characteristics STTH8L02D-Y

1 Characteristics

Table 2: Absolute ratings (limiting values per diode at 25 °C, unless otherwise specified)

Symbol	Pa	Value	Unit	
V _{RRM}	Repetitive peak reverse voltage $T_j = -40 \text{ °C to } +175 \text{ °C}$		200	V
I _{F(AV)}	Average forward current	4	Α	
I _{F(RMS)}	Forward rms current	10	Α	
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$		75	Α
T _{stg}	Storage temperature range	-65 to +175	°C	
Tj	Maximum operating junction ter	-40 to +175	°C	

Table 3: Thermal resistance parameters

Symbol	Parameter	Maximum	Unit	
D	lunation to acco	Per diode	4.0	9 C AA4
R _{th(j-c)}	Junction to case	Total	2.0	°C/W

Table 4: Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25 °C	V _R = V _{RRM}	-		3	μΑ
		T _j = 125 °C		-	2	20	
	Forward voltage drop	T _j = 25 °C	I _F = 4 A	-		1	
		T _j = 125 °C		•		0.85	
V _F ⁽²⁾		T _j = 150 °C		-	0.7	0.81	V
VF(=)		T _j = 25 °C	I _F = 8 A	-		1.1	V
		T _j = 125 °C				0.97	
		T _j = 150 °C		-	0.81	0.93	

Notes:

 $^{(1)}$ Pulse test: t_p = 5 ms, δ < 2%

 $^{(2)}$ Pulse test: t_p = 380 µs, δ < 2%

To evaluate the conduction losses use the following equation:

 $P = 0.70 \text{ x } I_{F(AV)} + 0.030 \text{ x } I_{F}^{2}_{(RMS)}$



For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses in a power diode

STTH8L02D-Y Characteristics

Table 5: Dynamic electrical characteristics per diode (T_j = 25 °C, unless otherwise specified)

Symbol	Parameters	Test conditions	Min.	Тур.	Max.	Unit
		I _F = 1 A dI _F /dt = -50 A/μs V _R = 30 V	-		40	
t _{rr}	Reverse recovery time	I _F = 1 A dI _F /dt = -100 A/µs V _R = 30 V	-	20	28	ns
		I _F = 4 A		35		
I _{RM}	Reverse recovery current	dl _F /dt = -200 A/μs V _R = 160 V	-	6.8	8.5	Α
Qrr	Reverse recovery charge	T _j = 125 °C	-	110		nC

Characteristics STTH8L02D-Y

1.1 Characteristics (curves)

Characteristics (curves)

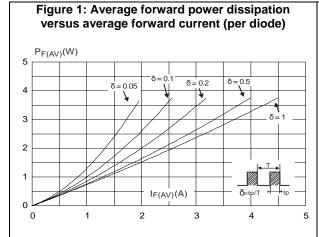


Figure 3: Forward voltage drop versus forward current (maximum values, per diode)

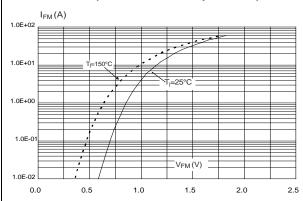


Figure 4: Junction capacitance versus reverse voltage applied (typical values, per diode)

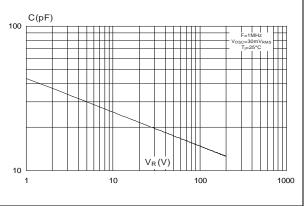


Figure 5: Reverse recovery charges versus dl_F / dt (typical values, per diode)

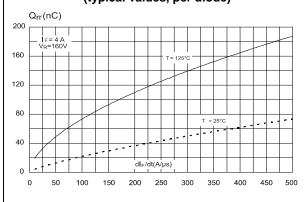
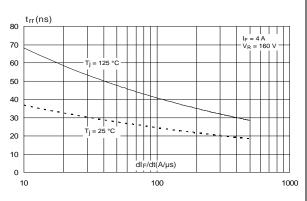
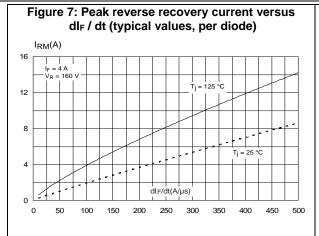


Figure 6: Reverse recovery time versus dl_F / dt (typical values, per diode)



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STTH8L02D-Y Characteristics



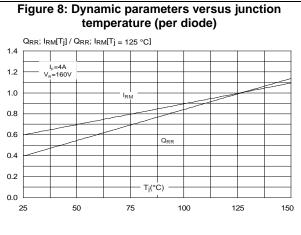


Figure 9: Relative variation of thermal impedance junction to case total versus pulse duration $Z_{th(j-c)}/R_{th(j-c)}$ 1.0 0.9 0.8 0.7 0.5 0.4 0.3 0.2 0.1 0.0 1.E-04 1.E-03 1.E-01

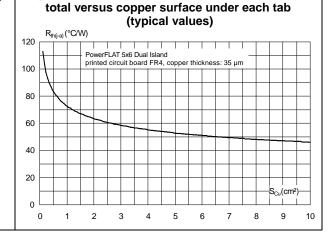


Figure 10: Thermal resistance junction to ambient

2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

2.1 PowerFLAT5x6 dual pad package information

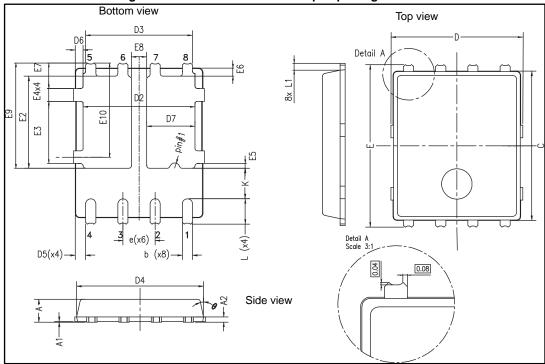


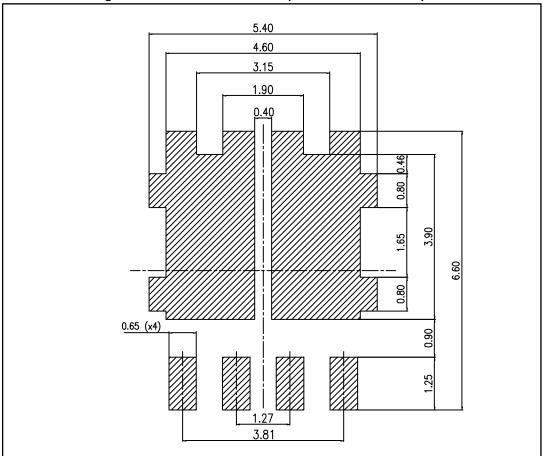
Figure 11: PowerFLAT™ 5x6 dual pad package outline

STTH8L02D-Y Package information

Table 6: PowerFLAT™ 5x6 dual pad package mechanical data

	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	0.80		1.00	0.0315		0.0394	
A1	0.02		0.05	0.0008		0.0020	
A2		0.25			0.0098		
b	0.30		0.50	0.0118		0.0197	
С	5.80	6.00	6.10	0.2283	0.2362	0.2402	
D	5.00	5.20	5.40	0.1969	0.2047	0.2126	
D2	4.15		4.45	0.1634		0.1752	
D3	4.05	4.20	4.35	0.1594	0.1654	0.1713	
D4	4.80	5.00	5.10	0.1890	0.1969	0.2008	
D5	0.25	0.40	0.55	0.0098	0.0157	0.0217	
D6	0.15	0.30	0.45	0.0059	0.0118	0.0177	
D7	1.68		1.98	0.0661		0.0780	
е		1.27			0.0500		
Е	6.20	6.40	6.60	0.2441	0.2520	0.2598	
E2	3.50		3.70	0.1378		0.1457	
E3	2.35		2.55	0.0925		0.1004	
E4	0.40		0.60	0.0157		0.0236	
E5	0.08		0.28	0.031		0.0110	
E6	0.20	0.325	0.45	0.0079	0.0128	0.0177	
E7	0.85	1.00	1.15	0.0335	0.0394	0.0453	
E8	0.55		0.75	0.0217		0.0295	
E9	4.00	4.20	4.40	0.1575	0.1654	0.1732	
E10	3.55	3.70	3.85	0.1398	0.1457	0.1516	
K	1.05		1.35	0.0502		0.0620	
L	0.90	1.00	1.10	0.0285	0.0325	0.0364	
L1	0.175	0.275	0.375	0.0069	0.0108	0.0148	
Θ	0°		12°	0°		12°	

Figure 12: PowerFLAT™ 5x6 dual pad recommended footprint



3 Ordering information

Table 7: Ordering information

Order code	ode Marking Package		Weight	Base qty.	Delivery mode
STTH8L02DDJFY-TR	TH8L 02DY	PowerFLAT™ 5x6 dual Island	0.095 g	3000	Tape and reel

4 Revision history

Table 8: Document revision history

Date	Revision	Changes
09-Jun-2017	1	First issue

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