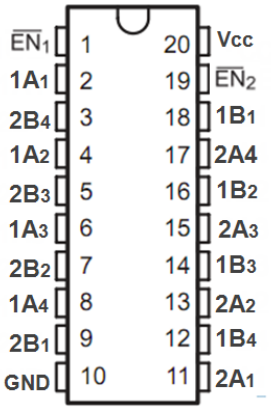
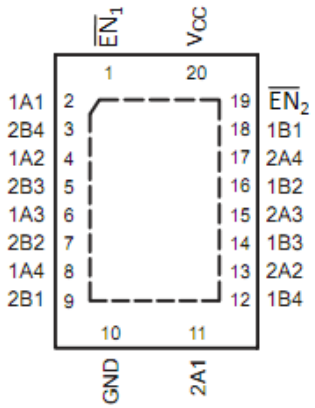
Pin Configuration



TSSOP



TQFN

Pin Description

Pin No	Pin Name	Description
1, 19	$\overline{EN}$x	Switch Enable
2, 4, 6, 8	1A1-1A4	A Ports
3, 5, 7, 9	2B1-2B4	B Ports
10	GND	Ground
12, 14, 16, 18	1B4-1B1	B Ports
13, 15, 17, 19	2A4-2A1	A Ports
20	V_{CC}	Power

Maximum Ratings

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential	-0.5V to + 4.6V
DC Input Voltage	-0.5V to + 6.0V
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

3.3V supply (Over operating range, T_A = -40 ~ +85°C, V_{CC}=3.3V±10%, unless otherwise noted)

Symbol	Description	Test Conditions(1)	Min	Typ(2)	Max	Unit
V_{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0	-	-	V
V_{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.5	-	0.8	V
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18mA	-	-1.3	-1.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}	-	-	±1	µA
I_{IL}	Input Low Current	V_{CC} = Max., V_{IN} = GND	-	-	±1	µA
I_{OZH}	High-Impedance Current	0 ≤ A, B ≤ V_{CC}	-	-	±1	µA
R_{ON}	Switch On-Resistance(3)	V_{CC} = Min., V_{IN} = 0.0V I_{ON} = -48mA or -64mA	-	4	6	Ω
		V_{CC} = Min., V_{IN} = 3.6V I_{ON} = -15mA	-	5	8	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 3.3V, TA = 25°C ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

2.5V supply (Over operating range, T_A = -40 ~ +85°C, V_{CC}=2.5V±10%, unless otherwise noted)

Symbol	Description	Test Conditions(1)	Min	Typ(2)	Max	Unit
V_{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	1.8	-	V_{CC}+0.3	V
V_{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.3	-	0.8	V
V_{IK}	Clamp Diode Voltage	V_{CC} = Max., I_{IN} = -6mA	-	-0.7	-1.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}	-	-	±1	µA
I_{IL}	Input Low Current	V_{CC} = Max., V_{IN} = GND	-	-	±1	µA
I_{OZH}	High-Impedance Current	0 ≤ A, B≤ V_{CC}	-	-	±1	µA
R_{ON}	Switch On-Resistance(3)	V_{CC} = Min., V_{IN} = 0.0V I_{ON} = -48mA	-	4	8	Ω
		V_{CC} = Min., V_{IN} = 2.25V I_{ON} = -15mA	-	7	14	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 2.5V, T_A = 25°C ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

1.8V supply (Over operating range, T_A = -40 ~ +85℃, V_{CC}=1.8V±10%, unless otherwise noted)

Symbol	Description	Test Conditions[1]	Min	Typ[2]	Max	Unit
V_{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	1.2	-	V_{CC}+0.3	V
V_{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.3	-	0.6	V
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18mA	-	-0.7	-1.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}	-	-	±1	μA
I_{IL}	Input Low Current	V_{CC} = Max., V_{IN} = GND	-	-	±1	μA
I_{OZH}	High-Impedance Current	0 ≤ A, B ≤ V_{CC}	-	-	±1	μA
R_{ON}	Switch On-Resistance[3]	V_{CC} = Min., V_{IN} = 0.0V I_{ON} = -48mA	-	4	8	Ω
		V_{CC} = Min., V_{IN} = 1.6V I_{ON} = -15mA	-	10	25	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 1.8V, TA = 25℃ ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

Capacitance (T_A = 25℃, f=1MHz)

Symbol[1]	Description	Test Conditions	Typ[2]	Unit
C_{IN}	Input Capacitance	V_{IN} = 0V	2.0	pF
C_{OFF}	A/B Capacitance, Switch Off		3.5	
C_{ON}	A/B Capacitance, Switch On		7.0	

Note:

1. These parameters are determined by device characterization but are not production tested

Power Supply Characteristics

Symbol	Description	Test Conditions[1]	Min	Typ[2]	Max	Unit
I_{CC}	Quiescent Power Supply Current	V_{CC} = 3.6V, V_{IN} = GND or V_{CC}	-	0.2	0.5	mA

Note:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at +25℃ ambient.

Dynamic Electrical Characteristics

(Over Operating Range, T_A = -40 ~ +85℃, V_{CC}=3.3V±10%)

Symbol	Description	Test Conditions	Min	Typ	Max	Unit
X_{TALK}	Crosstalk	10MHz	-	-60	-	dB
O_{IRR}	Off-Isolation	10MHz	-	-60	-	
BW	-3dB Bandwidth	See test Diagram	-	500	-	MHz

Switch Characteristics

Over 3.3V Operating Range

Symbol	Description	Test Conditions(1)	Min	Typ	Max	Unit
t_{PLH}, t_{PHL}	Propagation Delay(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	ns
t_{PZH}, t_{PZL}	Enable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	9.0	
t_{PHZ}, t_{PLZ}	Disable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	9.0	

Note:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Over 2.5V Operating Range

Symbol	Description	Test Conditions(1)	Min	Typ	Max	Unit
t_{PLH}, t_{PHL}	Propagation Delay(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	ns
t_{PZH}, t_{PZL}	Enable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	15.0	
t_{PHZ}, t_{PLZ}	Disable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	12.0	

Note:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

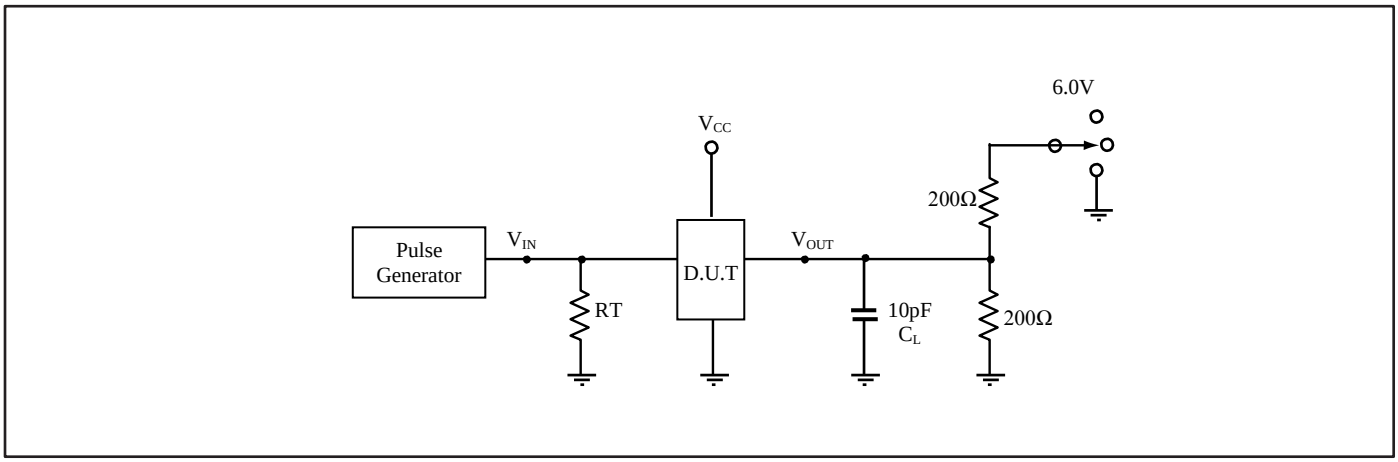
Over 1.8V Operating Range

Symbol	Description	Test Conditions(1)	Min	Typ	Max	Unit
t_{PLH}, t_{PHL}	Propagation Delay(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	ns
t_{PZH}, t_{PZL}	Enable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	25.0	
t_{PHZ}, t_{PLZ}	Disable Time $\overline{EN}$x to Ax or Bx	See test Diagram	1.5	-	12.0	

Note:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Test Circuit for Electrical Characteristics



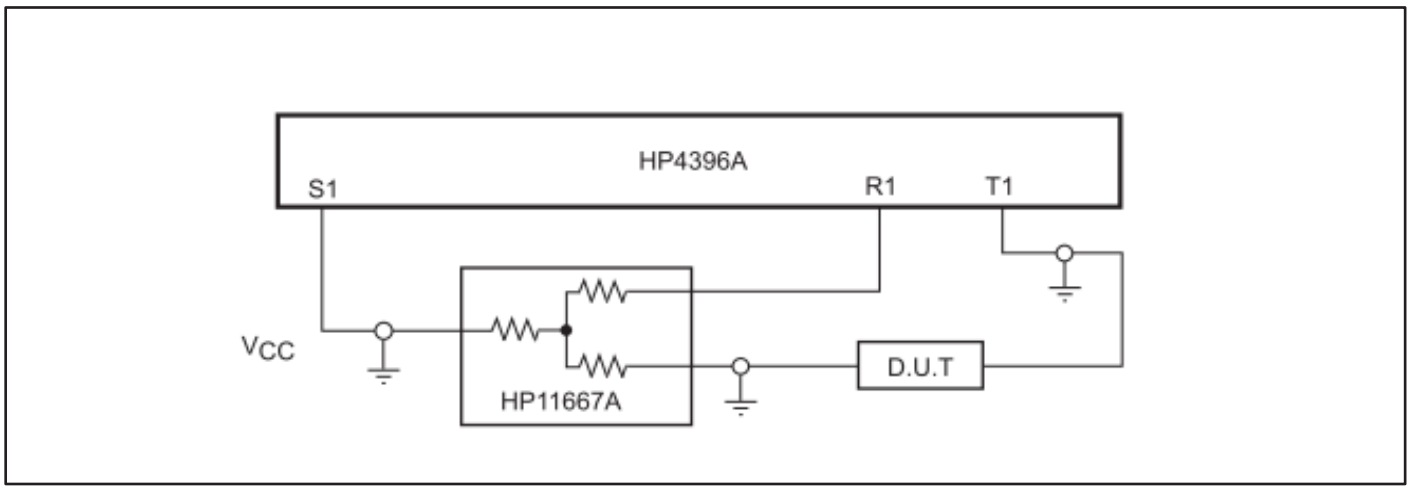
Notes:

1. C_L = Load capacitance: includes jig and probe capacitance.
2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
3. All input impulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50-ohm, t_R ≤ 2.5ns, t_F ≤ 2.5ns.
4. The outputs are measured one at a time with one transition per measurement.

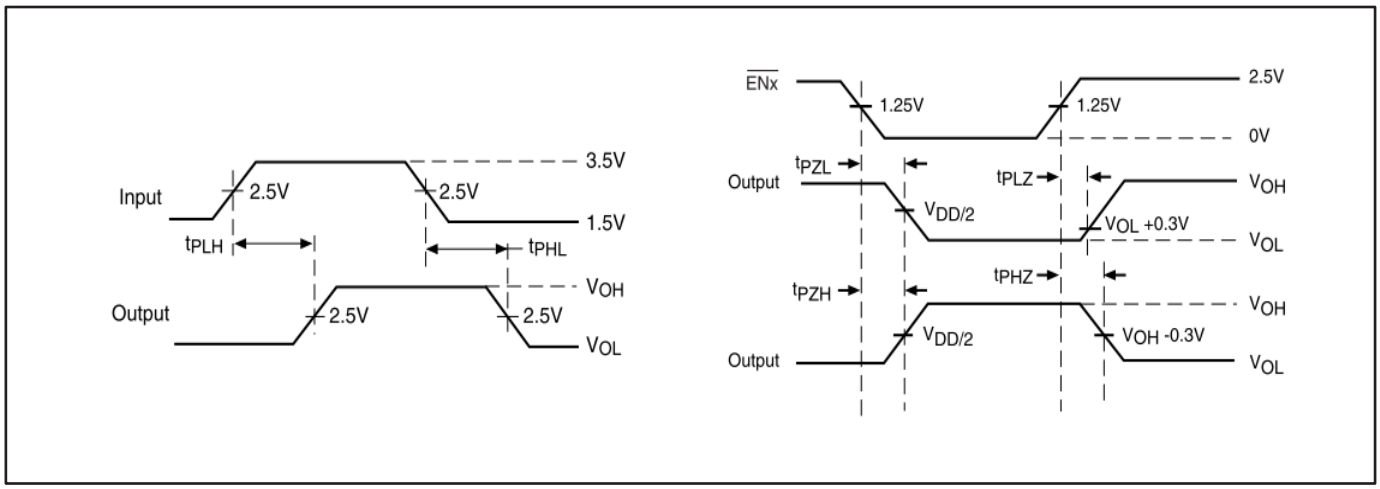
Switch Positions

Test	Switch
t_{PLZ}, t_{PZL}	6.0V
t_{PHZ}, t_{PZH}	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics



Switching Waveforms



Applications Information

Logic Inputs

The logic control inputs can be driven up to 3.6V regardless of the supply voltage. For example, given a +3.3V supply, $\overline{EN}$ may be driven LOW to 0V and HIGH to 3.6V. Driving $\overline{EN}$ Rail-to-Rail® minimizes power consumption.

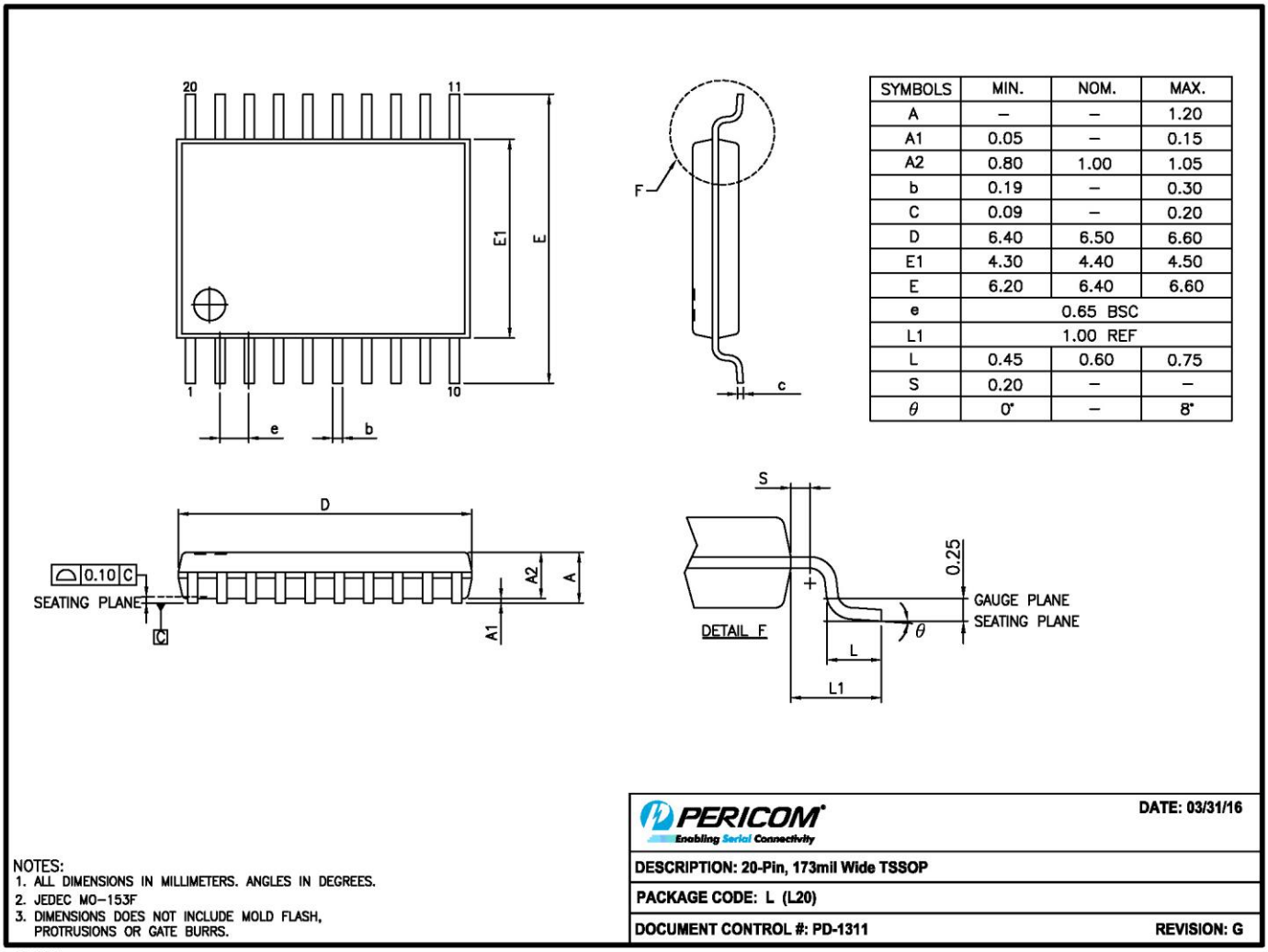
Hot Insertion

For Datacom and Telecom applications that have ten or more volts passing through the backplane, a high voltage from the power supply may be seen at the device input pins during hot insertion. The PI3CH3244 devices have maximum limits of 6V and 120mA for 20ns. If the power is higher or applied for a longer time or repeatedly reaches the maximum limits, the devices can be damaged.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Mechanical Packaging

20-TSSOP (L)



SYMBOLS	MIN.	NOM.	MAX.
A	–	–	1.20
A1	0.05	–	0.15
A2	0.80	1.00	1.05
b	0.19	–	0.30
C	0.09	–	0.20
D	6.40	6.50	6.60
E1	4.30	4.40	4.50
E	6.20	6.40	6.60
e	0.65 BSC		
L1	1.00 REF		
L	0.45	0.60	0.75
S	0.20	–	–
θ	0°	–	8°

NOTES:
1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. JEDEC MO-153F
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

PERICOM Enabling Serial Connectivity — DATE: 03/31/16

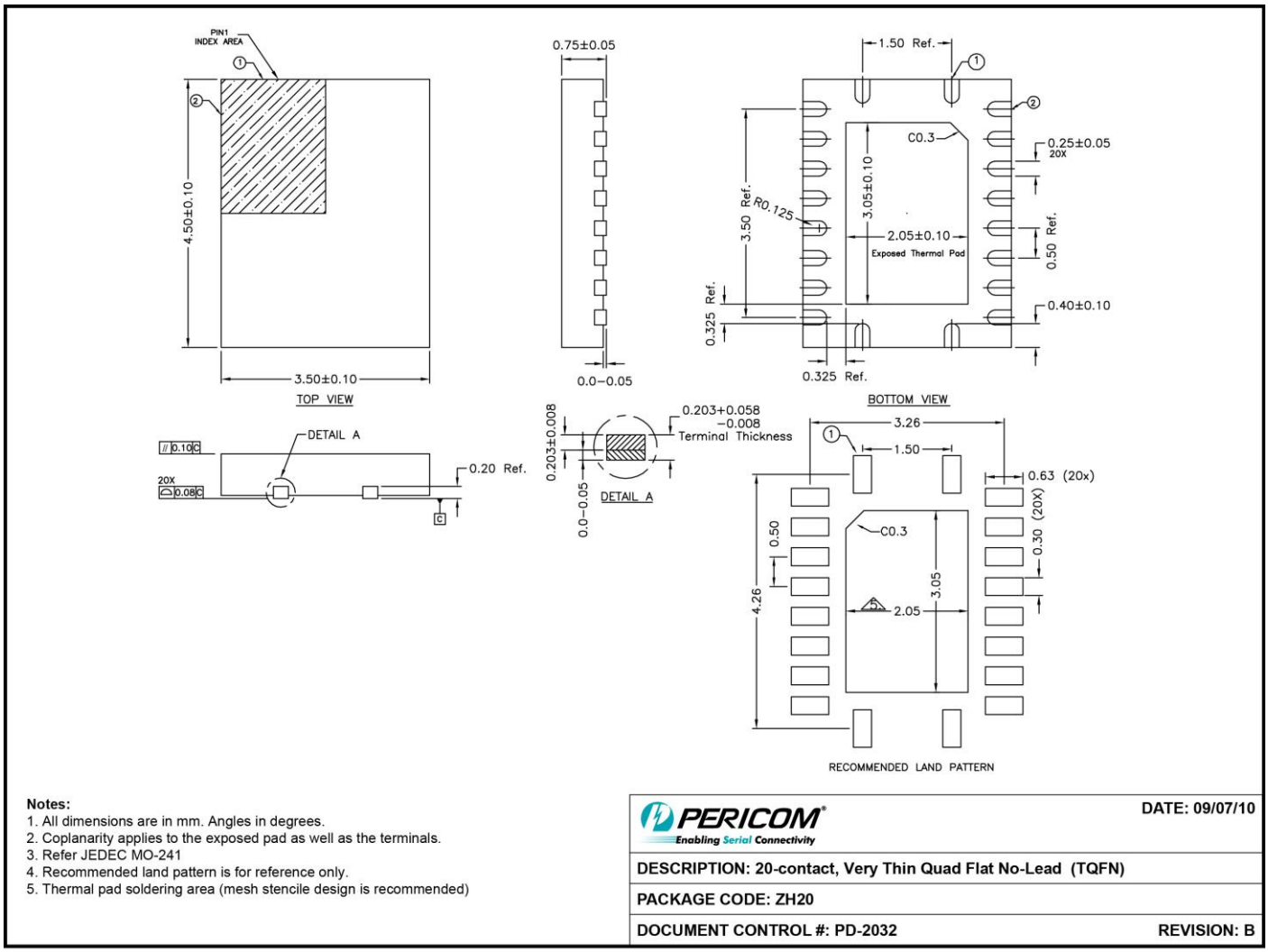
DESCRIPTION: 20-Pin, 173mil Wide TSSOP

PACKAGE CODE: L (L20)

DOCUMENT CONTROL #: PD-1311 — REVISION: G

16-0074

20-TQFN (ZH)



Notes:
1. All dimensions are in mm. Angles in degrees.
2. Coplanarity applies to the exposed pad as well as the terminals.
3. Refer JEDEC MO-241
4. Recommended land pattern is for reference only.
5. Thermal pad soldering area (mesh stencile design is recommended)

PERICOM Enabling Serial Connectivity — DATE: 09/07/10

DESCRIPTION: 20-contact, Very Thin Quad Flat No-Lead (TQFN)

PACKAGE CODE: ZH20

DOCUMENT CONTROL #: PD-2032 — REVISION: B

For latest package info.
please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Part No.	Package Code	Package Description
PI3CH3244LEX	L	20-Pin, 173mil Wide (TSSOP)
PI3CH3244ZHEX	ZH	20-contact, Very Thin Quad Flat No-Lead (TQFN)

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

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B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

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