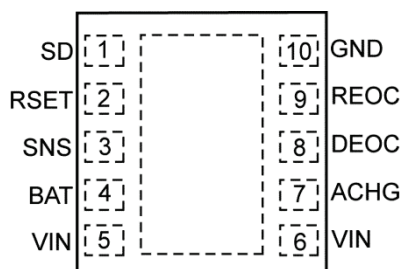


Ordering Information

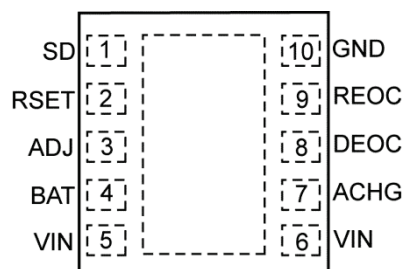
Part Number	Marking Code ⁽¹⁾	Voltage	Junction Temperature Range	Package
MIC79110-4.2YML	L942	4.2V	–40° to +125°C	10-Pin 3mm × 3mm DFN
MIC79110YML	L9AA	Adjustable	–40° to +125°C	10-Pin 3mm × 3mm DFN

1. Pb-Free “Y” indicator is added to the device mark after logo.

Pin Configuration



Fixed Output
10-Pin 3mm × 3mm DFN (ML)



Adjustable Output
10-Pin 3mm × 3mm DFN (ML)

Pin Description

Pin Number	Pin Name	Pin Function
1	SD	Shutdown Input. Logic HIGH = Off; Logic LOW = On.
2	RSET	Current limit: Sets constant current limit via an external resistor to ground. $I_{RSET} = (0.2V/R_{SET}) \times 1000$.
3	SNS	(Fixed voltage only): Sense output, connect directly to battery.
3	ADJ	(Adjustable voltage only): Feedback input.
4	BAT	Battery Terminal. Connect to single-cell lithium-ion battery.
5, 6	VIN	Input supply pin.
7	ACHG	Analog Charge Indicator Output: Current source whose output current is equal to 1/1000 of the BAT pin current.
8	DEOC	Digital End-of-Charge Output: N-Channel open-drain output. LOW indicates charging, a current that is higher than the programmed current set by R_{EOC} is charging the battery. When the current drops to less than the current set by R_{EOC} , the output goes high impedance, indicating end-of-charge.
9	REOC	End-of-Charge Set: Sets end-of-charge current threshold via an external resistor to ground. $I_{EOC} = (0.2V/R_{EOC}) \times 1000$.
10	GND	Ground

Absolute Maximum Ratings⁽²⁾

Input Supply Voltage (V_{IN}) 0V to 18V
 Shutdown Input Voltage (V_{SD}) 0V to 10V
 Output Voltage (ADJ) 10V
 Power Dissipation Internally Limited
 Junction Temperature -40°C to $+125^{\circ}\text{C}$

Operating Ratings⁽³⁾

Input Supply Voltage 2.5V to 16V
 Shutdown Input Voltage (V_{SD}) 0V to 7V
 Output Voltage (ADJ) 9.6V
 Junction Temperature Range (T_J) -40°C to $+125^{\circ}\text{C}$
 3mm \times 3mm DFN-10 (θ_{JA}) 60°C
 3mm \times 3mm DFN-10 (θ_{JC}) 2°C

Electrical Characteristics⁽⁴⁾

$T_A = 25^{\circ}\text{C}$ with $V_{IN} = V_{OUT} + 1\text{V}$; $I_{LOAD} = 100\mu\text{A}$; $C_{BAT} = 10\mu\text{F}$; $SD = 0\text{V}$; $R_{SET} = 1\text{k}\Omega$. **Bold** values indicate $-40^{\circ}\text{C} < T_J < +125^{\circ}\text{C}$; unless otherwise specified.

Parameter	Condition	Min.	Typ.	Max.	Units
Output Voltage Accuracy	$V_{OUT} = 4.2\text{V}$; $I_{LOAD} = 50\text{mA}$; $T_J = -5^{\circ}\text{C}$ to $+60^{\circ}\text{C}$	-0.75		+0.75	%
	$V_{OUT} = 4.2\text{V}$; $I_{LOAD} = 50\text{mA}$; $T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	-1.5		+1.5	
ADJ Pin Voltage Accuracy		0.5955	0.6	0.6045	V
Line Regulation	$V_{IN} = V_{OUT} + 1\text{V}$ to 16V @ $I_{LOAD} = 50\text{mA}$	-0.1		+0.1	%/V
Load Regulation	$I_{LOAD} = 0.1\text{mA}$ to 1A		0.3		%
Dropout Voltage ⁽⁵⁾	$I_{LOAD} = 100\text{mA}$, $R_{SET} = 167\Omega$		160	250	mV
	$I_{LOAD} = 700\text{mA}$, $R_{SET} = 167\Omega$		375	550	mV
Ground Current	$I_{LOAD} = 10\text{mA}$, $R_{SET} = 167\Omega$		2	3	mA
	$I_{LOAD} = 700\text{mA}$, $R_{SET} = 167\Omega$		24	35	mA
VIN Pin Current	$SD = V_{IN}$		120	300	μA
Shutdown Pin Current	$SD = 5.2\text{V}$, $V_{BAT} = 0$		0.1	5	μA
Shutdown Input Threshold	Logic High, regulator off	1.05			V
	Logic Low, regulator on			0.93	V
Shutdown Hysteresis			60		mV
Current Limit Accuracy ^(6, 7)	$V_{OUT} = 0.9 \times V_{NOM}$; $I_{OUT} = 1.2\text{A}$, $R_{SET} = 167\Omega$, $T_J = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	-5		+5	%
	$V_{OUT} = 0.9 \times V_{NOM}$; $I_{OUT} = 0.1\text{A}$, $R_{SET} = 2\text{k}\Omega$	-20		+20	%
Current-Limit Setpoint Range ⁽⁷⁾		0.1		1.2	A

Notes:

- Exceeding the absolute maximum rating may damage the device.
- The device is not guaranteed to function outside its operating rating.
- Specification for packaged product only.
- Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 2.5V, dropout voltage is the input-to-output voltage differential with the minimum input voltage 2.5V. Minimum input operating voltage is 2.5V.
- V_{NOM} denotes the nominal output voltage.
- $I_{RSET} = (0.2\text{V}/R_{SET}) \times 1000$.

Electrical Characteristics⁽⁴⁾ (Continued)

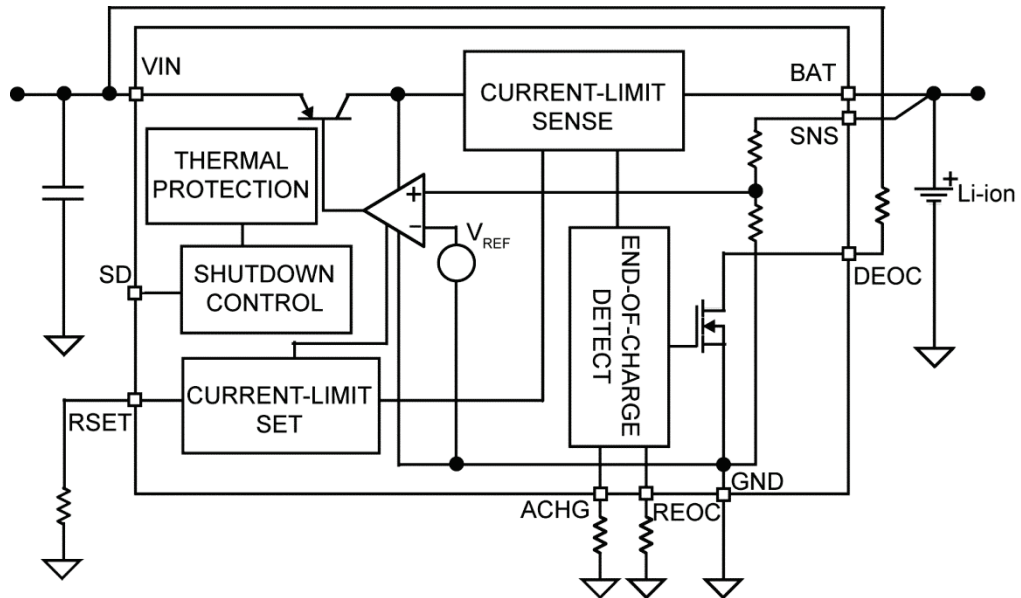
$T_A = 25^\circ\text{C}$ with $V_{IN} = V_{OUT} + 1\text{V}$; $I_{LOAD} = 100\mu\text{A}$; $C_{BAT} = 10\mu\text{F}$; $SD = 0\text{V}$; $R_{SET} = 1\text{k}\Omega$. **Bold** values indicate $-40^\circ\text{C} < T_J < +125^\circ\text{C}$; unless otherwise specified.

Parameter	Condition	Min.	Typ.	Max.	Units
Maximum Current Limit	R_{SET} shorted to ground, $V_{BAT} = 0.9 \times V_{NOM}$	1.25	1.65	2.5	A
V_{BAT} Reverse Current	$V_{IN} = \text{High impedance or ground}$		4.2	20	μA
Digital End-of-Charge (DEOC) Output					
$I_{EOC}^{(8,9)}$	$R_{EOC} = 4\text{k}\Omega$ Current Falling	35	50	65	mA
		30		70	
$I_{EOC}^{(8,9)}$	$R_{EOC} = 4\text{k}\Omega$ Current Rising	50	70	95	mA
		40		100	
D_{EOC} Logic-Low Voltage	$I_{EOC} = 5\text{mA}$, $I_{BAT} = 700\text{mA}$		0.74	0.95	V
D_{EOC} Leakage Current	Logic HIGH = $V_{IN} = 16\text{V}$		0.1		μA
D_{EOC} On Resistance	$V_{IN} = +5\text{V}$		150	190	Ω
R_{EOC} Maximum Current Limit	R_{EOC} shorted to ground	0.5	1.0	2.0	mA
Analog Charge Indicator (ACHG) Output					
$I_{SOURCE}^{(10)}$	$I_{BAT} = 50\text{mA}$	37	46	55	μA
	$I_{BAT} = 1.2\text{A}$, $T_J = -40^\circ\text{C}$ to $+85^\circ\text{C}$	800	950	1150	

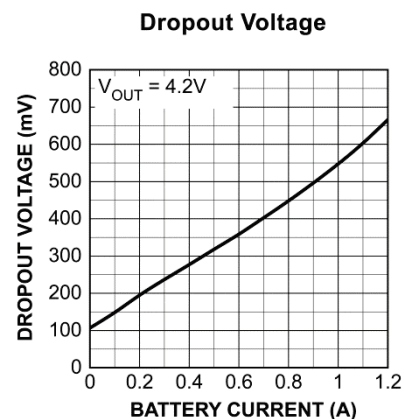
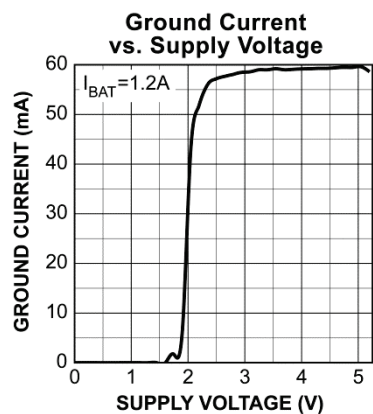
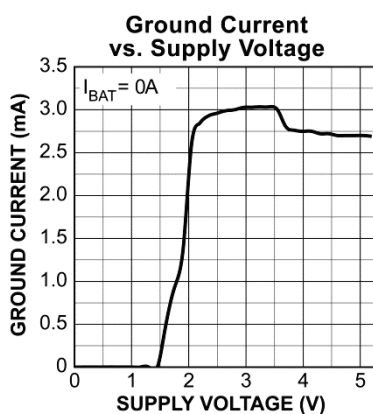
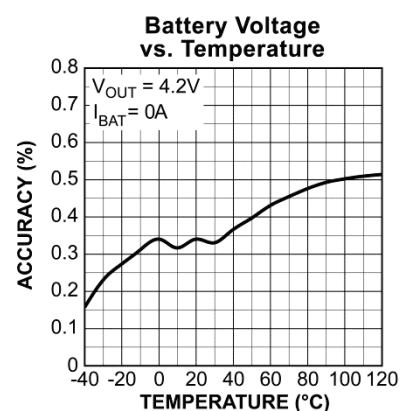
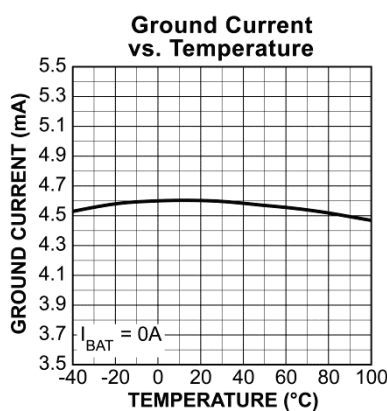
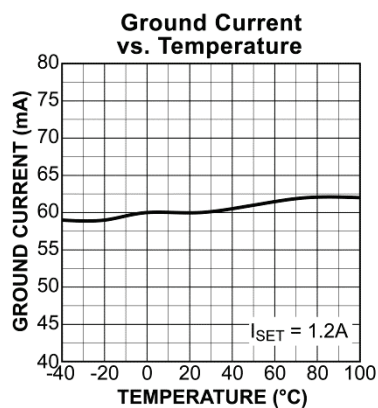
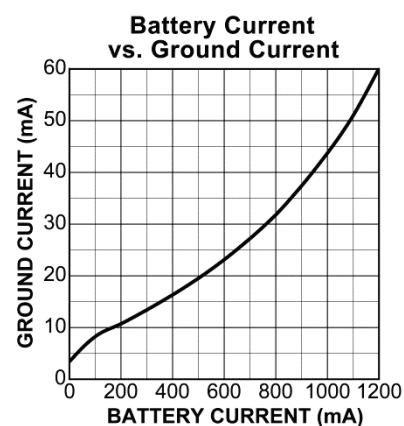
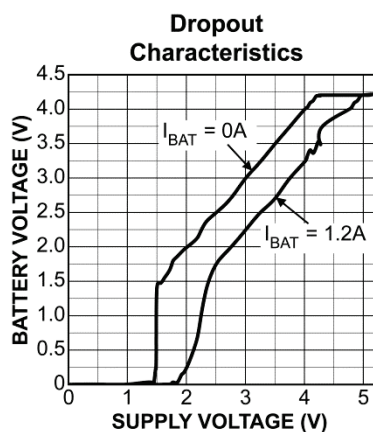
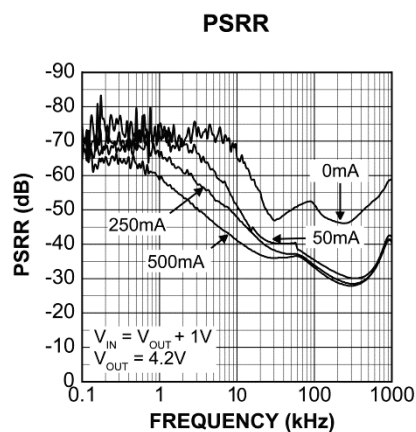
Notes:

8. Output current I_{EOC} when digital end-of-charge output goes high impedance. Currents greater than I_{EOC} , the DEOC output is low, currents lower than I_{EOC} , DEOC is high impedance.
9. $I_{EOC} = (0.2\text{V}/R_{EOC}) \times 1000$.
10. I_{SOURCE} is the current output from ACHG pin. A resistor to ground from the ACHG pin will program a voltage that is proportional to the output current.

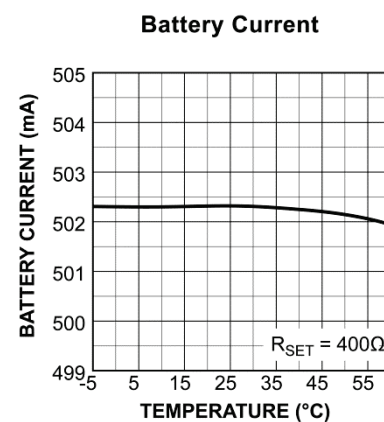
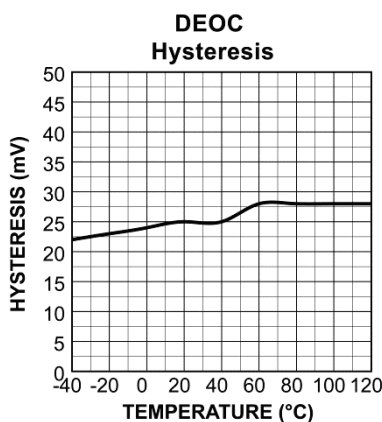
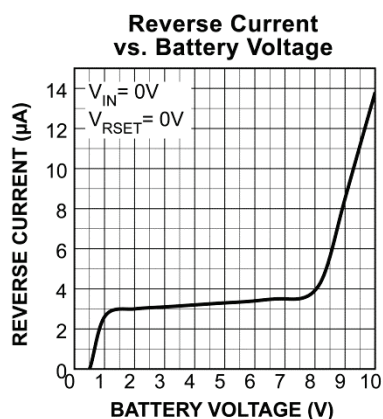
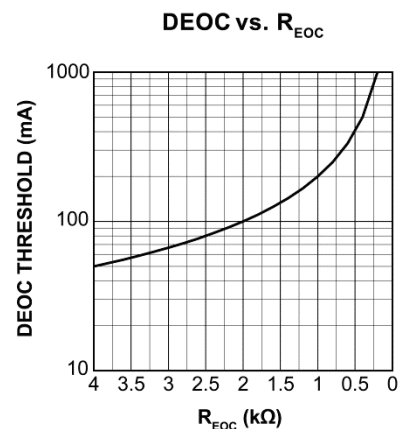
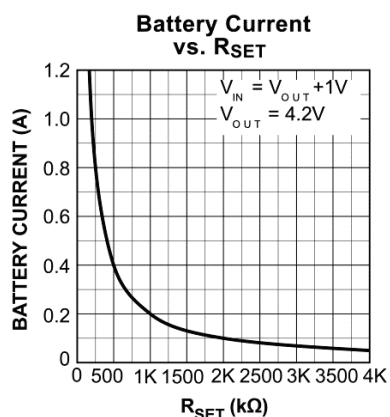
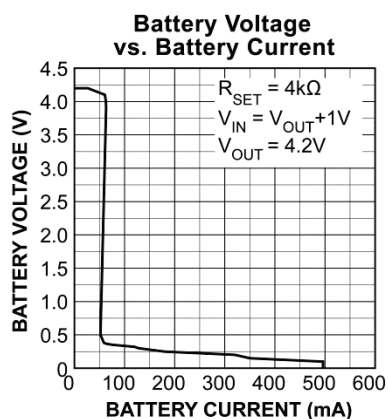
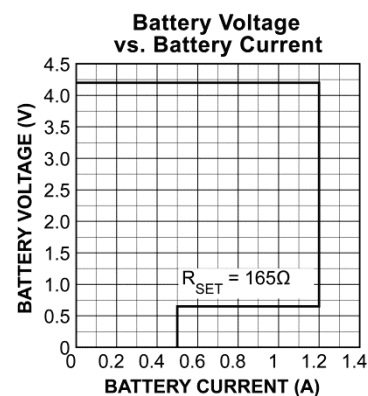
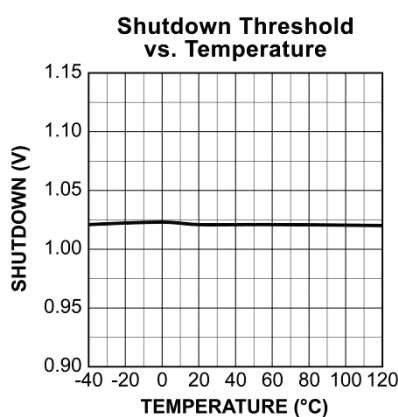
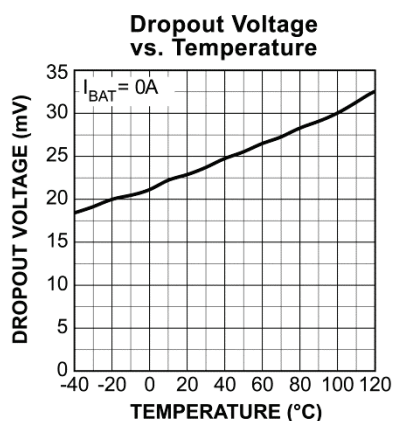
Block Diagram



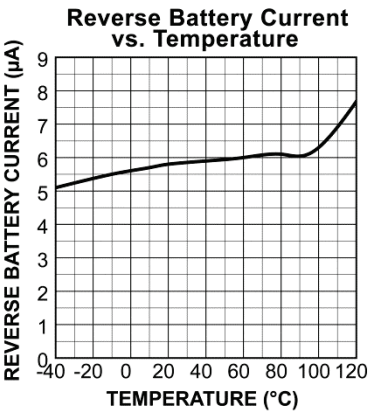
Typical Characteristics



Typical Characteristics (Continued)

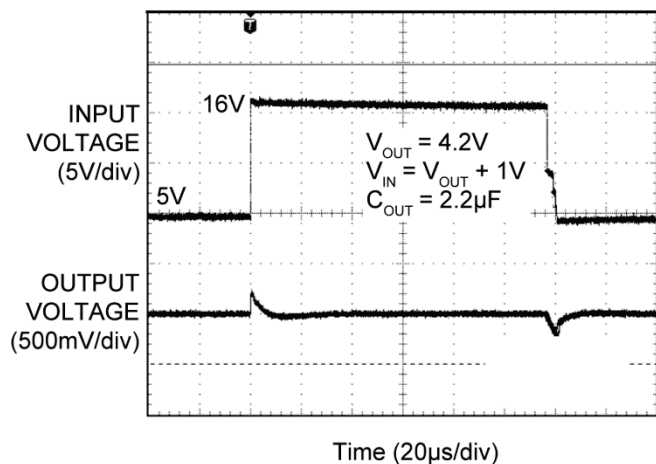


Typical Characteristics (Continued)

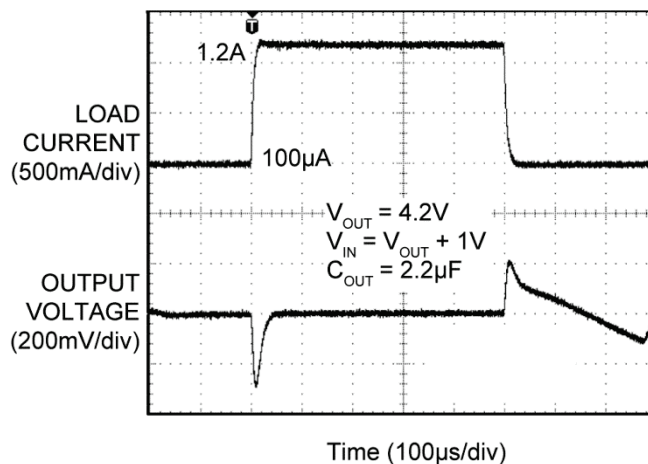


Functional Characteristics

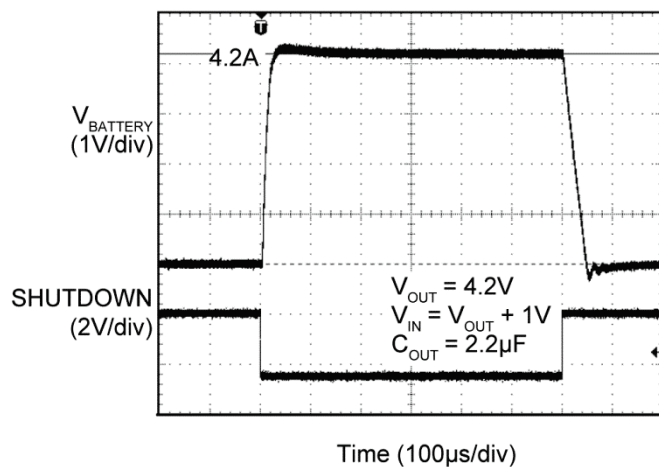
Line Transient



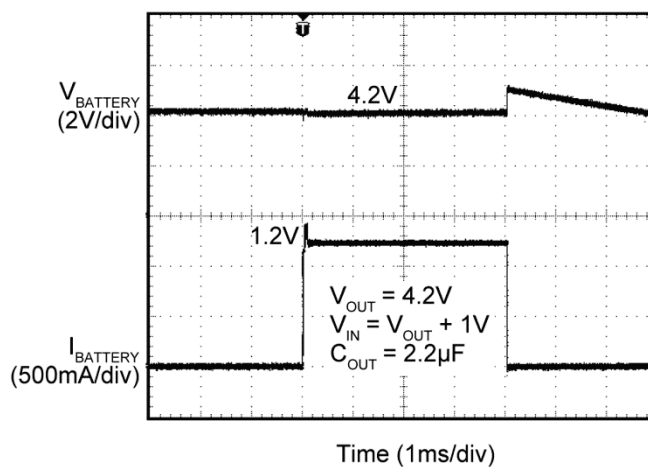
Load Transient



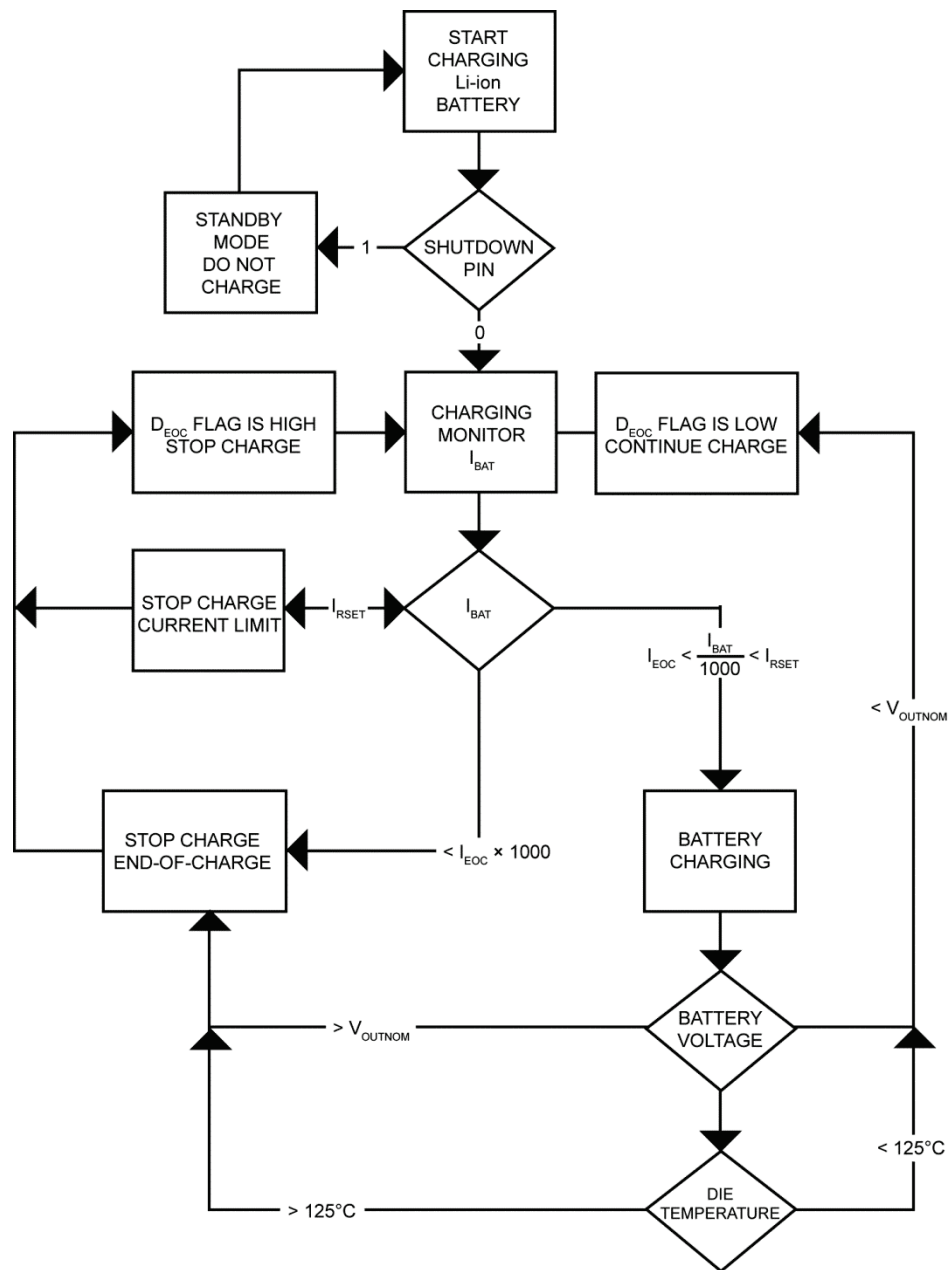
Shutdown Transient



Battery Current Enable Transient



Flow Chart



Application Information

Detailed Description

The MIC79110 forms a complete charger for 1-cell lithium-ion batteries. It includes precision voltage control (0.75% over temperature) to optimize both cell performance and cycle life. All are compatible with common 4.2V lithium-ion chemistries. Voltages other than 4.2V can be obtained with the adjustable version. Other features include current limit, end-of-charge flag, and end-of-charge current limit using an external resistor. The shutdown pin enables low quiescent current when not charging.

Current-Limit Mode

MIC79110 features an internal current limit that is set by the RSET pin with a resistor-to-ground. The maximum current is calculated by Equation 1:

$$I_{RSET} = (0.2/R_{SET}) \times 1000 \quad \text{Eq. 1}$$

Using a 167Ω RSET resistor will achieve the maximum current limit for the MIC79110 at 1.2 amperes.

End-of-Charge

REOC pin is connected to a resistor-to-ground. This resistor is used to set the end of charge current for the lithium-ion battery as in Equation 2:

$$I_{EOC} = (0.2/R_{EOC}) \times 1000 \quad \text{Eq. 2}$$

Using a 4kΩ REOC resistor will set the end-of-charge current at 50mA.

I_{EOC} should be set at 10% of the battery's rated current.

Digital End-of-Charge Output

This pin is the output of an open drain. When tied high to the supply using a resistor, the output will toggle high or low depending on the output current of I_{BAT} .

- Low state indicates that the I_{BAT} current is higher than the programmed current set by R_{EOC} .
- High state indicates that the I_{BAT} current is lower than the programmed current set by R_{EOC} . The output goes high impedance indicating end-of-charge.

Analog End-Of-Charge Output

The ACHG pin provides a small current that is proportional to the charge current. The ratio is set at 1/1000th of the output current.

Shutdown

The SD pin serves as a logic input (active low) to enable the charger.

Built-in hysteresis for the shutdown pin is 50mV over temperature.

Reverse Polarity Protection

In the event that $V_{BAT} > V_{IN}$ and the shutdown pin is active low, there is reverse battery current protection built in. The current is limited to less than 10μA over temperature.

Constant Output Voltage/Current Charging

The MIC79110 features constant voltage and constant current output to correctly charge lithium-ion batteries. The constant voltage is either 4.2V or adjustable. The constant current is set by the RSET pin and is constant down to around 300mV. Since R_{SET} can be set below 500mA, the minimum output current is set at 500mA for output voltages below 100mV. This minimum voltage starts the charging process in lithium-ion batteries. If the output current is too low, the battery will not begin charge.

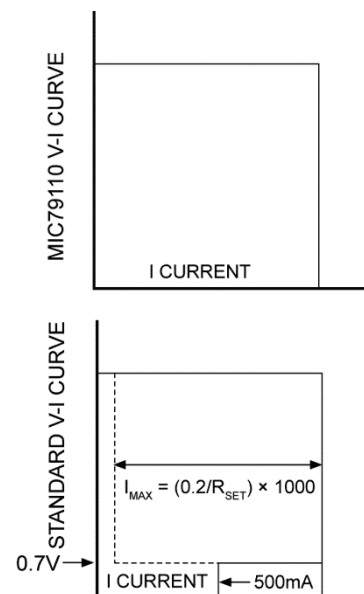


Figure 1. MIC79110 Constant Output Voltage

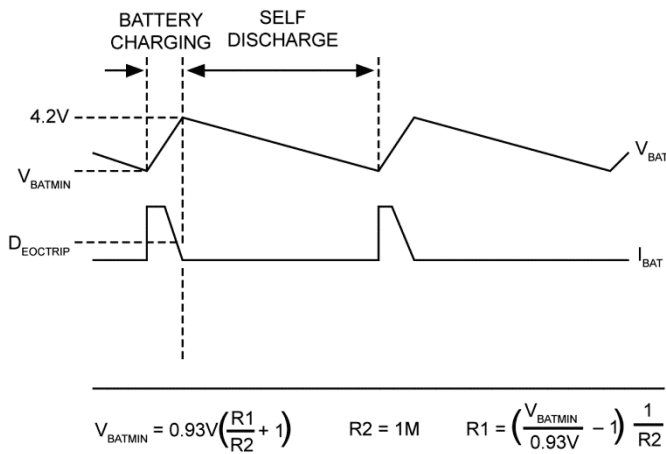


Figure 4. Auto Top-Off Charger Application

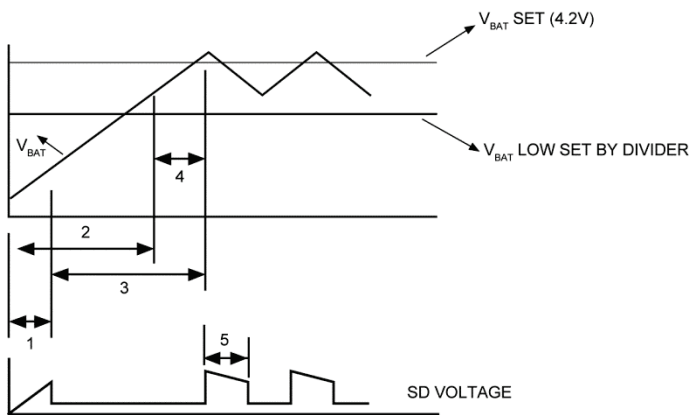


Figure 5. Charging Description

1. SD not held low by active high DEOC because DEOC Comparator's inputs do not common-mode to ground. Divider holds SD low so part can start.
2. SD held low by divider.
3. SD held low by active high DEOC.
4. Divider voltage above SD threshold and DEOC open.
5. Divider voltage drops below SD threshold and charging begins again.

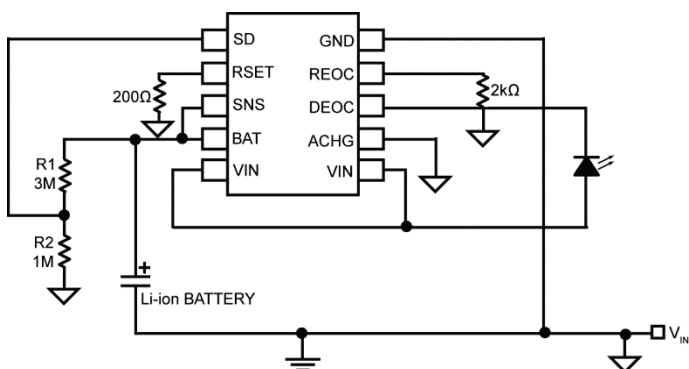


Figure 6. Top-Off Charger with Internal Reset – Application Circuit

This circuit is similar to the auto top-off charger circuit mentioned above except that the DEOC pin is externally triggered to restart the charging cycle. It still uses the same resistor divider to set the minimum battery voltage before the lithium-ion needs to be recharged.

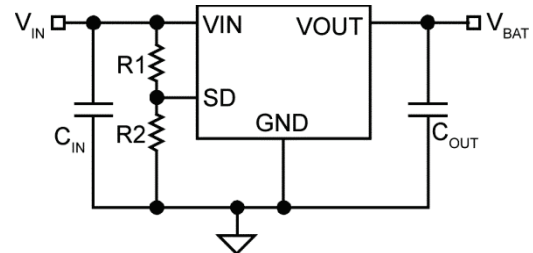


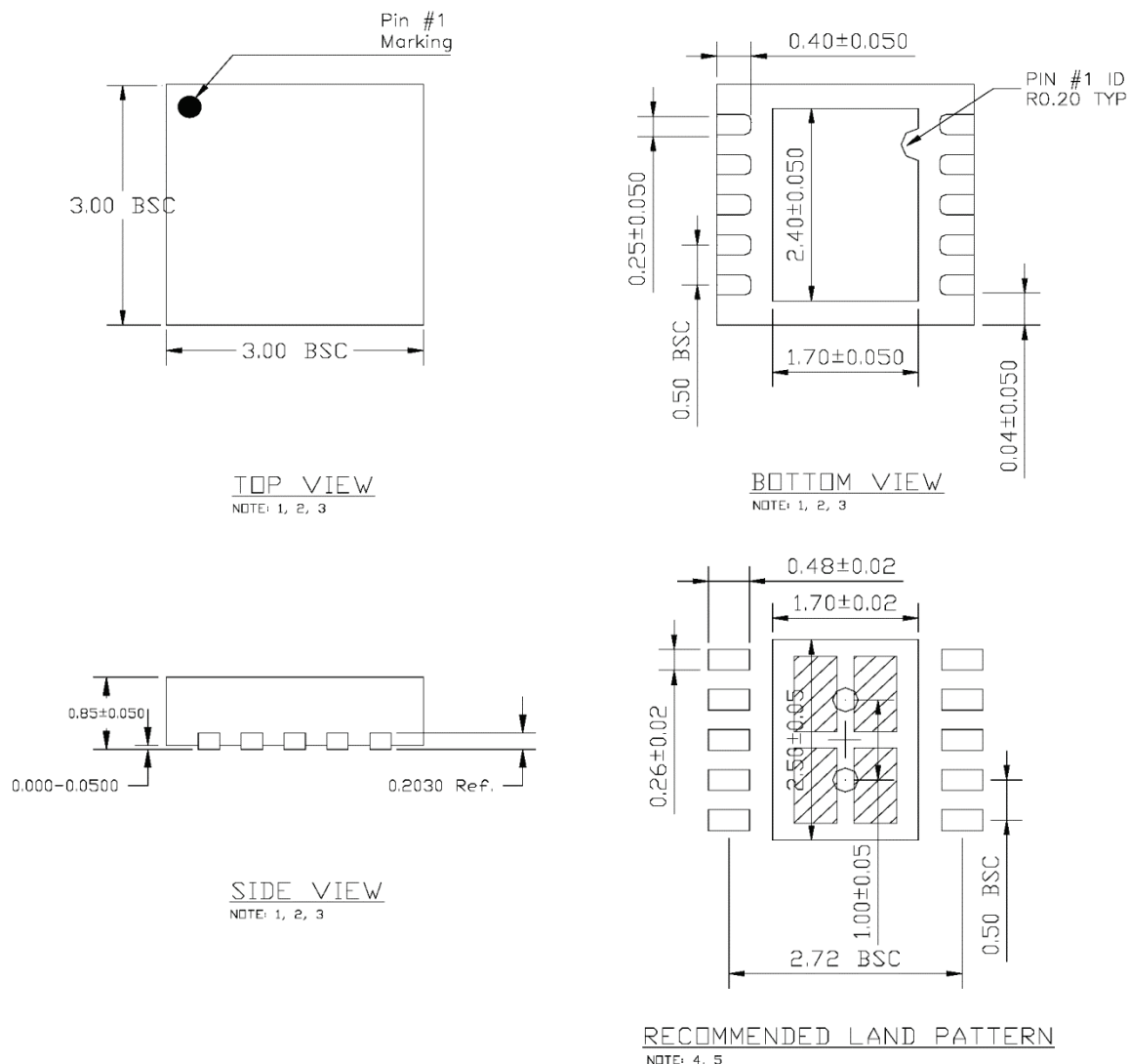
Figure 7. Auto-Shutdown using Shutdown Pin

The shutdown pin on the MIC79110 can be used to automatically shutdown the battery charger when the input voltage rises above a safe operating voltage. To keep the part from heating up and entering thermal shutdown, we can connect the shutdown pin to VIN using a resistor divider. Use Equation 3 to setup the maximum VIN:

$$\frac{VIN(MAX)}{V_{SD}} = \frac{R1}{R2} + 1 \quad \text{Eq. 3}$$

The MIC79110 can be connected to a wall wart with a rectified DC voltage and protected from over voltages at the input.

Package Information⁽¹¹⁾ and Recommended Landing Pattern



NOTE:

1. MAX PACKAGE WARPAGE IS 0.05 MM
2. MAX ALLOWABLE BURR IS 0.076MM IN ALL DIRECTIONS
3. PIN #1 IS ON TOP WILL BE LASER MARKED
4. RED CIRCLE IN LAND PATTERN INDICATE THERMAL VIA. SIZE SHOULD BE 0.30-0.3MM IN DIAMETER AND SHOULD BE CONNECTED TO GND FOR MAX THERMAL PERFORMANCE
5. GREEN RECTANGLES (SHADED AREA) indicate SOLDER STENCIL OPENING ON EXPOSED PAD AREA. SIZE SHOULD BE 0.50x0.95 MM IN SIZE, 0.20 MM SPACING.

10-Pin 3mm × 3mm DFN (MM)

Note:

11. Package information is correct as of the publication date. For updates and most current information, go to www.micrel.com.

MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USATEL +1 (408) 944-0800 FAX +1 (408) 474-1000 WEB <http://www.micrel.com>

Micrel makes no representations or warranties with respect to the accuracy or completeness of the information furnished in this data sheet. This information is not intended as a warranty and Micrel does not assume responsibility for its use. Micrel reserves the right to change circuitry, specifications and descriptions at any time without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Micrel's terms and conditions of sale for such products, Micrel assumes no liability whatsoever, and Micrel disclaims any express or implied warranty relating to the sale and/or use of Micrel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right.

Micrel Products are not designed or authorized for use as components in life support appliances, devices or systems where malfunction of a product can reasonably be expected to result in personal injury. Life support devices or systems are devices or systems that (a) are intended for surgical implant into the body or (b) support or sustain life, and whose failure to perform can be reasonably expected to result in a significant injury to the user. A Purchaser's use or sale of Micrel Products for use in life support appliances, devices or systems is a Purchaser's own risk and Purchaser agrees to fully indemnify Micrel for any damages resulting from such use or sale.

© 2013 Micrel, Incorporated.

Revision History

Date	Change Description/Edits by:	Rev.
05/21/13	Original DS edited and reflowed with applied edits – S. Thompson	2.0