

12V Hot-Plug Switch

ABSOLUTE MAXIMUM RATINGS

Voltage Range on V_{CC} and LOAD Relative to GND

Continuous-0.3V to +18V

1ms Maximum-0.3V to +22V

Voltage Range on ILIM and VRAMP

Relative to GND.....-0.3V to (V_{CC} + 0.3V),
but not to exceed +18V

Voltage Range on TIMER Relative to GND-0.3V to +5.0V

Drain Current

Continuous4A

Peak 15A

Operating Junction Temperature Range-40°C to +135°C

Storage Temperature Range-55°C to +135°C

Soldering Temperature.....Refer to the IPC/JEDEC
J-STD-020 Specification.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

(T_J = -40°C to +135°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	V _{CC}	(Notes 1, 2)	9.0		13.2	V
R _{ILIM} Value	R _{ILIM}		20		400	Ω
C _{VRAMP} Value	C _{VRAMP}		0.04		5.00	μF
C _{TIMER} Value	C _{TIMER}		0.04		5.00	μF
TIMER Turn-On Voltage	V _{ON}		2.6		5	V
TIMER Turn-Off Voltage	V _{OFF}		-0.3		+2.0	V

ELECTRICAL CHARACTERISTICS

(V_{CC} = +12V, T_J = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current	I _{CC}	(Note 3)		1.1	2.00	mA
UVLO Rising	V _{UVLOR}		7.5	8.0	8.5	V
UVLO Falling	V _{UVLOF}		6.5	7.0	7.5	V
UVLO Hysteresis	V _{UVLOH}			1		V
On-Resistance	R _{ON}			25	32	mΩ
MOSFET Output Capacitance	C _{OUT}			500		pF
LOAD Voltage During Off State	V _{LOFF}	(Note 4)			200	mV
Delay Time from Enable to Beginning of Conduction	t _{POND}	C _{VRAMP} = 1μF		5		ms
Gate-Charging Time from Conduction to 90% of V _{OUT}	t _{GCT}	C _{VRAMP} = 1μF, C _{LOAD} = 1000μF	48	66	80	ms
Shutdown Junction Temperature	T _{SHDN}	(Note 5)	120	135	150	°C
Thermal Hysteresis	T _{HYS}	(Note 5)		40		°C
TIMER Charging Current	I _{TIMER}		70	80	92	μA
Overvoltage Clamp	V _{OVCL}		13.5	15.0	16.5	V

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ELECTRICAL CHARACTERISTICS (continued)

($V_{CC} = +12V$, $T_J = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Short-Circuit Limit	I_{SCL}	$R_{ILIM} = 56\Omega$ (Note 6)	2.0	2.5	3.0	A
Overload Limit	I_{OVL}	$R_{ILIM} = 56\Omega$ (Note 7)	3.5	4.4	5.9	A
LOAD Voltage Slew Rate	SR_{LOAD}	$C_{VRAMP} = 1\mu F$	0.13	0.15	0.18	V/ms
VRAMP Charging Current	I_{VRAMP}		70	80	92	μA

Note 1: All voltages are referenced to ground. Currents entering the IC are specified positive and currents exiting the IC are negative.

Note 2: This supply range guarantees that the LOAD voltage is not clamped by the overvoltage limit.

Note 3: Supply current specified with no load on the LOAD pin.

Note 4: V_{LOFF} voltage specified with a 2.5mA load applied to LOAD.

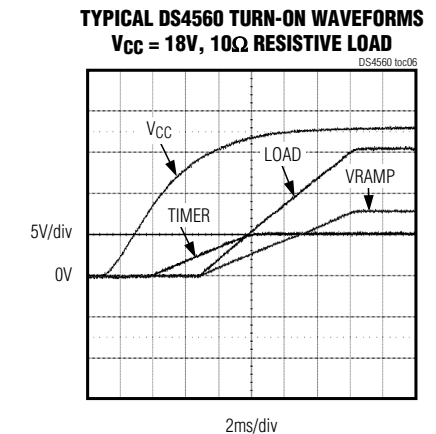
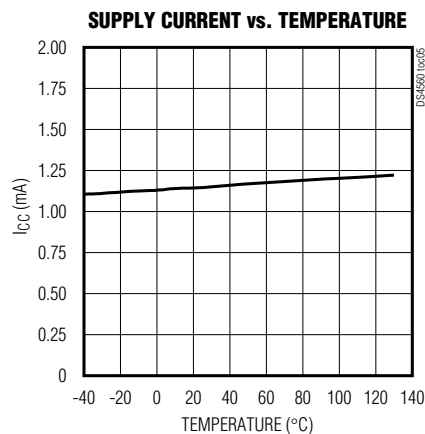
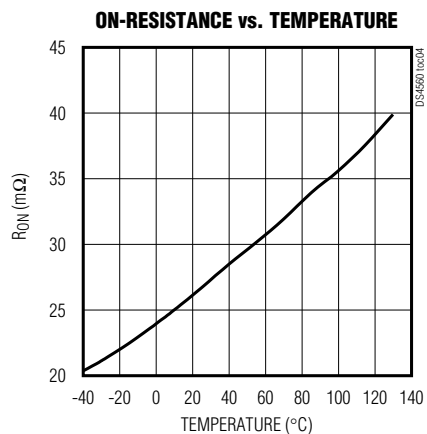
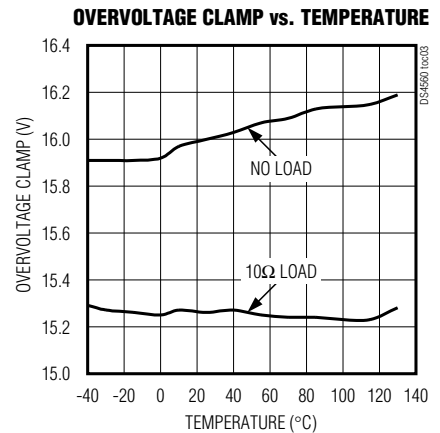
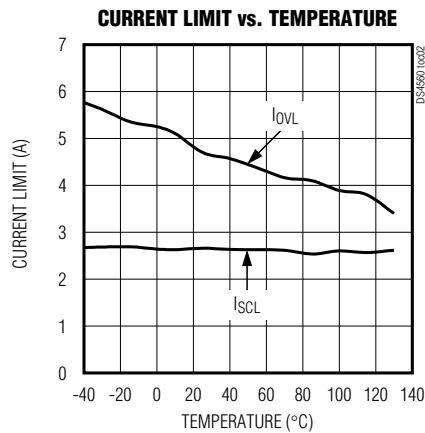
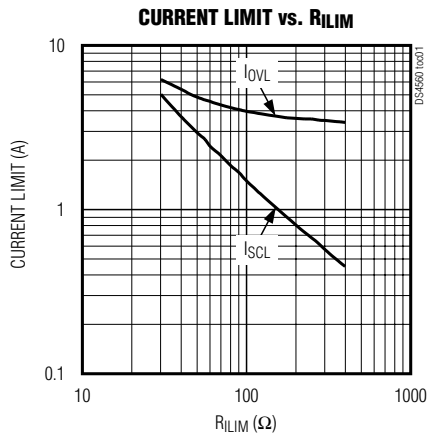
Note 5: Not production tested. Guaranteed by design.

Note 6: I_{SCL} is the current limit when the output voltage is initially ramping up.

Note 7: I_{OVL} is the current limit after the output voltage ramping is complete.

Typical Operating Characteristics

($V_{CC} = 12V$, $T_A = +25^\circ C$, $R_{ILIM} = 56\Omega$, $V_{CC} = 12V$, $C_{TIMER} = 0.1\mu F$, $C_{VRAMP} = 0.1\mu F$, unless otherwise noted.)

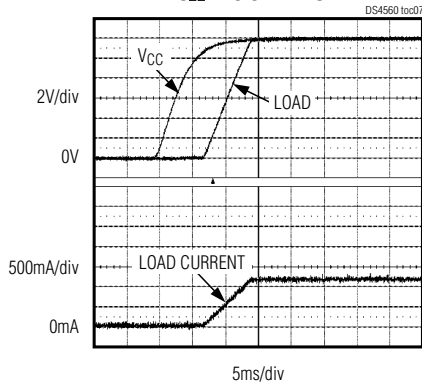


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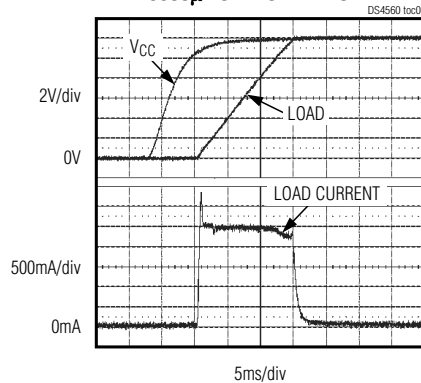
Typical Operating Characteristics (continued)

($V_{CC} = 12V$, $T_A = +25^\circ C$, $R_{LIM} = 56\Omega$, $V_{CC} = 12V$, $C_{TIMER} = 0.1\mu F$, $C_{VRAMP} = 0.1\mu F$, unless otherwise noted.)

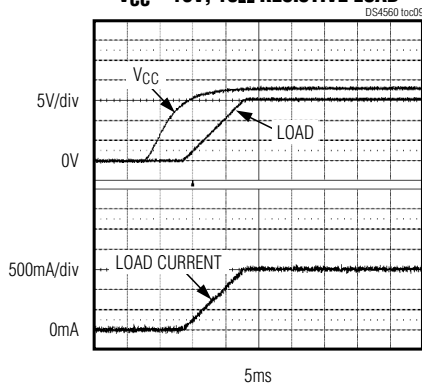
**TURN-ON WAVEFORMS
10 Ω RESISTIVE LOAD**



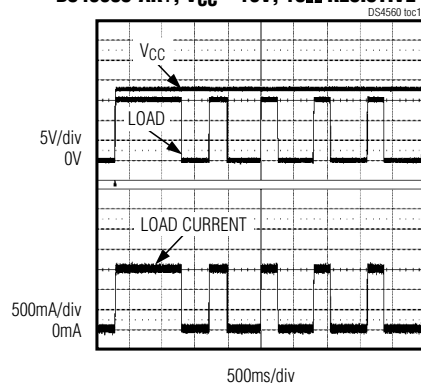
**TURN-ON WAVEFORMS
3300 μF CAPACITIVE LOAD**



**TURN-ON WAVEFORMS
 $V_{CC} = 18V$, 10 Ω RESISTIVE LOAD**



**THERMAL SHUTDOWN WITH AUTORETRY
DS4560S-AR+, $V_{CC} = 18V$, 10 Ω RESISTIVE LOAD**



DS4560

The schematic diagram illustrates the internal architecture of the MAX4560 precision current source. Key components and their connections include:

- Power Supply:** The circuit is powered by a +12V VCC and a +5V VREG.
- UVLO (Under Voltage Lock Out):** Monitors the VCC supply.
- CHARGE PUMP:** Generates a negative voltage for the internal comparators.
- THERMAL LIMIT:** A thermal shutdown circuit that disables the output when the device temperature exceeds a threshold.
- CURRENT LIMIT:** Limits the output current to a maximum value (ILIM).
- OVERVOLTAGE LIMIT:** Monitors the output voltage to prevent damage to the load.
- TIMER:** A precision timer circuit that controls the output current.
- 2.5V Reference:** A precision 2.5V reference voltage used for the current source.
- 5V Regulator:** A 5V regulator that provides a stable reference for the current source.
- 50μA Current Source:** A precision 50μA current source used for the current source.
- Output Stage:** The output current (ILIM) flows through a load resistor (RILIM) to a LOAD.

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Enable/Timer

The voltage level of the TIMER pin is compared to an internal source (see the *Block Diagram*). When the level on the pin exceeds V_{ON} , the comparator outputs a low level. This then turns on the voltage ramp circuit, enabling the device's output. This TIMER pin can be configured into one of four different modes of operation listed in Table 1. The TIMER pin was designed to work with most logic families. The TIMER pin will have at least 250mV of hysteresis between V_{ON} and V_{OFF} . It is recommended that any logic gate used to drive the TIMER pin be tested to ensure proper operation.

Once the device has been enabled, there is a delay (t_{POND}) until conduction begins from V_{CC} to $LOAD$. This delay is the time required for the charge pump to bring the gate voltage of the power MOSFET above its threshold level. Once the gate is above the threshold level, conduction begins and the output voltage begins ramping.

Automatic Enable Mode

When V_{CC} exceeds V_{UVLOR} , the gate holding the TIMER node low is released. The internal current source brings the node to a level greater than V_{ON} , enabling the device.

Delayed Automatic Enable Mode

When V_{CC} exceeds V_{UVLOR} , the gate holding the TIMER node low is released. The internal current source (I_{TIMER}) then begins charging C_{TIMER} . When C_{TIMER} is charged to a level greater than V_{ON} , the device turns on. The equation for the delay time is:

$$t_{DELAY} = (C_{TIMER} \times V_{ON}) / I_{TIMER}$$

Enable/Disable Mode

A logic gate or open-collector device can be connected to the TIMER pin to enable or disable the device. When the TIMER pin is held low, the device is disabled. When an open-collector device is used to drive the TIMER pin, the DS4560 is enabled when the open collector is in its high-impedance state by the internal current source bringing the TIMER node high. The TIMER pin is also compatible with most logic families if the out-

put high voltage level of the gate exceeds the V_{ON} level, and the gate can sink the I_{TIMER} current.

Enable with Delay/Disable Mode

An open-collector device is connected in parallel with C_{TIMER} . When the pin is held low, the DS4560 is disabled. When the open-collector driver is high impedance, the internal current source begins to charge C_{TIMER} as in the delayed mode.

Output-Voltage Ramp

The voltage ramp circuit uses an operational amplifier to control the gate bias of the n-channel power MOSFET. When the timer/enable circuit is disabled, a FET is used to keep C_{VRAMP} discharged, which forces the output voltage to GND. Once the enable/timer circuit has been enabled, an internal current source, I_{VRAMP} , begins to charge the external capacitor, C_{VRAMP} , connected to the VRAMP pin. The amplifier controls the gate of the power MOSFET so that the $LOAD$ output voltage divided by two tracks the rising voltage level of C_{VRAMP} . The output voltage continues to ramp until it reaches either the input V_{CC} level or the overvoltage clamp limits. The equation for the output-voltage ramp function is:

$$dV_{LOAD}/dt = 2 \times (I_{VRAMP}/C_{VRAMP})$$

Thermal Shutdown

The DS4560 enters a thermal shutdown state when the temperature of the power MOSFET reaches or exceeds T_{SHDN} , approximately $+135^{\circ}\text{C}$. When T_{SHDN} is exceeded, the thermal-limiting circuitry disables the DS4560 using the enable circuitry. The DS4560 is offered in two different versions: an autoretry version and a latchoff version.

Autoretry Version (DS4560S-AR)

The autoretry version continually monitors the temperature once it has entered thermal shutdown. If the junction temperature falls below approximately $+95^{\circ}\text{C}$ ($T_{SHDN} - T_{HYS}$), the power MOSFET is re-enabled. See the Thermal Shutdown with Autoretry graph for details.

Table 1. TIMER Pin Modes

MODE OF OPERATION	TIMER PIN SETUP
Automatic Enable	No connection to TIMER pin.
Delayed Automatic Enable	Capacitor C_{TIMER} connected to TIMER.
Enable/Disable	Open-collector device.
Enable with Delay/Disable	Open-collector device and C_{TIMER} .

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Latchoff Version (DS4560S-LO)

Once the latchoff version has entered thermal shutdown, it does not attempt to turn back on. The only way to turn this device back on is to cycle the power to the device. When power is reapplied to V_{CC} , the junction temperature needs to be less than T_{SHDN} for the device to be enabled.

Overvoltage Limit

The overvoltage-limiting clamp monitors the VRAMP level compared to an internal voltage reference. When the voltage on VRAMP exceeds $V_{OVC}/2$, the gate voltage of the n-channel power MOSFET is reduced, limiting the voltage on LOAD to V_{OVC} even as V_{CC} increases. If the device is in overvoltage for an extended period of time, the device may overheat and enter thermal shutdown. This is caused by the power created by the voltage drop across the power MOSFET and the load current. See the Thermal Shutdown with Autoretry graph for details.

Applications Information

The GND pin of the DS4560 is also a heat sink for the device. This pin should be connected to a large trace or plane capable of dissipating heat from the device.

Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
8 SO	—	21-0041

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/08	Initial release.	—
1	9/08	In the <i>Recommended Operating Conditions</i> , changed the TIMER Turn-On Voltage (V _{ON}) maximum specification from “V _{CC} + 0.3V” to “5V.”	2
2	5/09	Added the UL certification number to the <i>Features</i> section.	1

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