Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	0.9 0.7	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	3.0	A
Maximum Body Diode Forward Current (Note 6)			I_S	0.8	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	0.45	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	280	°C/W
Total Power Dissipation (Note 6)		P_D	0.89	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	140	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	112	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	100	nA	V_{DS} = 20V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±1.0	µA	V_{GS} = ±4.5V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.45	—	1.0	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	0.35	0.6	Ω	V_{GS} = 4.5V, I_D = 200mA
		—	0.45	0.8		V_{GS} = 2.5V, I_D = 200mA
		—	0.6	1.0		V_{GS} = 1.8V, I_D = 100mA
		—	0.7	1.6		V_{GS} = 1.5V, I_D = 50mA
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	V_{GS} = 0V, I_S = 500mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	52	—	pF	V_{DS} = 16V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	4.8	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	3.1	—	pF	
Gate Resistance	R_g	—	95	—	Ω	V_{DS} = 0V, V_{GS} = 0V
Total Gate Charge	Q_g	—	0.7	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 250mA
Gate-Source Charge	Q_{gs}	—	0.09	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.05	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.7	—	ns	V_{DD} = 10V, V_{GS} = 4.5V, R_L = 47Ω, R_G = 10Ω, I_D = 200mA
Turn-On Rise Time	t_R	—	2.4	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	20.9	—	ns	
Turn-Off Fall Time	t_F	—	5.6	—	ns	

Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

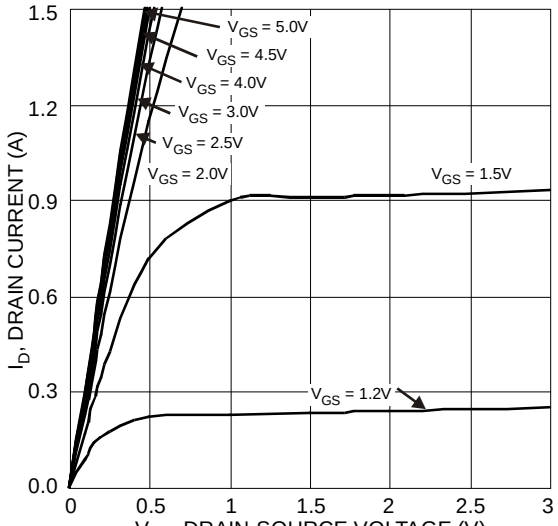


Figure 1 Typical Output Characteristics

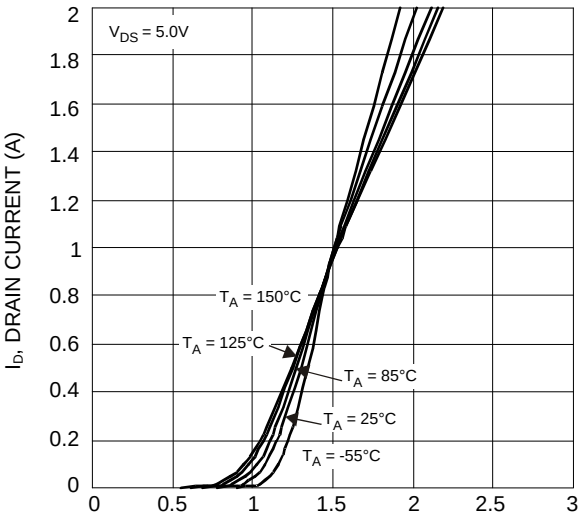


Figure 2 Typical Transfer Characteristics

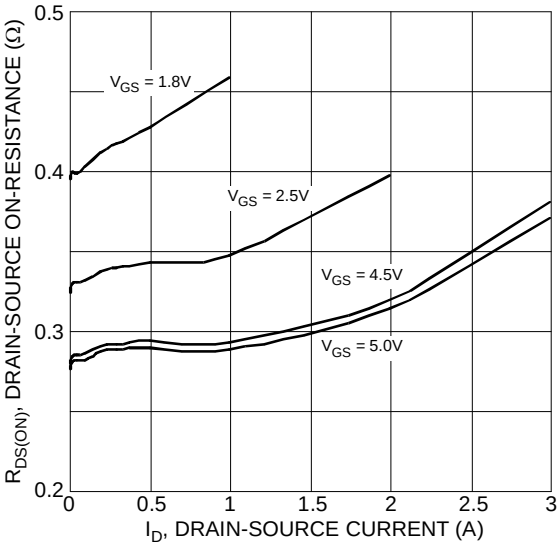


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

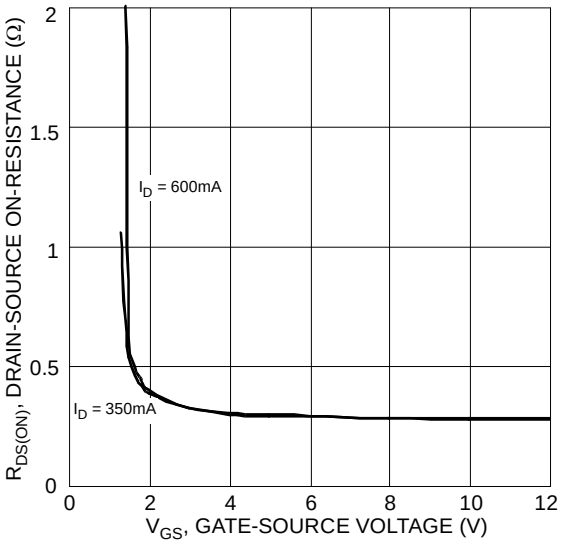


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

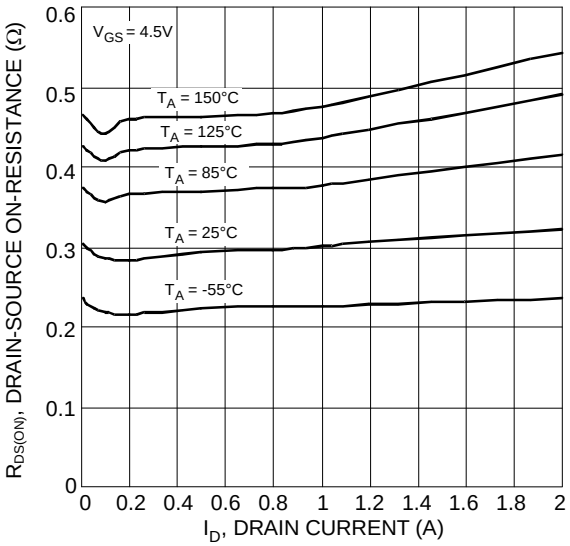


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

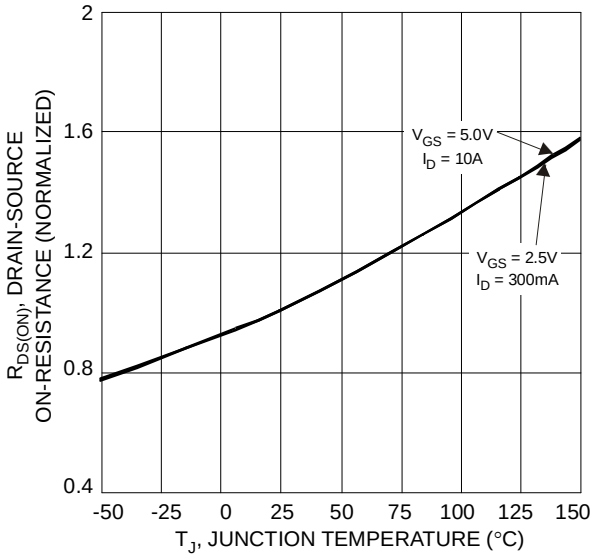


Figure 6 On-Resistance Variation with Temperature

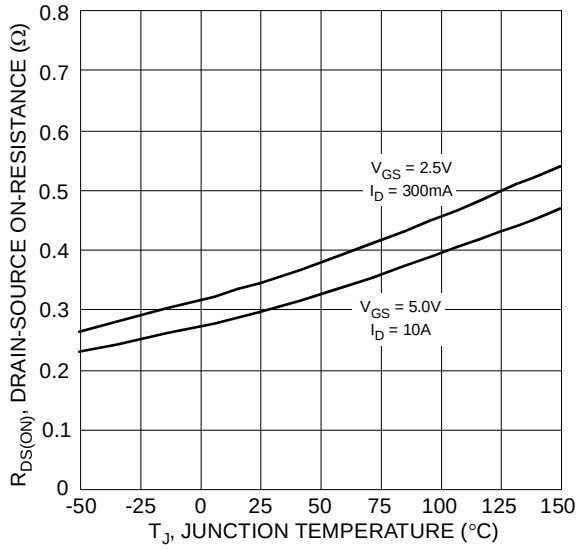


Figure 7 On-Resistance Variation with Temperature

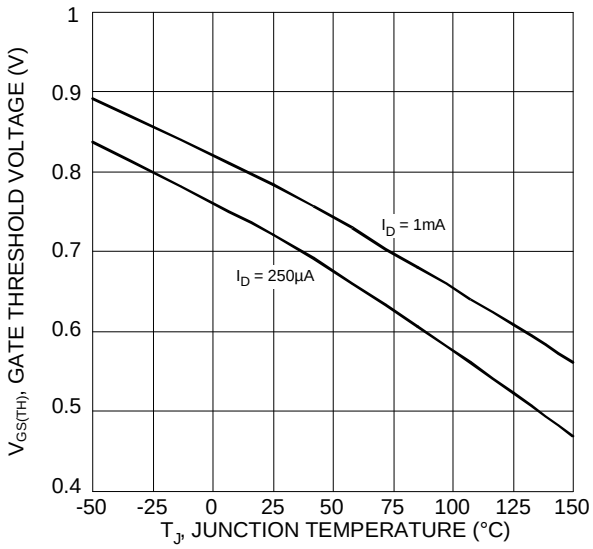


Figure 8 Gate Threshold Variation vs. Junction Temperature

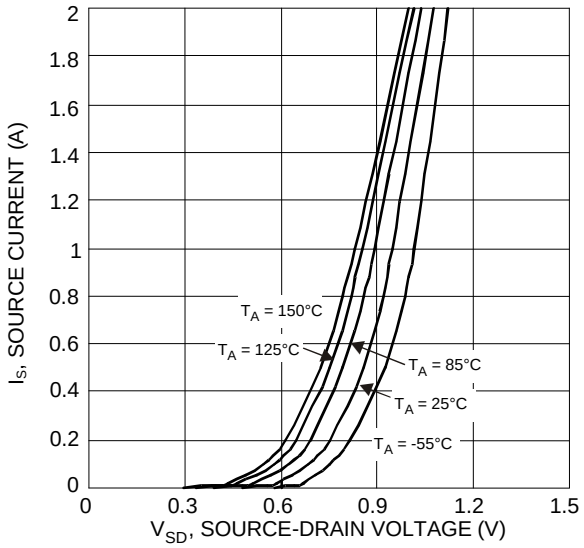


Figure 9 Diode Forward Voltage vs. Current

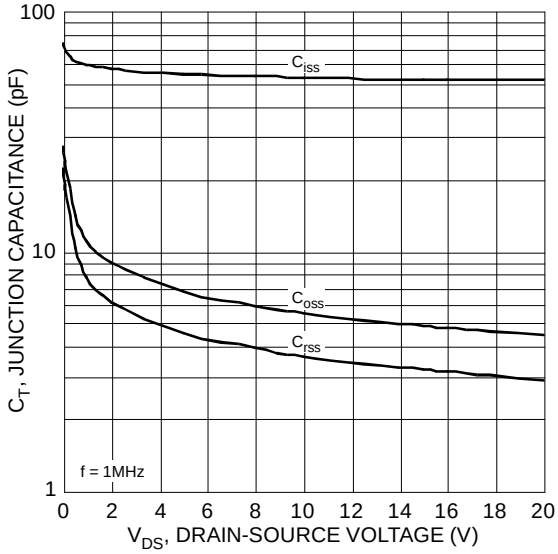


Figure 10 Typical Junction Capacitance

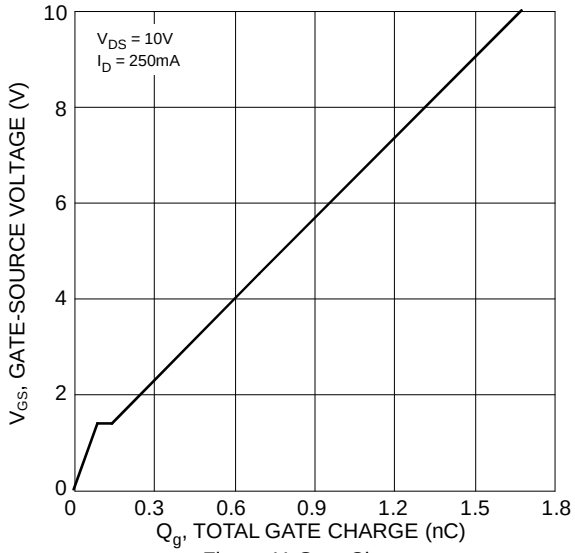


Figure 11 Gate Charge

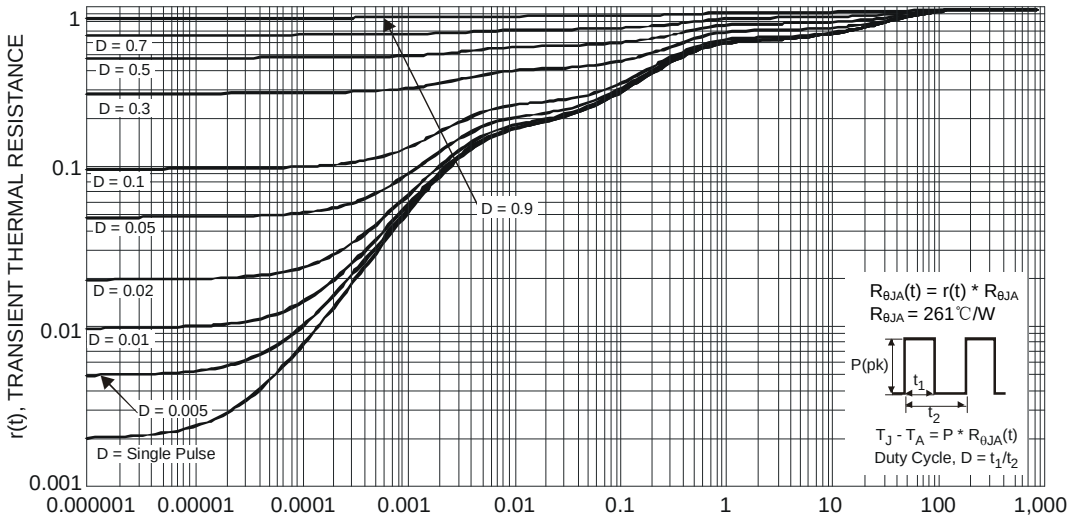
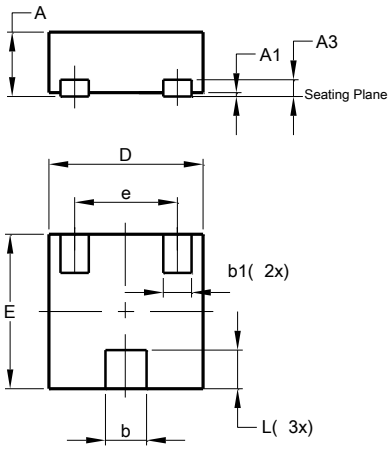


Figure 12 Transient Thermal Response

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1212-3

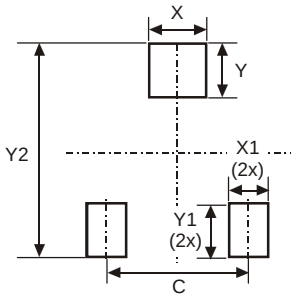


X1-DFN1212-3			
Dim	**Min**	**Max**	**Typ**
A	0.47	0.53	0.50
A1	0	0.05	0.02
A3	-	-	0.13
b	0.27	0.37	0.32
b1	0.17	0.27	0.22
D	1.15	1.25	1.20
E	1.15	1.25	1.20
e	-	-	0.80
L	0.25	0.35	0.30
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1212-3



Dimensions	Value (in mm)
C	0.80
X	0.42
X1	0.32
Y	0.50
Y1	0.50
Y2	1.50

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