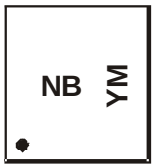
Marking Information

Site 1



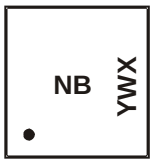
NB = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: H = 2020)
M = Month (ex: 9 = September)

Date Code Key

Year	2013	…	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	A	…	H	I	J	K	L	M	N	O	P	R

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Site 2



NB = Product Type Marking Code
YWX = Date Code Marking
Y = Year (ex: 0 = 2020)
W = Week (ex: a = Week 27; z Represents Week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Key

Year	2013	…	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	3	…	0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	12	V
Gate-Source Voltage			V_{GSS}	±10	V
Continuous Drain Current (Note 5) V_{GS} = 4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	6.9 5.5	A
	t < 5s	T_A = +25°C T_A = +70°C	I_D	8.8 7.0	A
Maximum Continuous Body Diode Forward Current (Note 5)			I_S	1	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	35	A
Avalanche Current (Note 6) L = 0.1mH			I_{AS}	9.8	A
Avalanche Energy (Note 6) L = 0.1mH			E_{AS}	4.8	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	P_D	1.7	W
	t < 5s		2.9	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	71	°C/W
	t < 5s		43	
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	13	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to+ 150	°C

Electrical Characteristics N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	12	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	1.0	µA	V_{DS} = 12V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.4	—	1	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	18	25	mΩ	V_{GS} = 4.5V, I_D = 5.2A
		—	20	30		V_{GS} = 2.5V, I_D = 4.8A
		—	25	38		V_{GS} = 1.8V, I_D = 2.5A
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	V_{GS} = 0V, I_S = 5.4A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	917	—	pF	V_{DS} = 6V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	120	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	102	—	pF	
Gate Resistance	R_g	—	11.4	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	—	12.6	—	nC	V_{DS} = 10V, I_D = 6.8A
Total Gate Charge (V_{GS} = 8V)		—	23.1	—	nC	
Gate-Source Charge	Q_{gs}	—	1.3	—	nC	
Gate-Drain Charge	Q_{gd}	—	1.6	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.0	—	ns	V_{DD} = 6V, V_{GS} = 4.5V, R_L = 1.1Ω, R_g = 1Ω
Turn-On Rise Time	t_R	—	9.3	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	17.2	—	ns	
Turn-Off Fall Time	t_F	—	2.8	—	ns	
Body Diode Reverse Recovery Time	t_{RR}	—	6.8	—	ns	I_S = 5.4A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	1.1	—	nC	I_S = 5.4A, dI/dt = 100A/µs

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

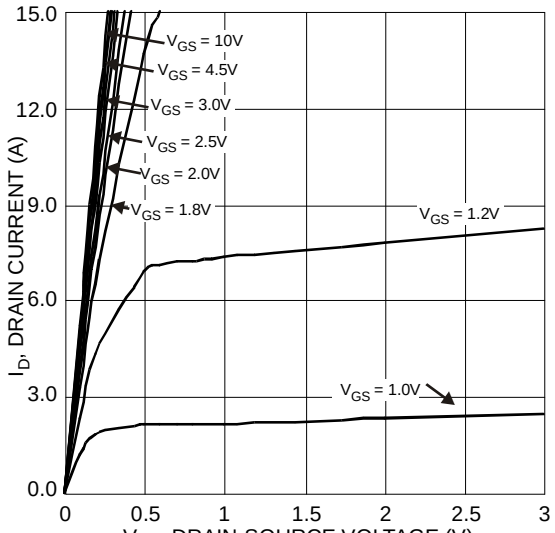


Figure 1 Typical Output Characteristic

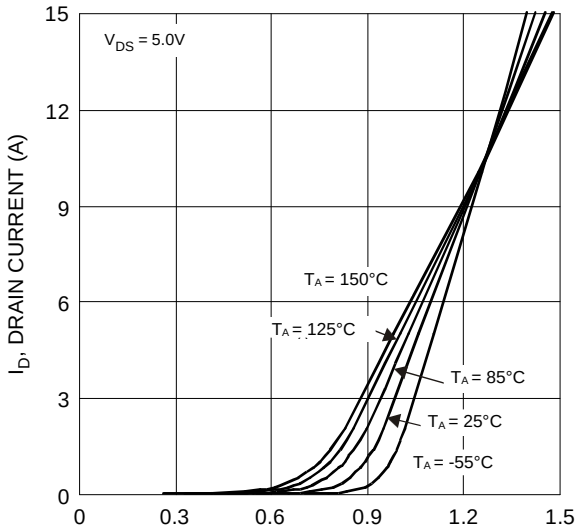


Figure 2 Typical Transfer Characteristics

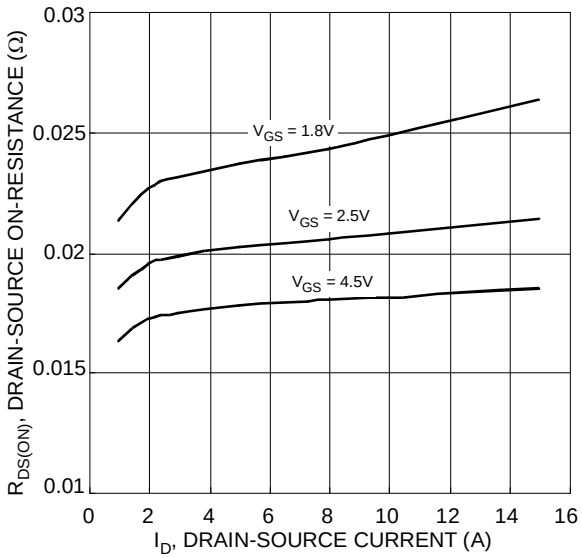


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

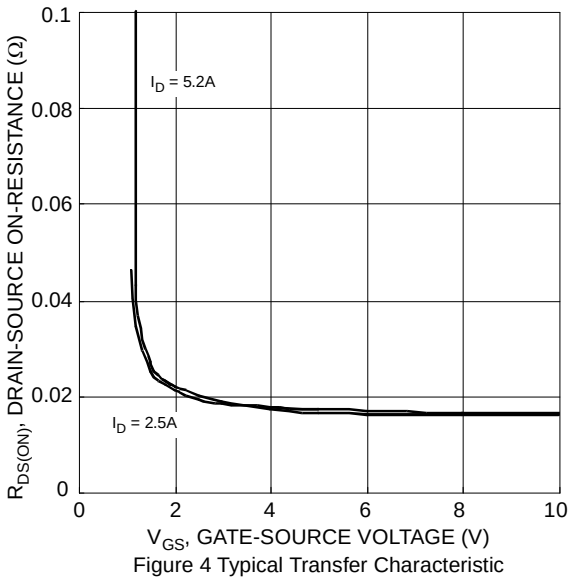


Figure 4 Typical Transfer Characteristic

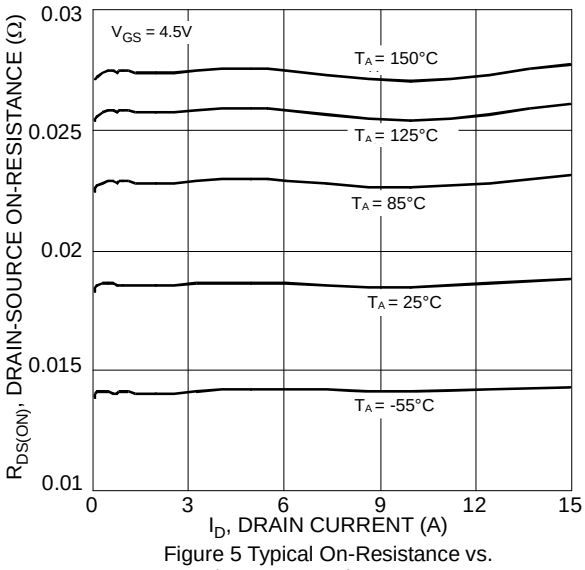


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

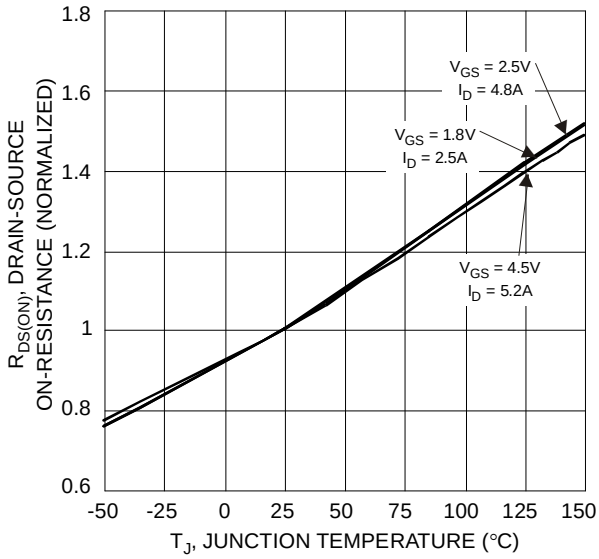


Figure 6 On-Resistance Variation with Temperature

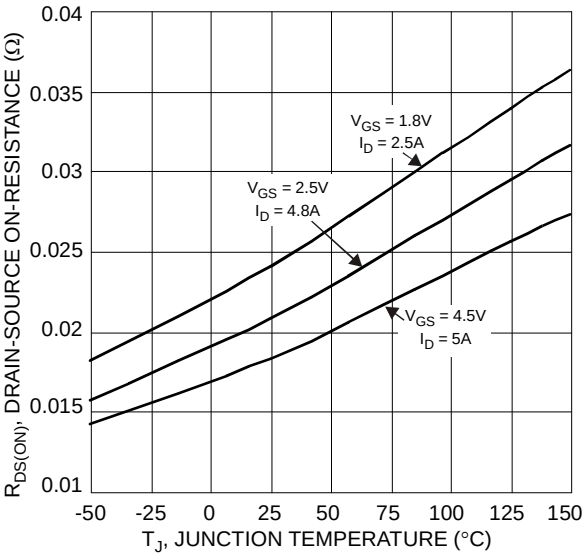


Figure 7 On-Resistance Variation with Temperature

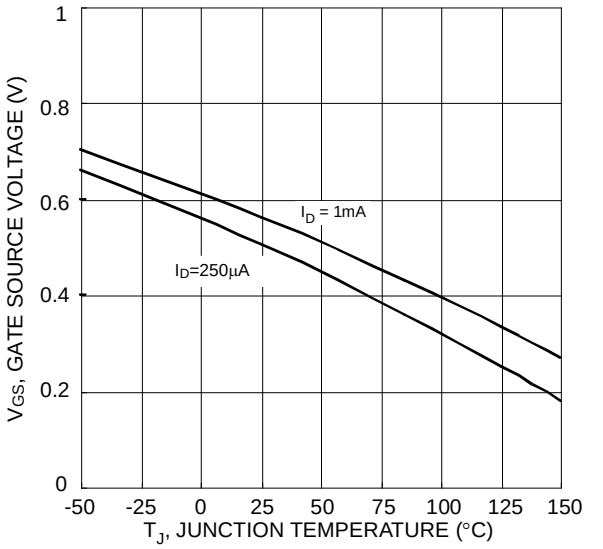


Figure 8 Gate Threshold Variation vs. Junction Temperature

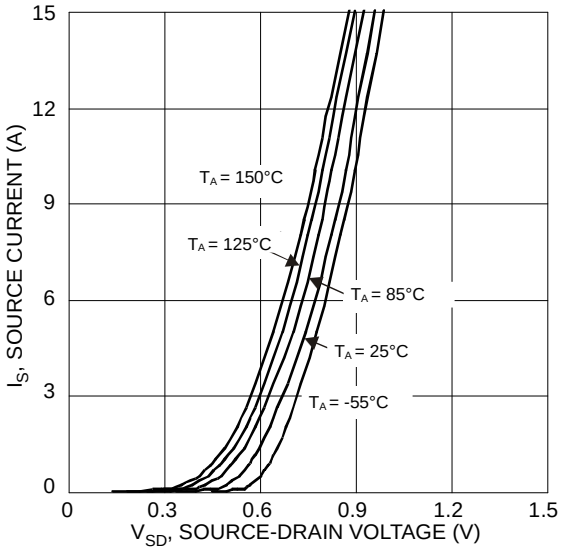


Figure 9 Diode Forward Voltage vs. Current

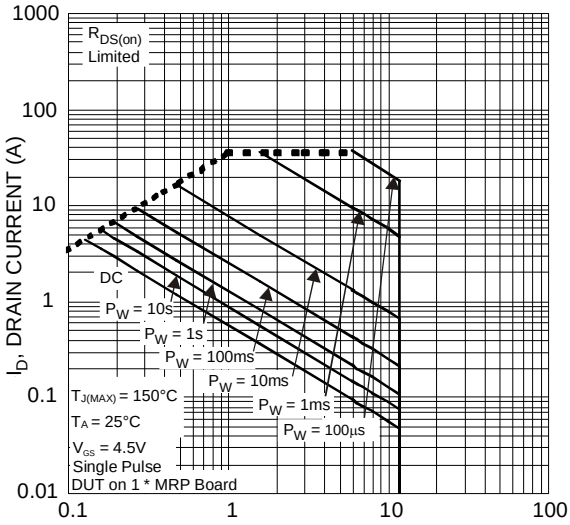


Figure 10 SOA, Safe Operation Area

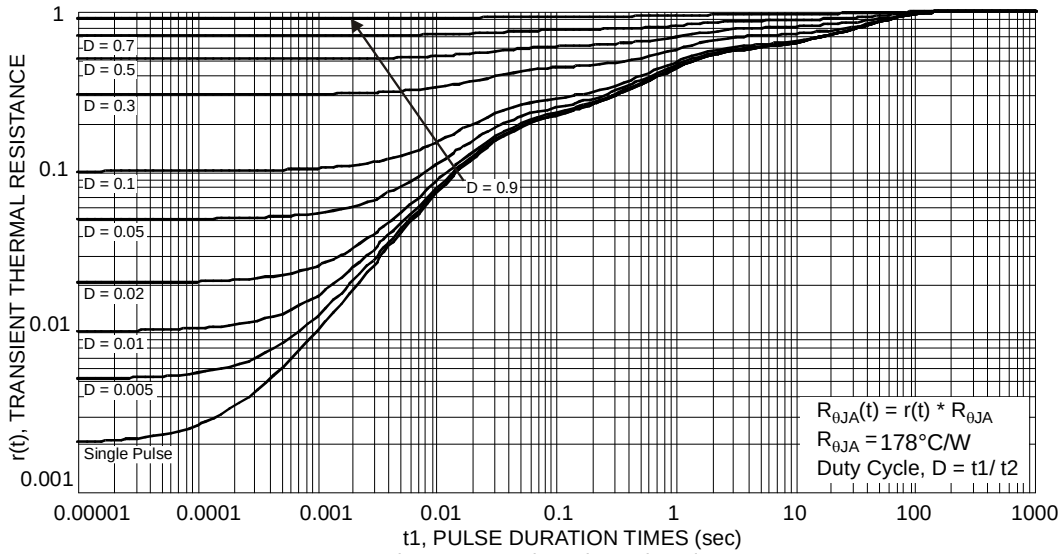
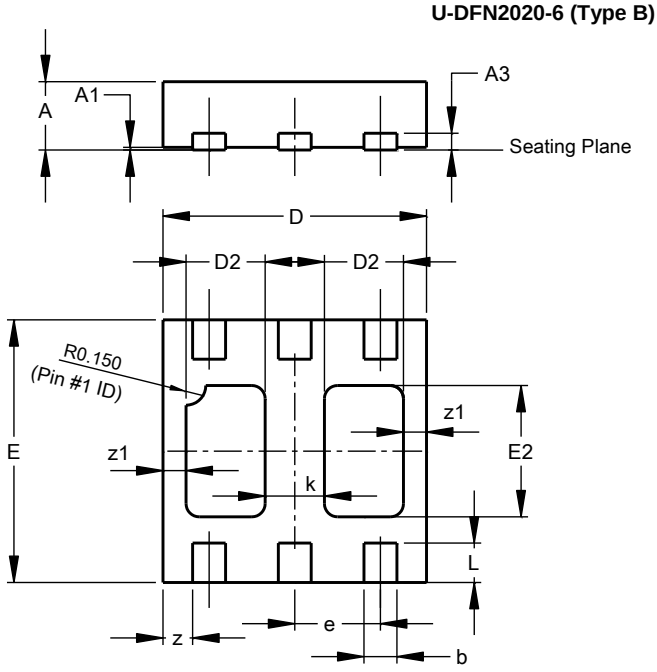


Figure 11 Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)

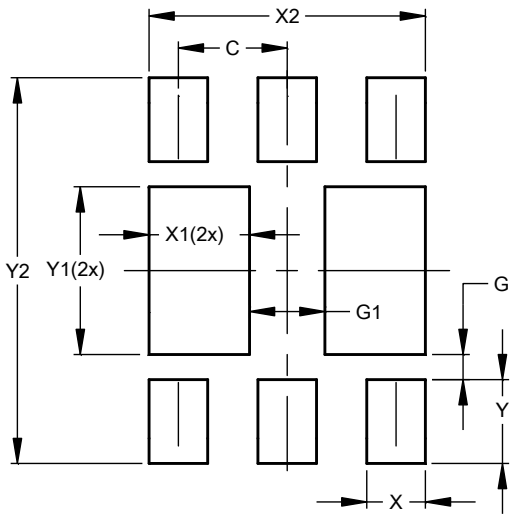


U-DFN2020-6 Type B			
Dim	**Min**	**Max**	**Typ**
A	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
e	-	-	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.175
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
C	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300

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