

Absolute Maximum Ratings (Voltage relative to GND, @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	VIN	-0.3 to 100	V
Continuous Input & Output Current	Iin, Iout	450	mA
Peak Pulsed Input & Output Current	Іім, Іом	2	А
Maximum Voltage applied to V _{OUT}	V _{OUT(max)}	Smaller of V _{IN} +5V or 11V	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Continuous Output Current	(Note 7)	I _{OUT}	50	mA	
Pulsed Output Current	(Note 8)		830	٣A	
	(Note 9)	IOM	170	mA	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissinction	(Note 5)	D	2.3	W
Power Dissipation	(Note 6)	P _D	1.1	vv
Thermal Registeres Junction to Ambient	(Note 5)	Р	44	
Thermal Resistance, Junction to Ambient	(Note 6)	R _{0JA}	90	0000
Thermal Resistance, Junction to Lead	(Note 10)	R _{θJL}	8.4	°C/W
Thermal Resistance, Junction to Case (Note 10)		R _{θJC}	14.6	
Recommended Operating Junction Temperature Range		TJ	-40 to +125	°C
Maximum Operating Junction and Storage Temperature Range		T _{J,} T _{STG}	-65 to +150	°C

ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

Notes: 5. For a device mounted with the exposed V_{IN} pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.

6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.

7. Same as note 5, whilst operating at V_{IN} = 48V. Refer to Safe Operating Area for other Input Voltages.

8. Same as note 5, except measured with a single pulse width = 100 μ s and V_{IN} = 48V.

9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.

10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).

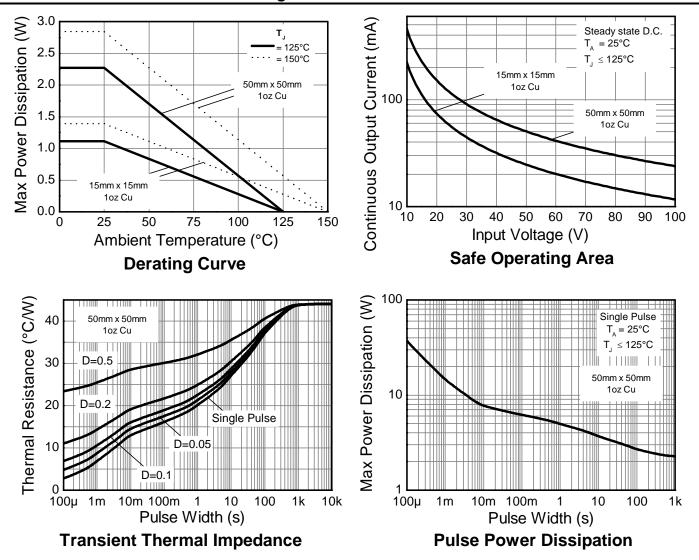
 $R_{\theta,JC}$ = Thermal resistance from junction to the top of case.

11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



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Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	V _{OUT}	4.5	5.0	5.5	V	V _{IN} = 48V, I _{OUT} = 15mA
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	—	195	300	mV	V_{IN} = 10 to 72V, I_{OUT} = 15mA
Temperature Coefficient	$\Delta V_{OUT} / \Delta T$	—	7.0	_	mV/°C	$T_J = -40^{\circ}C \text{ to } +125^{\circ}C$ $V_{IN} = 48V, I_{OUT} = 15\text{mA}$
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	_	-185 -205	-350 -400	mV	$I_{OUT} = 0.1$ to 30mA, $V_{IN} = 48V$ $I_{OUT} = 0.1$ to 100mA, $V_{IN} = 48V$
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	10	_	_	V	_
Quiescent Current	ΙQ	_	260 550	500 900	μA	V _{IN} = 48V, I _{OUT} = 10μA V _{IN} = 100V, I _{OUT} = 10μA
Power Supply Rejection Ratio	$\Delta V_{in} / \Delta V_{out}$	_	45	—	dB	$C_{OUT} = 100$ nF, $I_{OUT} = 15$ mA, $V_{OUT} = 5$ V, $V_{IN} = 10$ to 100V, f = 100Hz

Notes: 12. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%

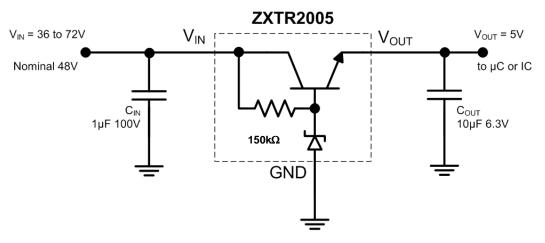
13. Line regulation $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 72V) - V_{OUT} (@V_{IN} = 10V)$

14. Load regulation

 $\Delta VOUT = VOUT (@ IOUT = 30mA) - VOUT (@ IOUT = 0.1mA)$

 $\Delta V_{OUT} = V_{OUT}$ (@ I_{OUT} = 100mA) - V_{OUT} (@ I_{OUT} = 0.1mA)

Typical Application Circuit



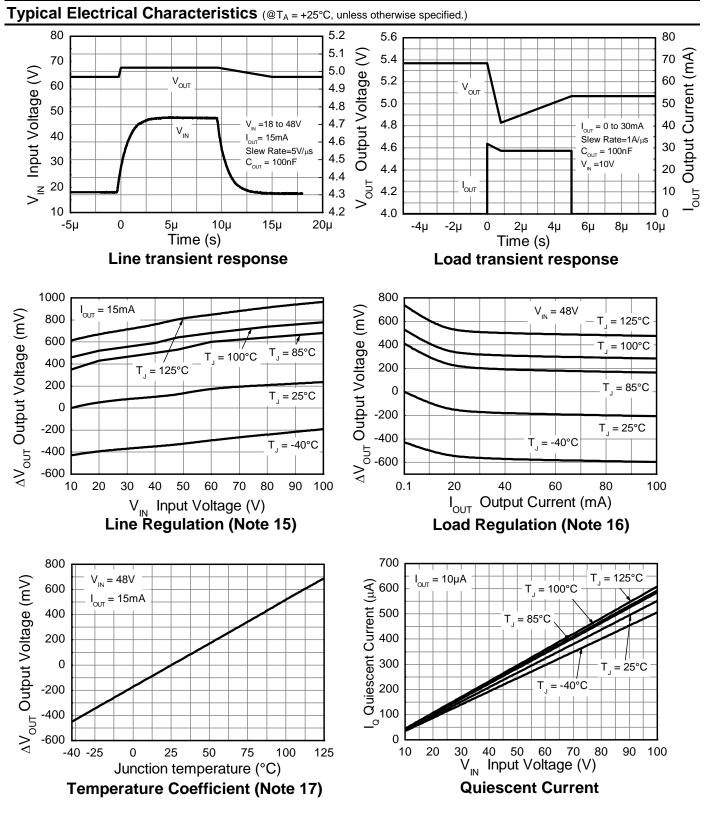
Example of a 5V regulated supply from a nominal 48V for powering a Controller IC.

Pin Functions

Pin Name	Pin Function	Notes
VIN	Input Supply	Input voltage can vary from -0.3V to 100V with respect to GND; for VOUT regulated then $10V \le VIN \le 100V$. It is recommended to connect a 1µF capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
νουτ	Voltage Output	Outputs a regulated 5V when $10V \le VIN \le 100V$. When VIN < 10V, then VOUT maximum = VIN - 1.5V. The pin can be pulled high to a maximum of +11V with respect to GND, or +5V with respect to VIN, whichever is lower. It is recommended to connect a 10μ F capacitor to GND and a minimum of 10μ A to be drawn from VOUT to maintain regulation.



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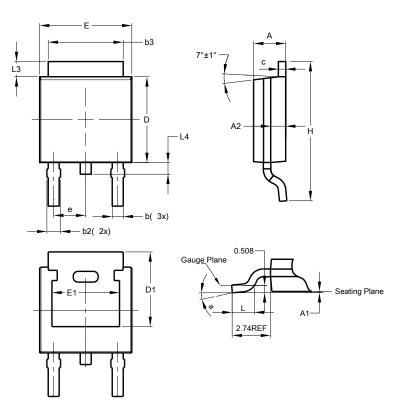


Notes: 15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ V_{IN} = 10V, I_{OUT} = 15mA, T_J = +25°C) 16. Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ V_{IN} = 48V, I_{OUT} = 0.1mA, T_J = +25°C) 17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ V_{IN} = 48V, I_{OUT} = 15mA, T_J = +25°C)



Package Outline Dimensions

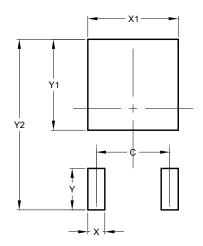
Please see http://www.diodes.com/package-outlines.html for the latest version.



TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Η	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700



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