

**ELECTRICAL / OPTICAL CHARACTERISTICS at T<sub>A</sub>=25°C**

Parameter	Symbol	Emitting Color	Value		Unit
			Typ.	Max.	
Wavelength at Peak Emission I <sub>F</sub> = 10mA	$\lambda_{\text{peak}}$	Green	565	-	nm
Dominant Wavelength I <sub>F</sub> = 10mA	$\lambda_{\text{dom}}$ <sup>[1]</sup>	Green	568	-	nm
Spectral Bandwidth at 50% $\Phi$ REL MAX I <sub>F</sub> = 10mA	$\Delta\lambda$	Green	30	-	nm
Capacitance	C	Green	15	-	pF
Forward Voltage I <sub>F</sub> = 10mA	V <sub>F</sub> <sup>[2]</sup>	Green	2	2.4	V
Reverse Current (V <sub>R</sub> = 5V)	I <sub>R</sub>	Green	-	10	μA
Temperature Coefficient of $\lambda_{\text{peak}}$ I <sub>F</sub> = 10mA, -10°C ≤ T ≤ 85°C	TC <sub><math>\lambda_{\text{peak}}</math></sub>	Green	0.1	-	nm/°C
Temperature Coefficient of $\lambda_{\text{dom}}$ I <sub>F</sub> = 10mA, -10°C ≤ T ≤ 85°C	TC <sub><math>\lambda_{\text{dom}}</math></sub>	Green	0.06	-	nm/°C
Temperature Coefficient of V <sub>F</sub> I <sub>F</sub> = 10mA, -10°C ≤ T ≤ 85°C	TC <sub>V</sub>	Green	-2	-	mV/°C

**Notes:**

1. The dominant wavelength ( $\lambda_d$ ) above is the setup value of the sorting machine. (Tolerance  $\lambda_d$  : ±1nm. )
2. Forward voltage: ±0.1V.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

**ABSOLUTE MAXIMUM RATINGS at T<sub>A</sub>=25°C**

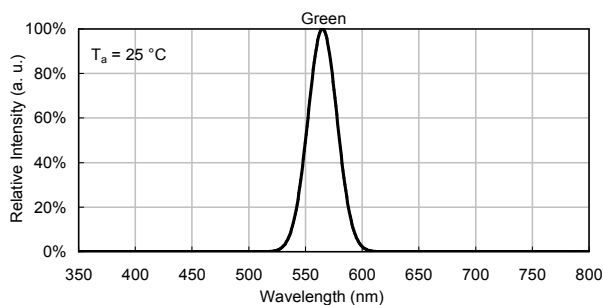
Parameter	Symbol	Value	Unit
Power Dissipation	P <sub>D</sub>	62.5	mW
Reverse Voltage	V <sub>R</sub>	5	V
Junction Temperature	T <sub>j</sub>	110	°C
Operating Temperature	T <sub>op</sub>	-40 to +85	°C
Storage Temperature	T <sub>stg</sub>	-40 to +85	°C
DC Forward Current	I <sub>F</sub>	25	mA
Peak Forward Current	I <sub>FM</sub> <sup>[1]</sup>	140	mA
Electrostatic Discharge Threshold (HBM)	-	8000	V
Thermal Resistance (Junction / Ambient)	R <sub>th JA</sub> <sup>[2]</sup>	540	°C/W
Thermal Resistance (Junction / Solder point)	R <sub>th JS</sub> <sup>[2]</sup>	310	°C/W
Lead Solder Temperature <sup>[3]</sup>		260°C For 3 Seconds	
Lead Solder Temperature <sup>[4]</sup>		260°C For 5 Seconds	

**Notes:**

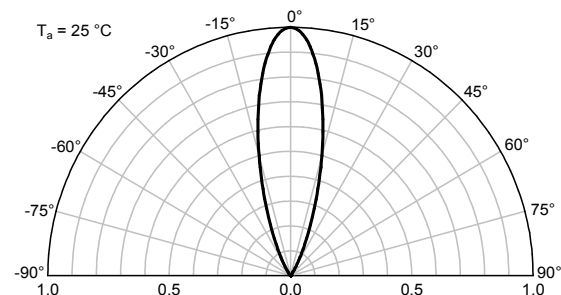
1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. R<sub>th JA</sub>, R<sub>th JS</sub> Results from mounting on PC board FR4 (pad size ≥ 16 mm<sup>2</sup> per pad).
3. 2mm below package base.
4. 5mm below package base.
5. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

## TECHNICAL DATA

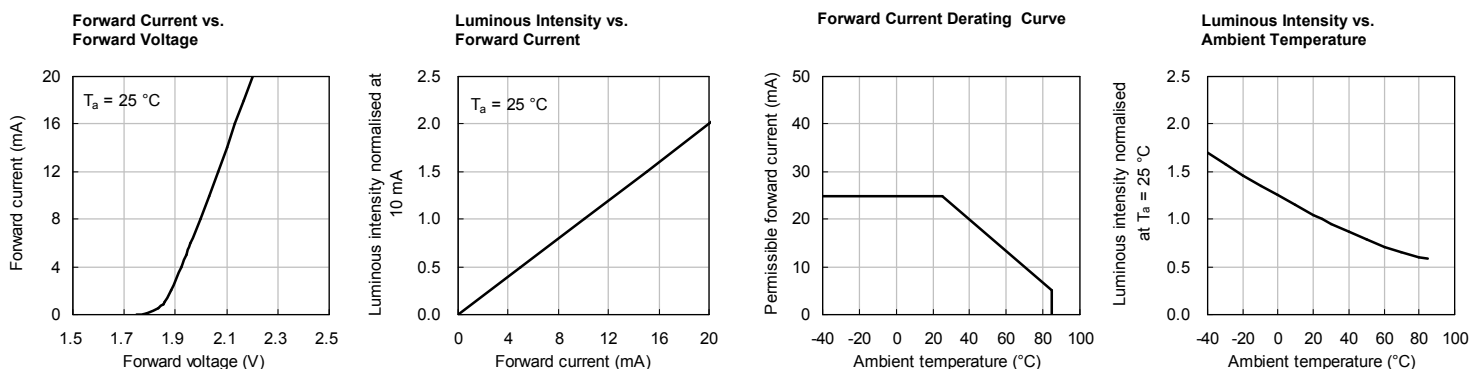
### RELATIVE INTENSITY vs. WAVELENGTH



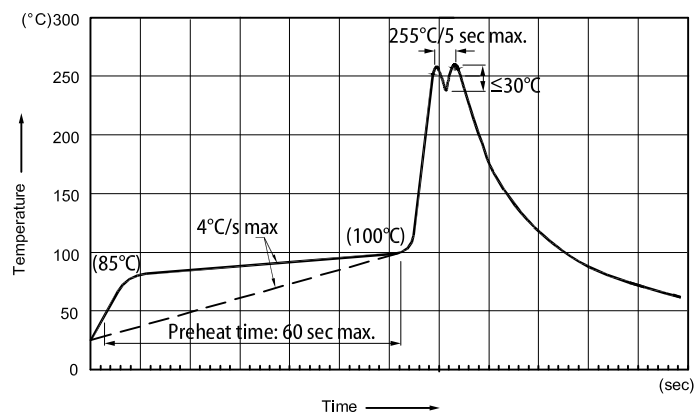
### SPATIAL DISTRIBUTION



## GREEN

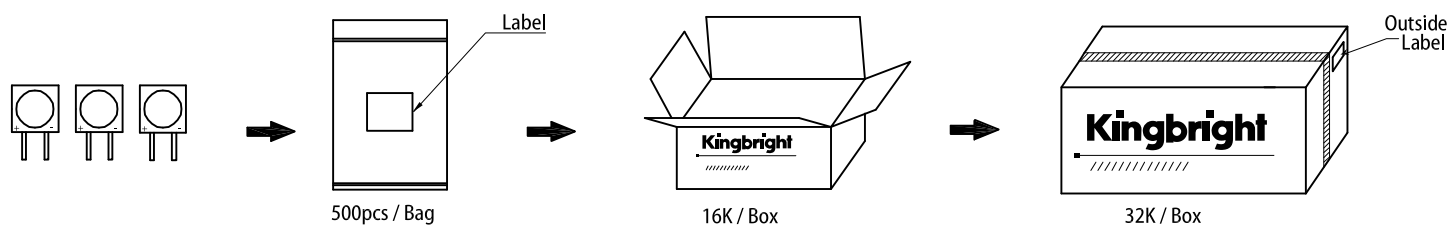


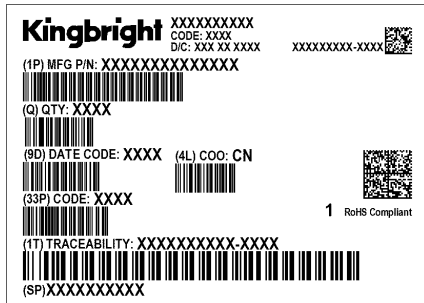
### RECOMMENDED WAVE SOLDERING PROFILE



- Notes:
1. Recommend pre-heat temperature of  $105^\circ\text{C}$  or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of  $260^\circ\text{C}$ .
  2. Peak wave soldering temperature between  $245^\circ\text{C} \sim 255^\circ\text{C}$  for 3 sec (5 sec max).
  3. Do not apply stress to the epoxy resin while the temperature is above  $85^\circ\text{C}$ .
  4. Fixtures should not incur stress on the component when mounting and during soldering process.
  5. SAC 305 solder alloy is recommended.
  6. No more than one wave soldering pass.

### PACKING & LABEL SPECIFICATIONS





## PRECAUTIONS

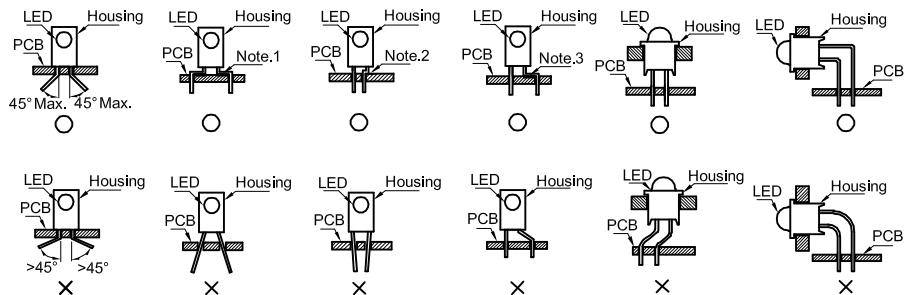
### Storage Conditions

1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
2. LEDs should be stored with temperature  $\leq 30^{\circ}\text{C}$  and relative humidity  $< 60\%$ .
3. Product in the original sealed package is recommended to be assembled within 72 hours of opening.  
Product in opened package for more than a week should be baked for 30 (+10/-0) hours at  $85 \sim 100^{\circ}\text{C}$ .

### LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.  
Lead-forming may be required to insure the lead pitch matches the hole pitch.  
Refer to the figure below for proper lead forming procedures.

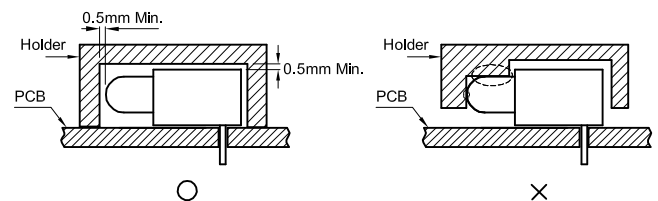
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.



" O " Correct mounting method " X " Incorrect mounting method

### Lead Forming Procedures

1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
2. The tip of the soldering iron should never touch the lens epoxy.
3. Through-hole LEDs are incompatible with reflow soldering.
4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



## PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
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