

ABSOLUTE MAXIMUM RATINGS¹

(T_A = +25°C unless otherwise specified)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V _{CC}	Supply Voltage ²	V	3.6
I _{CC}	Circuit Current	mA	15
P _D	Power Dissipation ³	mW	270
T _A	Operating Ambient Temperature	°C	-40 to +85
T _{STG}	Storage Temperature	°C	-55 to +150
P _{IN}	Input Power	dBm	+5

Notes:

- Operation in excess of any one of these conditions may result in permanent damage.
- Pins 4 and 6.
- Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB, T_A = +85°C.

RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
V _{CC}	Supply Voltage ¹	V	2.4	3.0	3.3
T _A	Operating Ambient Temperature	°C	-40	+25	+85

Note:

- Same voltage applied to pins 4 and 6.

SERIES PRODUCTS¹ (T_A = +25°C, V_{CC} = V_{out} = 3.0 V, Z_S = Z_L = 50 Ω)

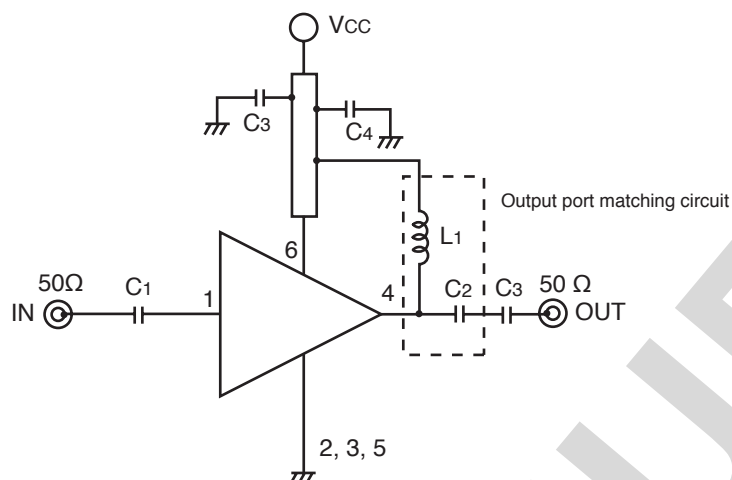
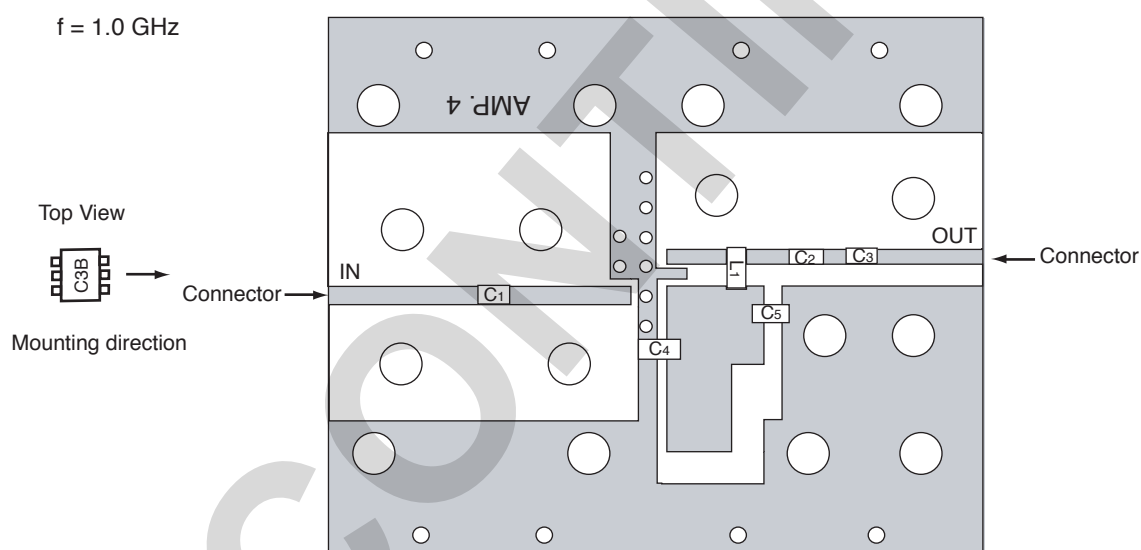
Parameter Part No.	I _{CC} (mA)	1.0 GHz output port matching frequency			1.66 GHz output port matching frequency			1.9 GHz output port matching frequency			2.4 GHz output port matching frequency			Marking
		G _P (dB)	ISOL (dB)	P _O (1 dB) (dBm)	G _P (dB)	ISOL (dB)	P _O (1 dB) (dBm)	G _P (dB)	ISOL (dB)	P _O (1 dB) (dBm)	G _P (dB)	ISOL (dB)	P _O (1 dB) (dBm)	
UPC8178TB	1.9	11	39	-4.0	—	—	—	11.5	40	-7.0	11.5	38	-7.5	C3B
UPC8179TB	4.0	13.5	44	+3.0	—	—	—	15.5	42	+1.5	15.5	41	+1.0	C3C
UPC8128TB	2.8	12.5	39	-4.0	13	39	-4.0	13	37	-4.0	—	—	—	C2P
UPC8151TB	4.2	12.5	38	+2.5	15	36	+1.5	15	34	+0.5	—	—	—	C2U
UPC8152TB	5.6	23	40	-4.5	19.5	38	-8.5	17.5	35	-8.5	—	—	—	C2V

Note:

- Typical performance.

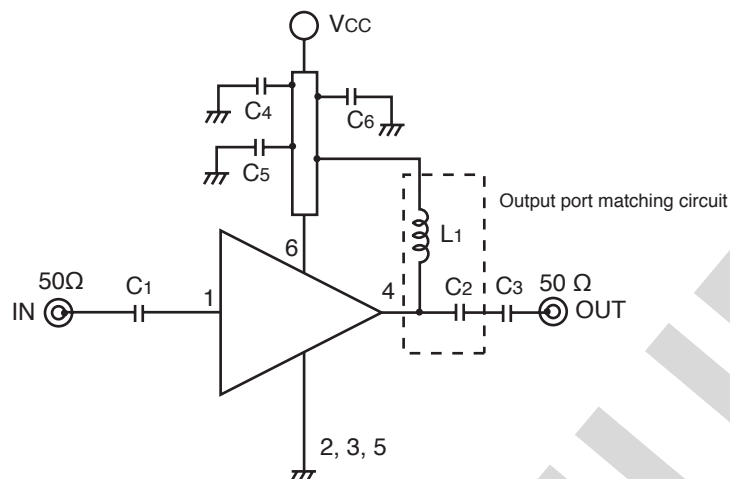
PIN FUNCTIONS (Pin Voltage is measured at V_{CC} = 3.0 V)

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)	Function and Applications	Internal Equivalent Circuit
1	Input	—	0.91	Signal input pin. An internal matching circuit, configured with resistors, enables a 50 Ω connection over a wide band. This pin must be coupled to the signal source with the capacitor for DC out.	
2 3 5	GND	0	—	Ground pin. This pin should be connected to the system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All the ground pins must be connected together with a wide ground pattern to decrease impedance difference.	
4	Output	Voltage same as V _{CC} through external inductor	—	Signal output pin. This pin is designed as the collector output. Due to the high impedance output, this pin should be externally equipped with LC matching circuit to next stage. For L, a size 1005 chip inductor can be used.	
6	V _{CC}	2.4 to 3.3	—	Power supply pin. This pin should be externally equipped with a bypass capacitor to minimize its impedance.	

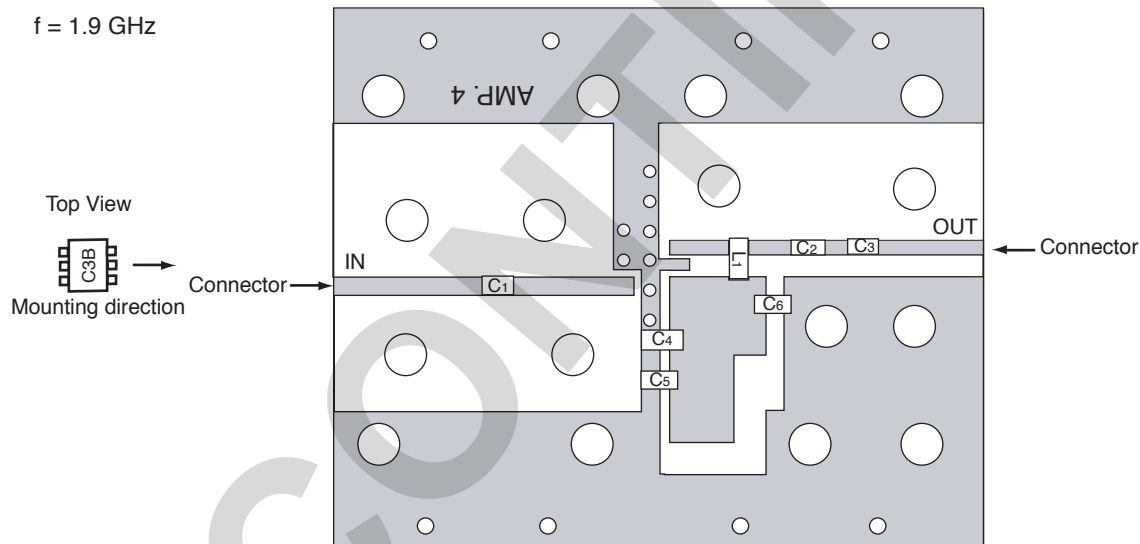
TEST CIRCUIT 1 ($f = 1.0 \text{ GHz}$)**EXAMPLE OF TEST CIRCUIT 1 ASSEMBLED ON EVALUATION BOARD****COMPONENT LIST**

	1.0 GHz Output Port Matching
C1, C3, C5	1000 pF
C2	0.75 pF
C4	10 pF
L1	12 nH

TEST CIRCUIT 2 (f = 1.9 GHz)



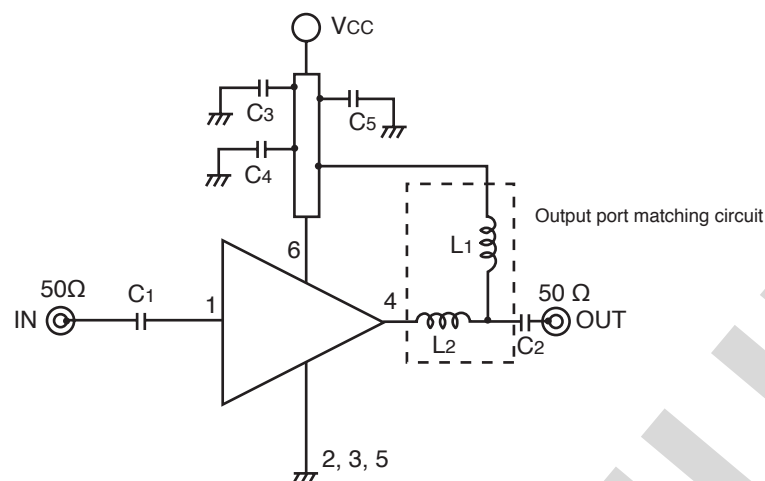
EXAMPLE OF TEST CIRCUIT 2 ASSEMBLED ON EVALUATION BOARD



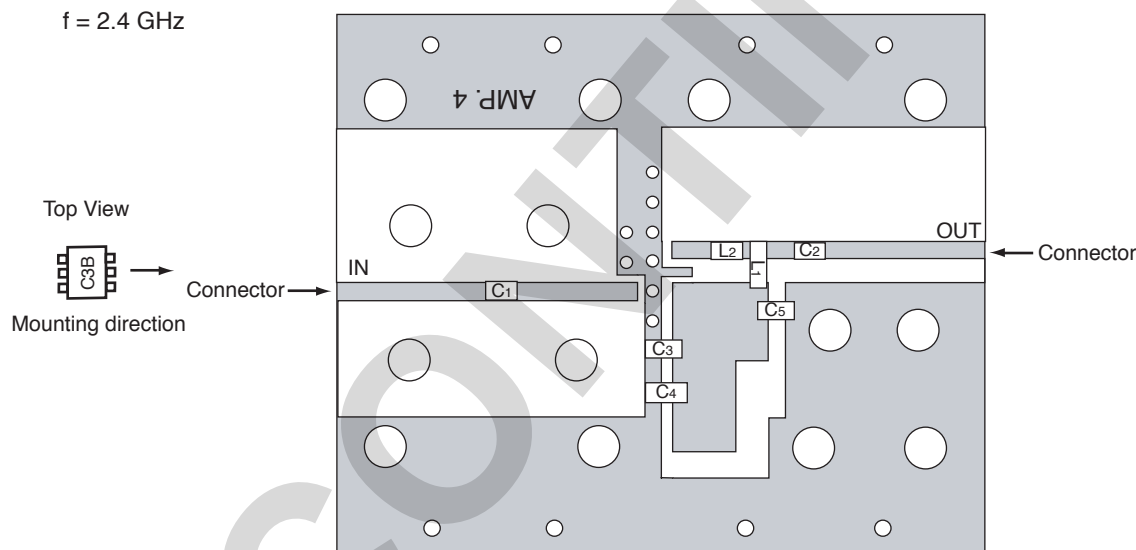
COMPONENT LIST

	1.9 GHz Output Port Matching
C1, C3, C5, C6	1000 pF
C2	0.5 pF
C4	10 pF
L1	3.9 nH

TEST CIRCUIT 3 (f = 2.4 GHz)



EXAMPLE OF TEST CIRCUIT 3 ASSEMBLED ON EVALUATION BOARD

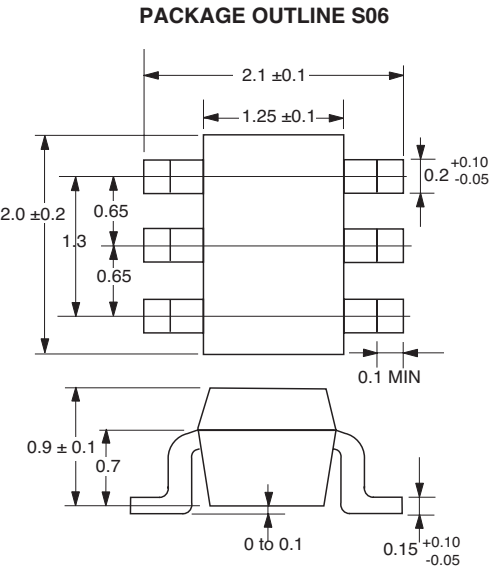


COMPONENT LIST

	2.4 GHz Output Port Matching
C1, C3, C4, C5	1000 pF
C3	10 pF
L1	1.8 nH
L2	2.7 nH

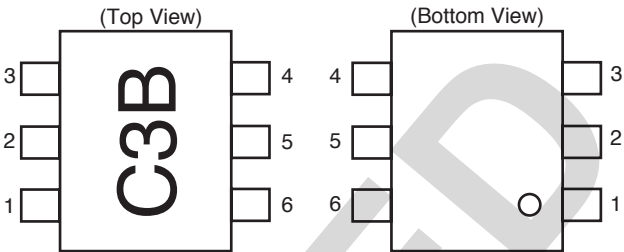
- NOTES:
1. 42 x 35 x 0.4 mm double sided copper clad polyimide board.
 2. Solder plated on pattern.
 3. Back side: GND pattern.
 4. ○ Through holes.

OUTLINE DIMENSIONS (Units in mm)



Note:
All dimensions are typical unless otherwise specified.

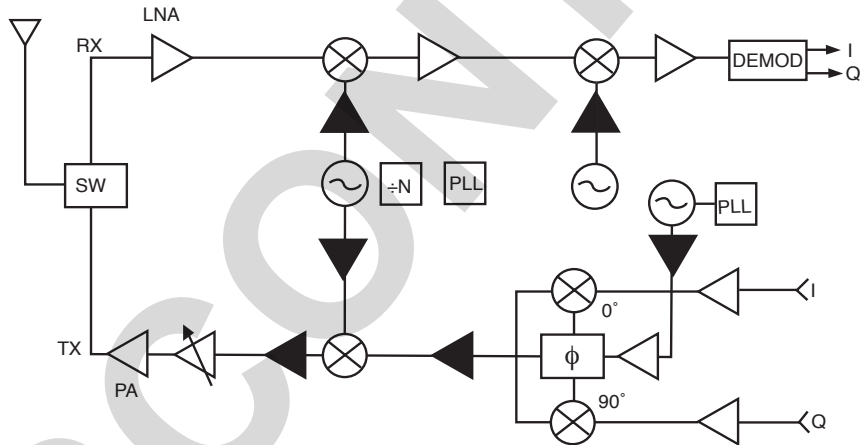
PIN CONNECTIONS



PIN NO.	PIN NAME
1	Input
2	GND
3	GND
4	Output
5	GND
6	Vcc

SYSTEM APPLICATION EXAMPLE

Location examples in digital cellular



ORDERING INFORMATION

Part Number	Quantity
UPC8178TB-E3-A	3 K pcs/reel

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

Note: Embossed tape, 8 mm wide. Pins 1, 2 and 3 face the tape perforation side.

Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
		-A	-AZ
Lead (Pb)	< 1000 PPM	Not Detected	(*)
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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