### Vishay Semiconductors



### **Absolute Maximum Ratings**

 $T_{amb} = 25^{\circ}C$ 

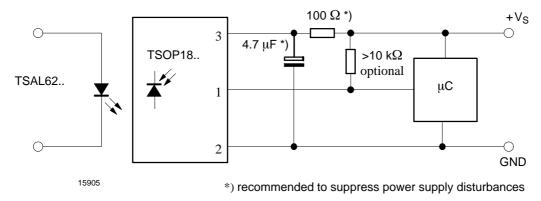
Parameter	Test Conditions	Symbol	Value	Unit
Supply Voltage	(Pin 3)	Vs	-0.36.0	V
Supply Current	(Pin 3)	I <sub>S</sub>	5	mA
Output Voltage	(Pin 1)	Vo	-0.36.0	V
Output Current	(Pin 1)	l <sub>o</sub>	5	mA
Junction Temperature		Τ <sub>i</sub>	100	°C
Storage Temperature Range		T <sub>stq</sub>	-25+85	°C
Operating Temperature Range		T <sub>amb</sub>	-25+85	°C
Power Consumption	$(T_{amb} \leq 85 \ ^{\circ}C)$	P <sub>tot</sub>	50	mW
Soldering Temperature	$t \leq 10$ s, 1 mm from case	T <sub>sd</sub>	260	°C

### **Basic Characteristics**

 $T_{amb} = 25^{\circ}C$ 

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
Supply Current (Pin 3)	$V_{S} = 5 V, E_{v} = 0$	I <sub>SD</sub>	0.9	1.2	1.5	mA
	$V_{S} = 5 V, E_{v} = 40 klx, sunlight$	I <sub>SH</sub>		1.3		
Supply Voltage (Pin 3)		VS	4.5		5.5	V
Transmission Distance	$E_v = 0$ , test signal see fig.6, IR diode TSAL6200, I <sub>F</sub> = 300 mA	d		35		m
Output Voltage Low (Pin 1)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2, \text{ f} = f_o$	V <sub>OSL</sub>			250	mV
Irradiance (30 – 40 kHz)	Pulse width tolerance: $t_{pi} - 4/f_0 < t_{po} <$	E		0.3	0.5	—  m\///m∠
Irradiance (56 kHz)	t <sub>pi</sub> + 6/f <sub>o</sub> , test signal see fig.6	E <sub>e min</sub>		0.4	0.7	
Irradiance		E <sub>e max</sub>	30			W/m <sup>2</sup>
Directivity	Angle of half transmission distance	Φ1/2		±45		deg

## **Application Circuit**





#### **Suitable Data Format**

The circuit of the TSOP18.. is designed in that way that unexpected output pulses due to noise or disturbance signals are avoided. A bandpassfilter, an integrator stage and an automatic gain control are used to suppress such disturbances.

The distinguishing mark between data signal (not suppressed) and disturbance signal (supressed) are carrier frequency, burst length and Signal Gap Time (see diagram below).

The data signal should fullfill the following condition:

• Carrier frequency should be close to center frequency of the bandpass (e.g. 38kHz).

• Burst length should be 6 cycles/burst or longer.

• After each burst a gap time of at least 9 cycles is neccessary.

• The data format should not make a continuous signal transmission. There must be a Signal Gap Time (longer than 15ms) at least each 90ms (see Figure A).

Some examples for suitable data format are: NEC Code (repetitive pulse), NEC Code (repetitive data), Toshiba Micom Format, Sharp Code, RC5 Code, RECS–80 Code, R–2000 Code.

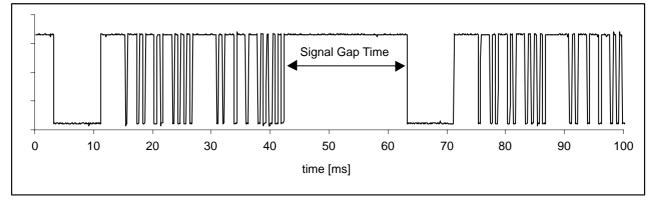
When a disturbance signal is applied to the TSOP18.. it can still receive the data signal. However the sensitivity is reduced to that level that no unexpected pulses will occure.

Some examples for such disturbance signals which are suppressed by the TSOP18.. are:

• DC light (e.g. from tungsten bulb or sunlight),

• Continuous signal at 38kHz or at any other frequency,

- Signals from fluorescent lamps (see Figure B).
- Continuous IR signal (e.g. 1ms burst, 2ms pause)



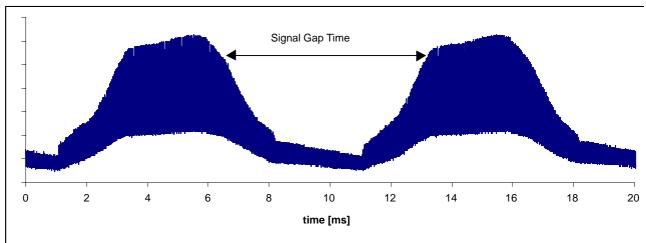


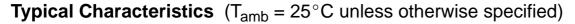
Figure A: Data Signal (Output of IR Receiver) with a Signal Gap Time of 20ms

Figure B: Disturbance Signal from Fluorescent Lamp with Signal Gap Time of 7ms

# TSOP18..



#### **Vishay Semiconductors**



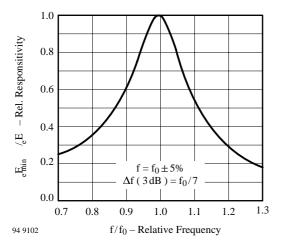


Figure 1. Frequency Dependence of Responsivity

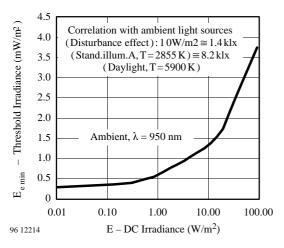


Figure 2. Sensitivity in Bright Ambient

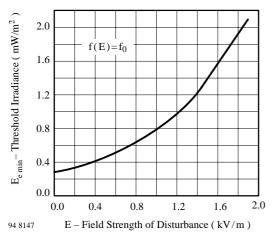


Figure 3. Sensitivity vs. Electric Field Disturbances

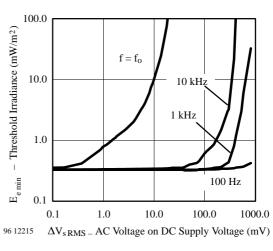
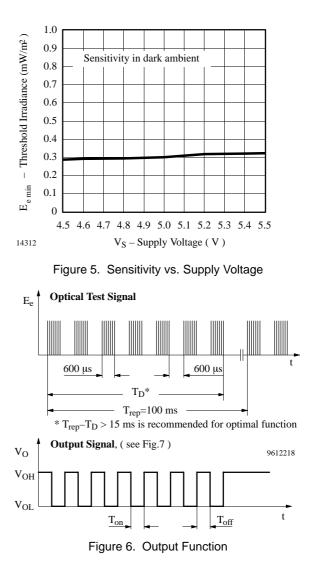
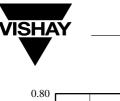
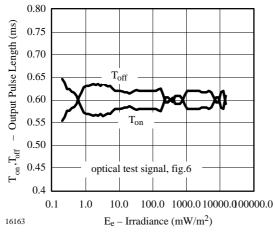


Figure 4. Sensitivity vs. Supply Voltage Disturbances





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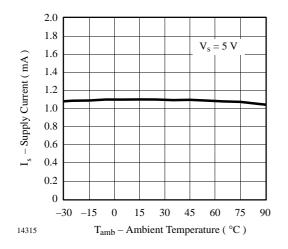


Figure 8. Supply Current vs. Ambient Temperature

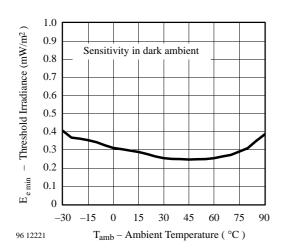
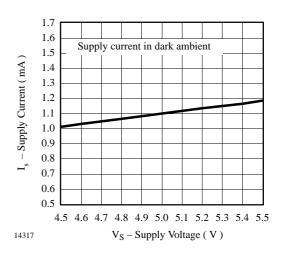


Figure 9. Sensitivity vs. Ambient Temperature





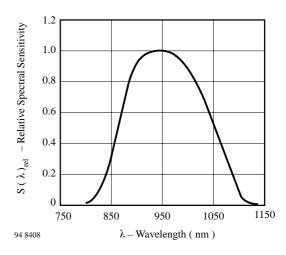


Figure 11. Relative Spectral Sensitivity vs. Wavelength

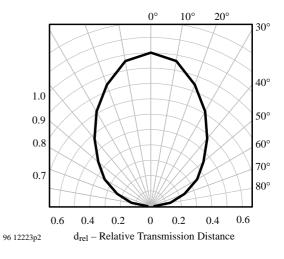


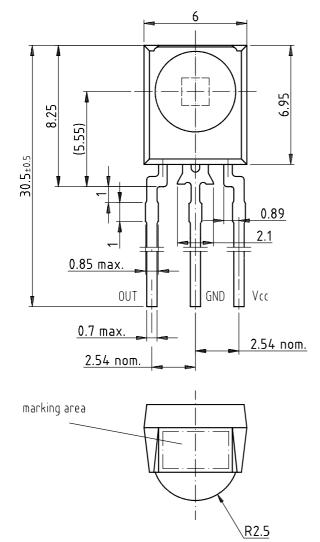
Figure 12. Directivity

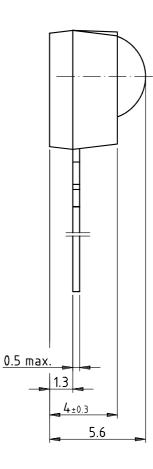
# TSOP18..

### Vishay Semiconductors



### **Dimensions in mm**





Not indicated tolerances ±0.2

9612211



technical drawings according to DIN specifications

www.vishay.com 6 (7)





### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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