Vishay Siliconix

MOSFET SPECIFICATIONS	Ĵ.		Min	Tree	Max	Unit
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static			r	-	1	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.0		3.0	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	1			
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 70 °C		10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			А
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 15 \text{ A}$			0	
		$V_{GS} = 4.5 \text{ V}, I_D = 12 \text{ A}$			0.0125	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		56		S
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.8 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V
Dynamic ^b						
Total Gate Charge	Qg			12	18	nC
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 15 A		5.9		
Gate-Drain Charge	Q _{gd}			4.0		
Gate Resistance	R _g		0.8	1.7	2.5	Ω
Turn-On Delay Time	t _{d(on)}			10	15	
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		13	20	ns
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong \text{1}$ A, V_GEN = 10 V, R_g = 6 Ω		45	70	
Fall Time	t _f			13	20	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.8 A, dI/dt = 100 A/μs		25	50	

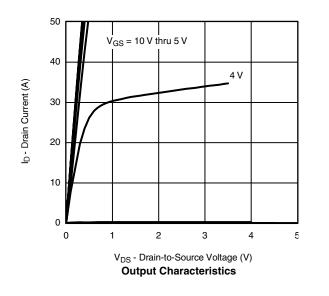
Notes:

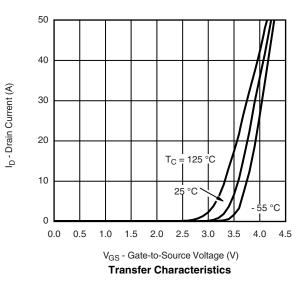
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

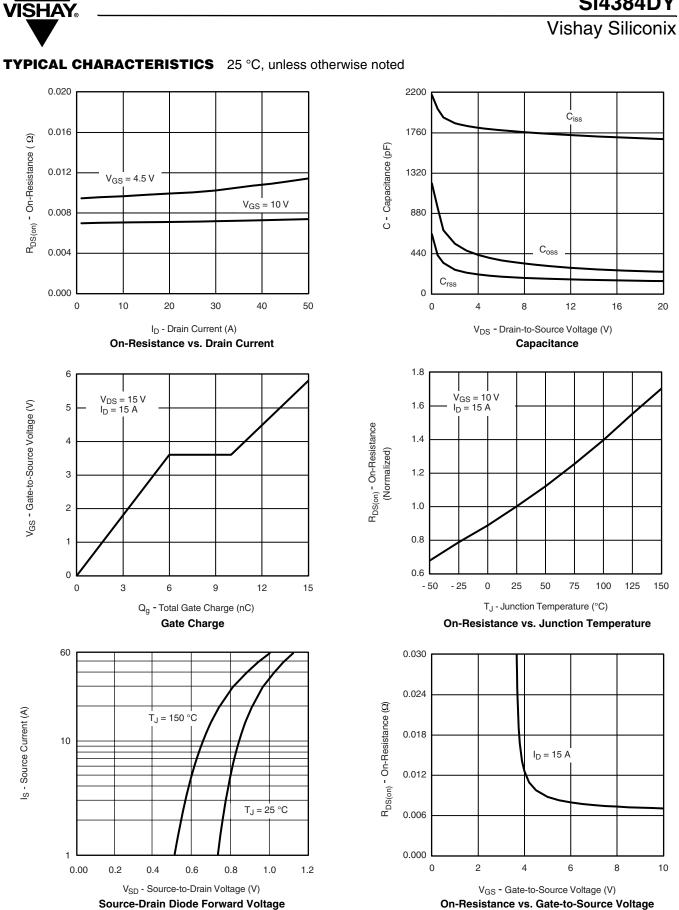
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





www.vishay.com 2





Document Number: 72645 S09-0226-Rev. C, 09-Feb09 Si4384DY

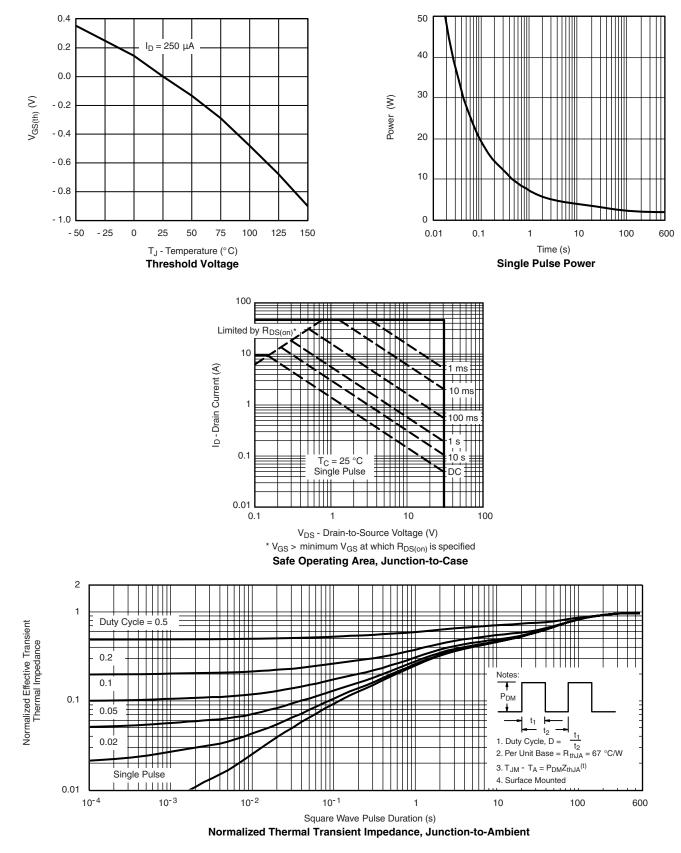
Si4384DY

Vishay Siliconix



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

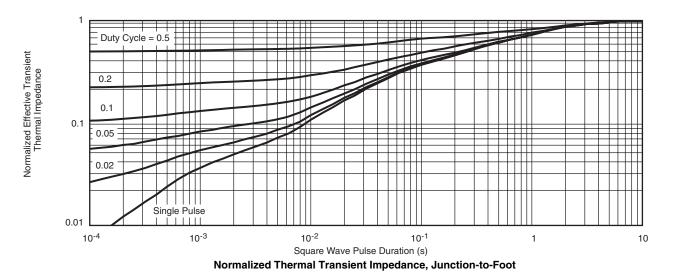


www.vishay.com 4



Si4384DY Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



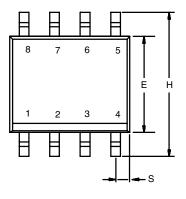
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72645.



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





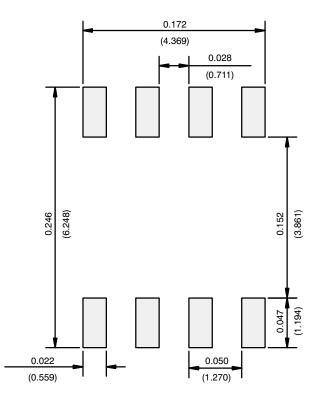
	MILLIM	IETERS	INCHES				
DIM	Min	Мах	Min	Max			
A	1.35	1.75	0.053	0.069			
A ₁	0.10	0.20	0.004	0.008			
В	0.35	0.51	0.014	0.020			
С	0.19	0.25	0.0075	0.010			
D	4.80	5.00	0.189	0.196			
E	3.80	4.00	0.150	0.157			
е	1.27 BSC		0.050 BSC				
н	5.80	6.20	0.228	0.244			
h	0.25	0.50	0.010	0.020			
L	0.50	0.93	0.020	0.037			
q	0°	8°	0°	8°			
S	0.44	0.64	0.018	0.026			
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498							

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

www.vishay.com 22



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