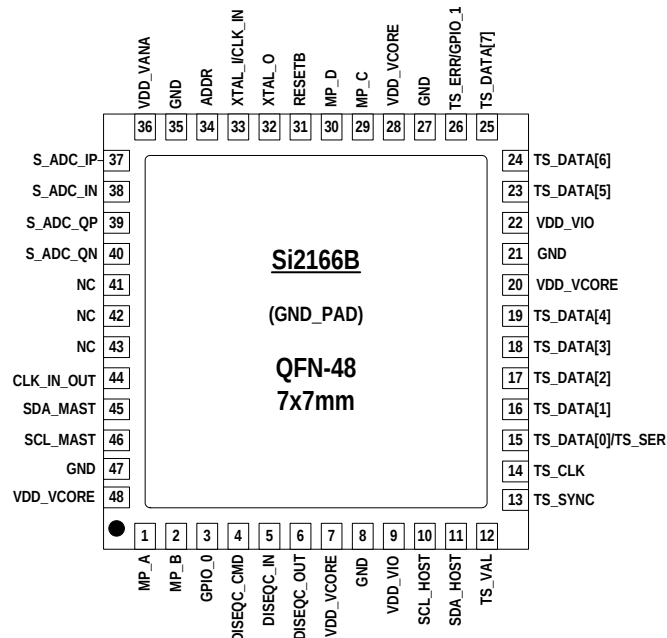


Selected Electrical Specifications

(T_A = -10 to 75 °C)

Parameter	Test Condition	Min	Typ	Max	Unit
General					
Input clock reference		4	—	30	MHz
Supported XTAL frequency		16	—	30	MHz
Total power consumption	DVB-S ¹	—	230	—	mW
	DVB-S2 ²	—	465	—	mW
Thermal resistance, θ_{JA}	2 layer PCB	—	32	—	°C/W
	4 layer PCB	—	23	—	°C/W
Input ADC Sampling Clock					
Satellite DVB-S	45 MBaud	91	94	101	MHz
Satellite DVB-S2	32 MBaud	65	70	101	MHz
Power Supplies					
V _{DD-VCORE}		1.14	1.20	1.30	V
V _{DD-VANA}		3.00	3.30	3.60	V
V _{DD-VIO}		3.00	3.30	3.60	V
Notes:					
1. Test conditions: 30 MBaud, CR = 7/8, parallel TS (at QEF: BER = 2. 10 ⁻⁴).					
2. Test conditions: 32 MBaud, 3/5 Code Rate, 8PSK, pilots On, parallel TS, C/N at picture failure (PER = 10 ⁻⁴).					

Pin Assignments



Selection Guide

Part Number	Description
Si2166-B20-GM	Satellite TV Demodulator for DVB-S/S2, 7x7 mm QFN-48.