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4.Test circuit



(1) Oppillogoon

(1) Oscilloscope

· Band width should be minimum 5 times higher (wider) than measurement frequency.

• Probe earth should be placed closely from test point and lead length should be as short as possible.

* Recommendable to use miniature socket. (Don't use earth lead.)

(2) L_CMOS also includes probe capacitance.

(3) By-pass capacitor (0.01 mF to 0.1 mF) is placed closely between VCC and GND.

(4) Use the current meter whose internal impedance value is small.

(5) Power supply

• Start up time (0 %VCC ® 90 %VCC) of power source should be more than 150 ms.

· Impedance of power supply should be as lowest as possible.



7.Reflow profile

Pre Heating Temperature Tp1 ~ Tp2 = + 170 °C Heating Temperature TMIt = + 220 °C Peek Temperature TMax. = + 260 °C Point of measuring In case of Solder ability Terminal. In case of Resistance to soldering heat Surface.

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]Product number last 2 digits code(xx) description			The recommended code is "00"
Q3102LC	C020005xx		
Code	Condition	Code	Condition
01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
11	Any Q'ty / Reel	14	1000pcs / Reel
12	250pcs / Reel	00	2000pcs / Reel
(number la Q3102LC Code 01 11 12	number last 2 digits code(xx) descriptionQ3102LC020005xxCodeCondition01Any Q'ty vinyl bag(Tape cut)11Any Q'ty / Reel12250pcs / Reel	number last 2 digits code(xx) descriptionQ3102LC020005xxCodeConditionCode01Any Q'ty vinyl bag(Tape cut)1311Any Q'ty / Reel1412250pcs / Reel00

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