ELECTRICAL CHARACTERISTICS ($T_C = 25$ °C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS	-		1		
Collector-Emitter Sustaining Voltage (Note 1) $(I_C = 10 \text{ mAdc}, I_B = 0)$		V _{CEO(sus)}	25	_	Vdc
Collector Cutoff Current $(V_{CB} = 40 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 40 \text{ Vdc}, I_E = 0, T_J = 125^{\circ}C)$		I _{CBO}	- -	100 100	nAdc μAdc
Emitter Cutoff Current (V _{BE} = 8.0 Vdc, I _C = 0)		I _{EBO}	-	100	nAdc
ON CHARACTERISTICS					
DC Current Gain (Note 1) $ (I_C = 500 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}) $ $ (I_C = 2.0 \text{ Adc}, V_{CE} = 1.0 \text{ Vdc}) $ $ (I_C = 5.0 \text{ Adc}, V_{CE} = 2.0 \text{ Vdc}) $		h _{FE}	70 45 10	- 180 -	-
Collector–Emitter Saturation Voltage (Note 1) (I_C = 500 mAdc, I_B = 50 mAdc) (I_C = 2.0 Adc, I_B = 200 mAdc) (I_C = 5.0 Adc, I_B = 1.0 Adc)		V _{CE(sat)}		0.3 0.75 1.8	Vdc
Base-Emitter Saturation Voltage (Note 1) (I _C = 5.0 Adc, I _B = 1.0 Adc)		V _{BE(sat)}	-	2.5	Vdc
Base-Emitter On Voltage (Note 1) (I _C = 2.0 Adc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	-	1.6	Vdc
DYNAMIC CHARACTERISTICS			-	•	
Current-Gain - Bandwidth Product (Note 2) (I _C = 100 mAdc, V _{CE} = 10 Vdc, f _{test} = 10 MHz)		f _T	65	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz)	MJE200 MJE210	C _{ob}		80 120	pF

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \approx 2.0%. 2. $f_T = |h_{fe}| \bullet f_{test}$.

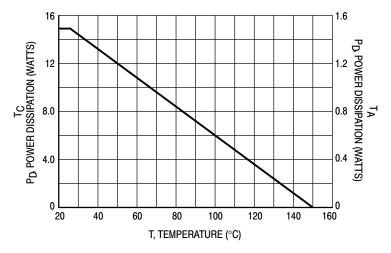
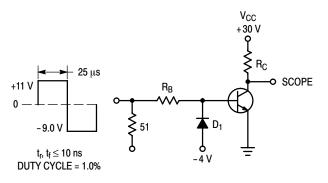


Figure 1. Power Derating



 R_B and R_C Varied to obtain desired current levels D_1 MUST be fast recovery type, e.g.: 1N5825 USED ABOVE $I_B\approx 100~\text{mA}$ MSD6100 USED BELOW $I_B\approx 100~\text{mA}$

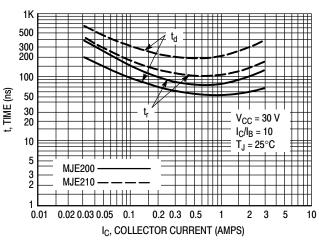
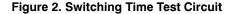


Figure 3. Turn-On Time



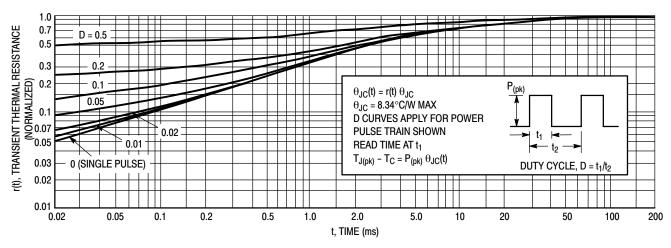


Figure 4. Thermal Response

MJE200 - NPN,

MJE210 - PNP

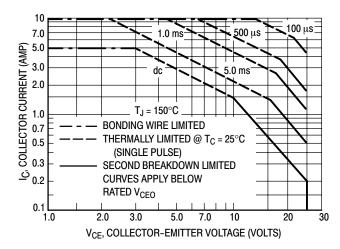


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}\text{C}$; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^{\circ}\text{C}$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

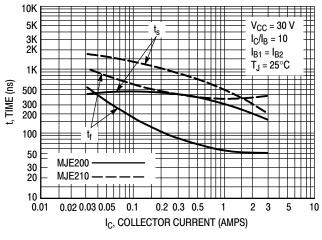


Figure 6. Turn-Off Time

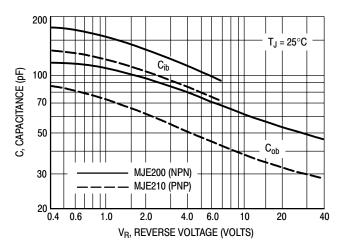


Figure 7. Capacitance

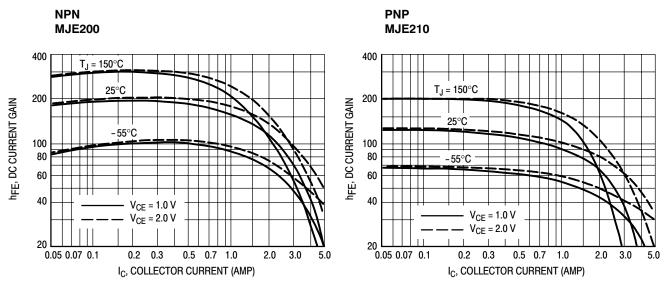


Figure 8. DC Current Gain

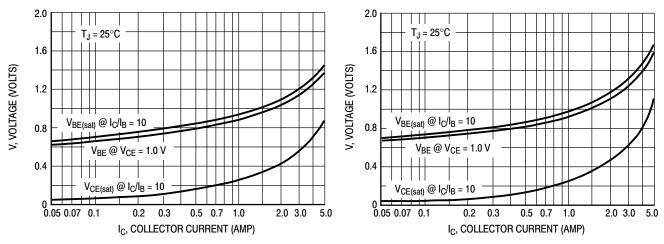


Figure 9. "On" Voltage

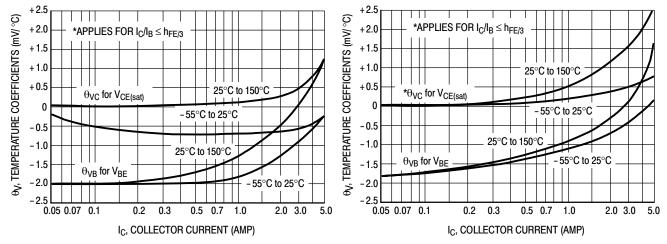
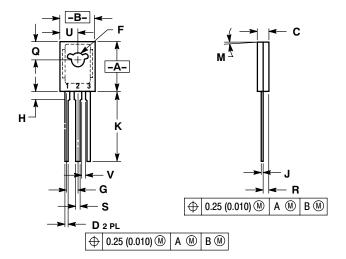


Figure 10. Temperature Coefficients

MJE200 - NPN, **MJE210 - PNP**

PACKAGE DIMENSIONS

TO-225 CASE 77-09 **ISSUE Z**



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094 BSC		2.39 BSC		
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5° TYP		5° TYP		
Q	0.148	0.158	3.76	4.01	
R	0.045	0.065	1.15	1.65	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
V	0.040		1.02		

STYLE 1:

PIN 1. EMITTER 2. COLLECTOR

3. BASE

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