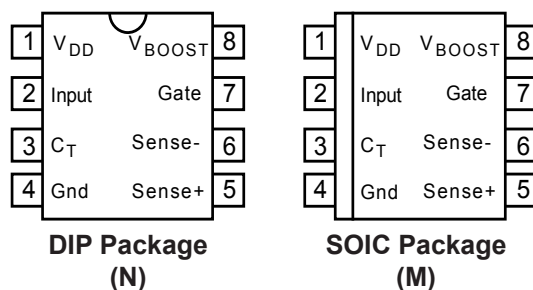
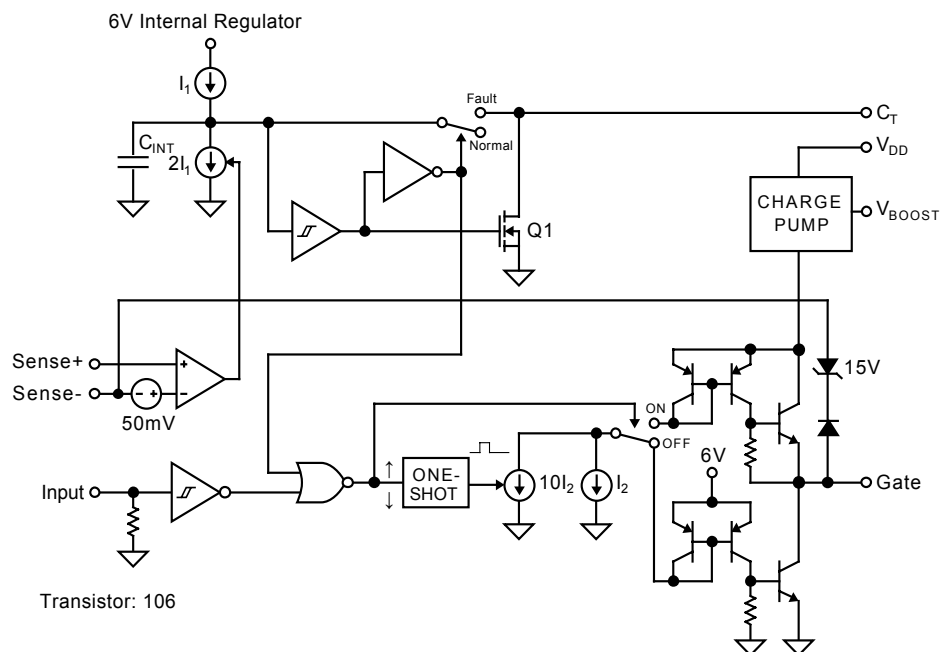


## Pin Configuration



## Block Diagram



## Pin Description

Pin Number	Pin Name	Pin Function
1	$V_{DD}$	Supply: +12V to +36V. Decouple with $\geq 10\mu\text{F}$ capacitor.
2	Input	TTL Compatible Input: Logic high turns the external MOSFET on. An internal pull-down returns an open pin to logic low.
3	$C_T$	Retry Timing Capacitor: Controls the off time ( $t_{G(OFF)}$ ) of the overcurrent retry cycle. (Duty cycle adjustment.) <ul style="list-style-type: none"> <li>• Open = approx. 20% duty cycle.</li> <li>• Capacitor to Ground = approx. 20% to &lt; 1% duty cycle.</li> <li>• Pull-up resistor = approx. 20% to approx. 75% duty cycle.</li> <li>• Ground = maintained shutdown upon overcurrent condition.</li> </ul>
4	Gnd	Circuit Ground
5	Sense +	Current Sense Comparator (+) Input: Connect to high side of sense resistor or current sensing MOSFET sense lead. A built-in offset in conjunction with $R_{SENSE}$ sets the load overcurrent trip point.
6	Sense -	Current Sense Comparator (-) Input: Connect to the low side of the sense resistor (usually the high side of the load).
7	Gate	Gate Drive: Drives the gate of an external power MOSFET. Also limits $V_{GS}$ to 15V max. to prevent Gate-to-Source damage. Will sink and source current.
8	$V_{BOOST}$	Charge Pump Boost Capacitor: A bootstrap capacitor from $V_{BOOST}$ to the FET source pin supplies charge to quickly enhance the Gate output during turn-on.

## Absolute Maximum Ratings

Supply Voltage ( $V_{DD}$ ).....	+40V
Input Voltage .....	–0.5V to +15V
Sense Differential Voltage .....	±6.5V
Sense + or Sense – to Gnd .....	–0.5V to +36V
Timer Voltage ( $C_T$ ).....	+5.5V
$V_{BOOST}$ Capacitor.....	0.01 $\mu$ F

## Operating Ratings

Supply Voltage ( $V_{DD}$ ).....	+12V to +36V
Temperature Range	
PDIP .....	–40°C to +85°C
SOIC .....	–40°C to +85°C

## Electrical Characteristics

$T_A = 25^\circ\text{C}$ , Gnd = 0V,  $V_{DD} = 12\text{V}$ ,  $C_T = \text{Open}$ , Gate  $C_L = 1500\text{pF}$  (IRF540 MOSFET) unless otherwise specified

Symbol	Parameter	Condition	Min	Typ	Max	Units
	D.C. Supply Current	$V_{DD} = 12\text{V}$ , Input = 0V		1.8	4	mA
		$V_{DD} = 36\text{V}$ , Input = 0V		2.5	6	mA
		$V_{DD} = 12\text{V}$ , Input = 5V		1.7	4	mA
		$V_{DD} = 36\text{V}$ , Input = 5V		2.5	6	mA
	Input Threshold		0.8	1.4	2.0	V
	Input Hysteresis			0.1		V
	Input Pull-Down Current	Input = 5V	10	20	40	$\mu$ A
	Current Limit Threshold	<b>Note 1</b>	30	50	70	mV
	Gate On Voltage	$V_{DD} = 12\text{V}$ <b>Note 2</b>	16	18	21	V
		$V_{DD} = 36\text{V}$ <b>Note 2</b>	46	50	52	V
$t_{G(ON)}$	Gate On Time, Fixed	Sense Differential > 70mV	2	6	10	$\mu$ s
$t_{G(OFF)}$	Gate Off Time, Adjustable	Sense Differential > 70mV, $C_T = 0\text{pF}$	10	20	50	$\mu$ s
$t_{DLH}$	Gate Turn-On Delay	<b>Note 3</b>		500	1000	ns
$t_R$	Gate Rise Time	<b>Note 4</b>		400	500	ns
$t_{DLH}$	Gate Turn-Off Delay	<b>Note 5</b>		800	1500	ns
$t_F$	Gate Fall Time	<b>Note 6</b>		400	500	ns
$f_{max}$	Maximum Operating Frequency	<b>Note 7</b>	100	150		kHz

**Note 1** When using sense MOSFETs, it is recommended that  $R_{SENSE} < 50\Omega$ . Higher values may affect the sense MOSFET's current transfer ratio.

**Note 2** DC measurement.

**Note 3** Input switched from 0.8V (TTL low) to 2.0V (TTL high), time for Gate transition from 0V to 2V.

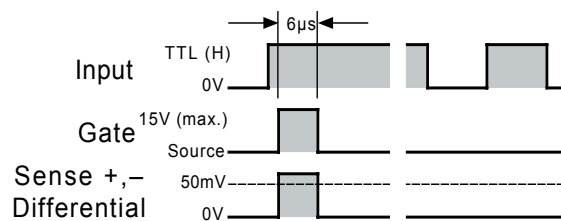
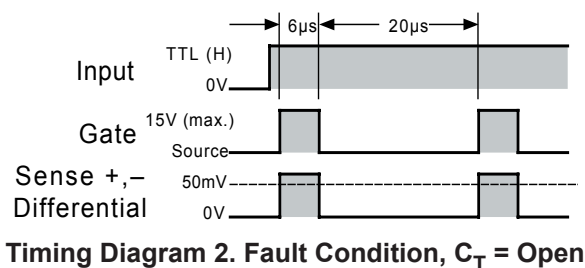
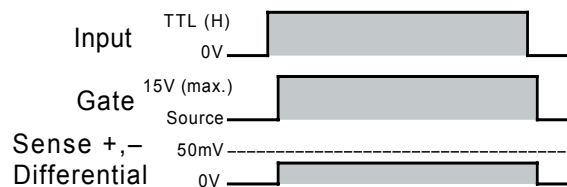
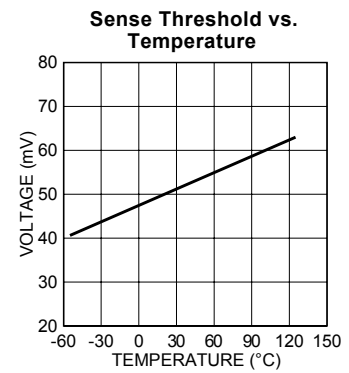
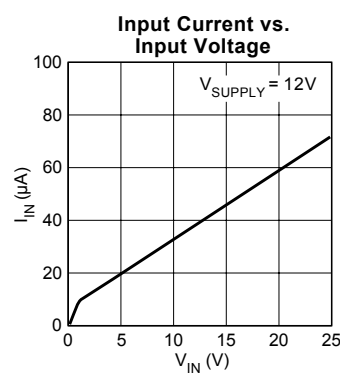
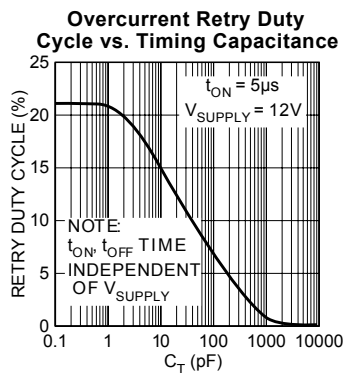
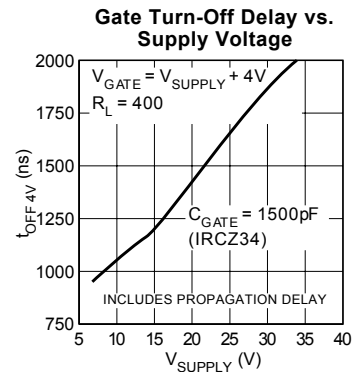
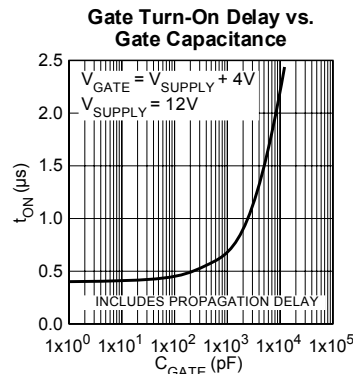
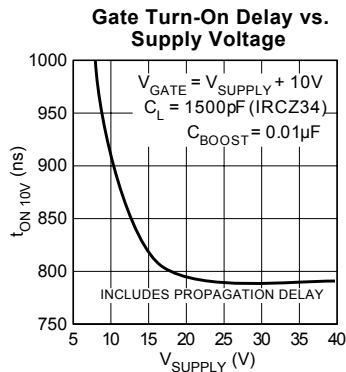
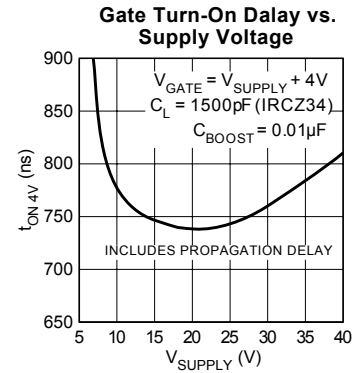
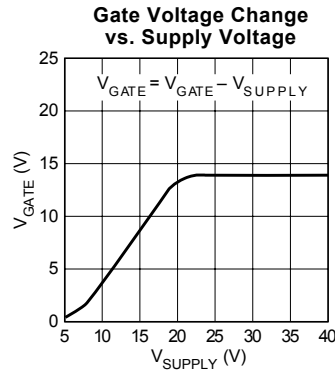
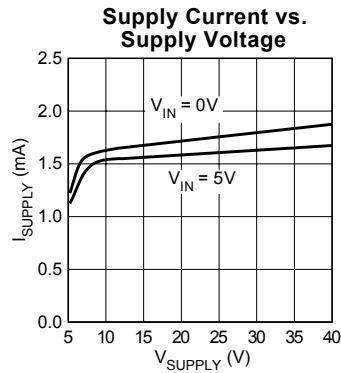
**Note 4** Input switched from 0.8V (TTL low) to 2.0V (TTL high), time for Gate transition from 2V to 17V.

**Note 5** Input switched from 2.0V (TTL high) to 0.8V (TTL low), time for Gate transition from 20V (Gate on voltage) to 17V.

**Note 6** Input switched from 2.0V (TTL high) to 0.8V (TTL low), time for Gate transition from 17V to 2V.

**Note 7** Frequency where gate on voltage reduces to 17V with 50% input duty cycle.

# Typical Characteristics



## Functional Description

Refer to the MIC5021 block diagram.

### Input

A signal greater than 1.4V (nominal) applied to the MIC5021 INPUT causes gate enhancement on an external MOSFET turning the MOSFET on.

An internal pull-down resistor insures that an open INPUT remains low, keeping the external MOSFET turned off.

### Gate Output

Rapid rise and fall times on the GATE output are possible because each input state change triggers a one-shot which activates a high-value current sink ( $10I_2$ ) for a short time. This draws a high current though a current mirror circuit causing the output transistors to quickly charge or discharge the external MOSFET's gate.

A second current sink continuously draws the lower value of current used to maintain the gate voltage for the selected state.

An internal charge pump utilizes an external "boost" capacitor connected between  $V_{BOOST}$  and the source of the external MOSFET. (Refer to typical application.) The boost capacitor stores charge when the MOSFET is off. As the MOSFET turns on, its source to ground voltage increases and is added to the voltage across the capacitor, raising the  $V_{BOOST}$  pin voltage. The boost capacitor charge is directed through the GATE pin to quickly charge the MOSFET's gate to 16V maximum above  $V_{DD}$ . The internal charge pump maintains the gate voltage.

An internal zener diode protects the external MOSFET by limiting the gate to source voltage.

### Sense Inputs

The MIC5021's 50mV (nominal) trip voltage is created by internal current sources that force approximately 5 $\mu$ A out of SENSE + and approximately 15 $\mu$ A (at trip) out of SENSE -. When SENSE - is 50mV or more below SENSE +, SENSE - steals base current from an internal drive transistor shutting off the external MOSFET.

### Overcurrent Limiting

Current source  $I_1$  charges  $C_{INT}$  upon power up. An optional external capacitor connected to  $C_T$  is kept discharged through a MOSFET Q1.

A fault condition (> 50mV from SENSE + to SENSE -) causes the overcurrent comparator to enable current sink  $2I_1$  which overcomes current source  $I_1$  to discharge  $C_{INT}$  in a short time. When  $C_{INT}$  is discharged, the INPUT is disabled, which turns off the gate output, and  $C_{INT}$  and  $C_T$  are ready to be charged.

When the gate output turns the MOSFET off, the overcurrent signal is removed from the sense inputs which deactivates current sink  $2I_1$ . This allows  $C_{INT}$  and the optional capacitor connected to  $C_T$  to recharge. A Schmitt trigger delays the retry while the capacitor(s) recharge. Retry delay is increased by connecting a capacitor to  $C_T$  (optional).

The retry cycle will continue until the fault is removed or the input is changed to TTL low.

If  $C_T$  is connected to ground, the circuit will not retry upon a

## Applications Information

The MIC5021 MOSFET driver is intended for high-side switching applications where overcurrent limiting and high speed are required. The MIC5021 can control MOSFETs that switch voltages up to 36V.

### High-Side Switch Circuit Advantages

High-side switching allows more of the load related components and wiring to remain near ground potential when compared to low-side switching. This reduces the chances of short-to-ground accidents or failures.

### Speed Advantage

The MIC5021 is about two orders of magnitude faster than the low cost MIC5014 making it suitable for high-frequency high-efficiency circuit operation in PWM (pulse width modulation) designs used for motor control, SMPS (switch mode power supply) and heating element control.

Switched loads (on/off) benefit from the MIC5021's fast switching times by allowing use of MOSFETs with smaller safe operating areas. (Larger MOSFETs are often required when using slower drivers.)

### Supply Voltage

The MIC5021's supply input ( $V_{DD}$ ) is rated up to 36V. The supply voltage must be equal to or greater than the voltage applied to the drain of the external N-channel MOSFET.

A 16V minimum supply is recommended to produce continuous on-state, gate drive voltage for standard MOSFETs (10V nominal gate enhancement).

When the driver is powered from a 12V to 16V supply, a logic-level MOSFET is recommended (5V nominal gate enhancement).

PWM operation may produce satisfactory gate enhancement at lower supply voltages. This occurs when fast switching repetition makes the boost capacitor a more significant voltage supply than the internal charge pump.

## Logic-Level MOSFET Precautions

Logic-level MOSFETs have lower maximum gate-to-source voltage ratings (typically  $\pm 10\text{V}$ ) than standard MOSFETs (typically  $\pm 20\text{V}$ ). When an external MOSFET is turned on, the doubling effect of the boost capacitor can cause the gate-to-source voltage to momentarily exceed  $10\text{V}$ . Internal zener diodes clamp this voltage to  $16\text{V}$  maximum which is too high for logic-level MOSFETs. To protect logic-level MOSFETs, connect a zener diode ( $5\text{V} \leq V_{\text{Zener}} < 10\text{V}$ ) from gate to source.

## Overcurrent Limiting

A  $50\text{mV}$  comparator is provided for current sensing. The low level trip point minimizes  $I^2R$  losses when a power resistor is used for current sensing.

The adjustable retry feature can be used to handle loads with high initial currents, such as lamps or heating elements, and can be adjusted from the  $C_T$  connection.

$C_T$  to ground maintains gate drive shutdown following an overcurrent condition.

$C_T$  open, or a capacitor to ground, causes automatic retry. The default duty cycle ( $C_T$  open) is approximately 20%. Refer to the electrical characteristics when selecting a capacitor for reduced duty cycle.

$C_T$  through a pull-up resistor to  $V_{DD}$  increases the duty cycle. *Increasing the duty cycle increases the power dissipation in the load and MOSFET under a "fault" condition.* Circuits may become unstable at a duty cycle of about 75% or higher, depending on conditions. *Caution: The MIC5021 may be damaged if the voltage applied to  $C_T$  exceeds the absolute maximum voltage rating.*

## Boost Capacitor Selection

The boost capacitor value will vary depending on the supply voltage range.

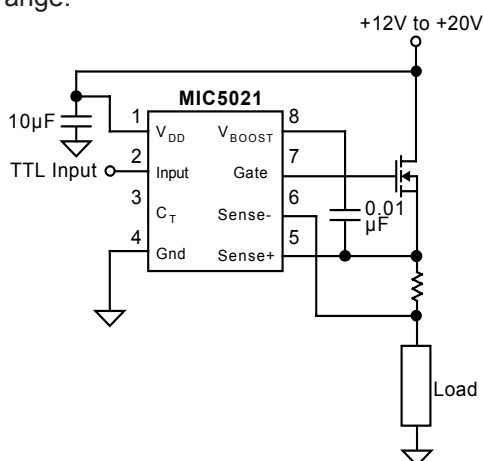


Figure 1. 12V to 20V Configuration

A  $0.01\mu\text{F}$  boost capacitor is recommended for best performance in the  $12\text{V}$  to  $20\text{V}$  range. Refer to figure 1. Larger capacitors may damage the MIC5021.

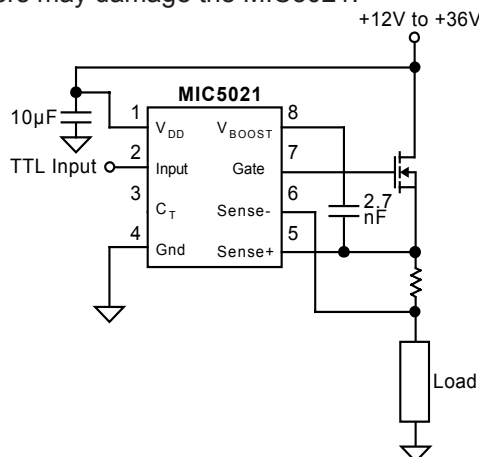


Figure 2. 12V to 36V Configuration

If the full  $12\text{V}$  to  $36\text{V}$  voltage range is required, the boost capacitor value must be reduced to  $2.7\text{nF}$ . Refer to Figure 2. The recommended configuration for the  $20\text{V}$  to  $36\text{V}$  range is to place the capacitor is placed between  $V_{DD}$  and  $V_{BOOST}$  as shown in Figure 3.

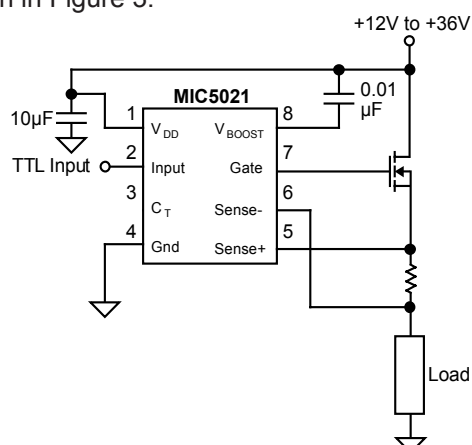


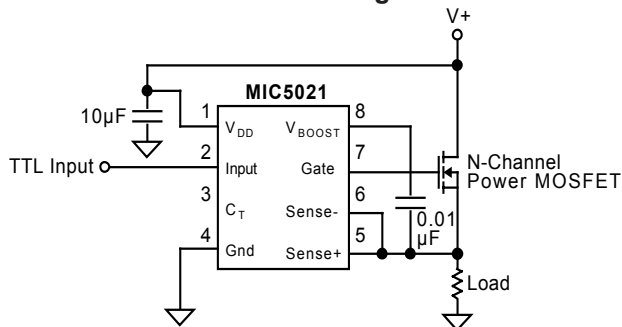
Figure 3. Preferred 20V to 36V Configuration

Do not use both boost capacitor between  $V_{BOOST}$  and the MOSFET source and  $V_{BOOST}$  and  $V_{DD}$  at the same time.

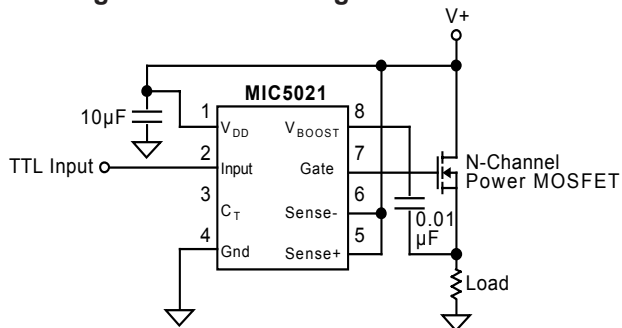
## Current Sense Resistors

Lead length can be significant when using low value ( $< 1\Omega$ ) resistors for current sensing. Errors caused by lead length can be avoided by using four-terminal current sensing resistors. Four-terminal resistors are available from several manufacturers.

## Circuits Without Current Sensing



**Figure 4a. Connecting Sense to Source**



**Figure 4b. Connecting Sense to Supply**

Current sensing may be omitted by connecting the SENSE + and SENSE – pins to the source of the MOSFET or to the supply. Connecting the SENSE pins to the supply is preferred for inductive loads. Do not connect the SENSE pins to ground.

### Inductive Load Precautions

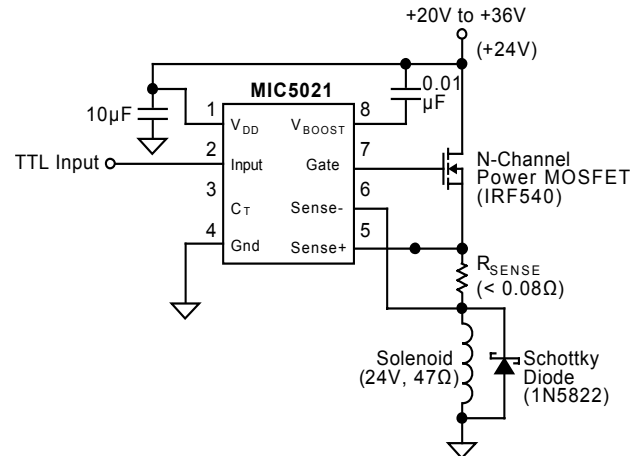
Circuits controlling inductive loads, such as solenoids (Figure 5) and motors, require precautions when controlled by the MIC5021. Wire wound resistors, which are sometimes used to simulate other loads, can also show significant inductive properties.

An inductive load releases stored energy when its current flow is interrupted (when the MOSFET is switched off). The voltage across the inductor reverses and the inductor attempts to force current flow. Since the circuit appears open (the MOSFET appears as a very high resistance) a very large negative voltage occurs across the inductor.

### Limiting Inductive Spikes

The voltage across the inductor can be limited by connecting a Schottky diode across the load. The diode is forward biased only when the load is switched off. The Schottky diode clamps negative transients to a few volts. This protects the MOSFET from drain-to-source breakdown and prevents the transient from damaging the charge pump by way of the boost capacitor. Also see *Sense Pin Considerations* below.

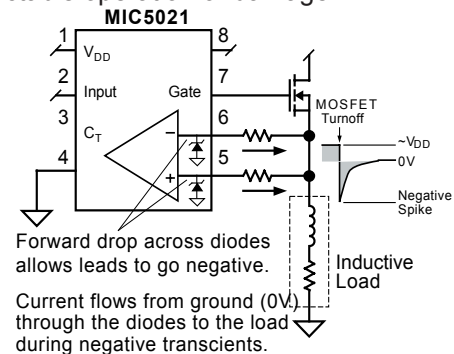
The diode should have a peak forward current rating greater than the load current. This is because the current through the diode is the same as the load current at the instant the MOSFET is turned off.



**Figure 5. Solenoid Driver with Current Sensing**

### Sense Pin Considerations

The sense pins of the MIC5021 are sensitive to negative voltages. Forcing the sense pins much below  $-0.5V$  effectively reverses the supply voltage on portions of the driver resulting in unpredictable operation or damage.



**Figure 6. Inductive Load Turnoff**

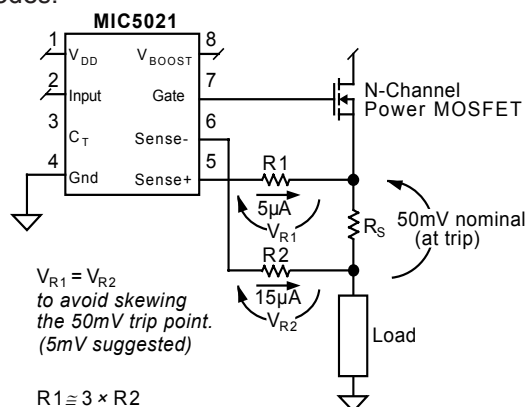
Figure 6 shows current flowing out of the sense leads of an MIC5021 during a negative transient (inductive kick). Internal Schottky diodes attempt to limit the negative transient by maintaining a low forward drop.

Although the internal Schottky diodes can protect the driver in low-current resistive applications, they are inadequate for inductive loads or the lead inductance in high-current resistive loads. Because of their small size, the diodes' forward voltage drop quickly exceeds  $0.5V$  as current increases.



### External Protection

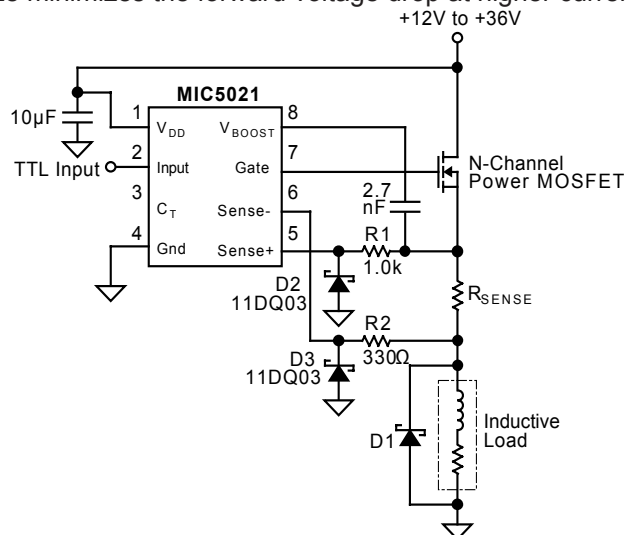
Resistors placed in series with each SENSE connection limit the current drawn from the internal Schottky diodes during a negative transient. This minimizes the forward drop across the diodes.



**Figure 7. Resistor Voltage Drop**

During normal operation, sensing current from the sense pins is unequal (5µA and 15µA). The internal Schottky diodes are reverse biased and have no effect. To avoid skewing the trip voltage, the current limiting resistors must drop equal voltages at the trip point currents. See Figure 7. To minimize resistor tolerance error, use a voltage drop lower than the trip voltage of 50mV. 5mV is suggested.

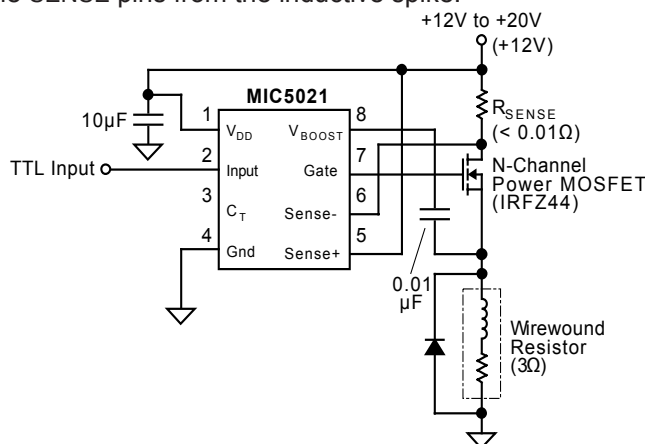
External Schottky diodes are also recommended. See D2 and D3 in Figure 8. The external diodes clamp negative transients better than the internal diodes because their larger size minimizes the forward voltage drop at higher currents.



**Figure 8. Protection from Inductive Kick**

### High-Side Sensing

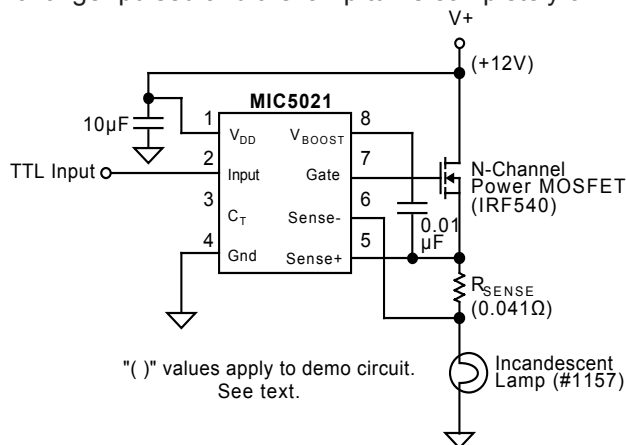
Sensing the current on the high side of the MOSFET isolates the SENSE pins from the inductive spike.



**Figure 9. High Side Sensing**

### Lamp Driver Application

Incandescent lamps have a high inrush current (low resistance) when turned on. The MIC5021 can perform a "soft start" by pulsing the MOSFET (overcurrent condition) until the filament is warm and its current decreases (resistance increases). The sense resistor value is selected so the voltage drop across the sense resistor decreases below the sense threshold (50mV) as the filament becomes warm. The FET is no longer pulsed and the lamp turns completely on.



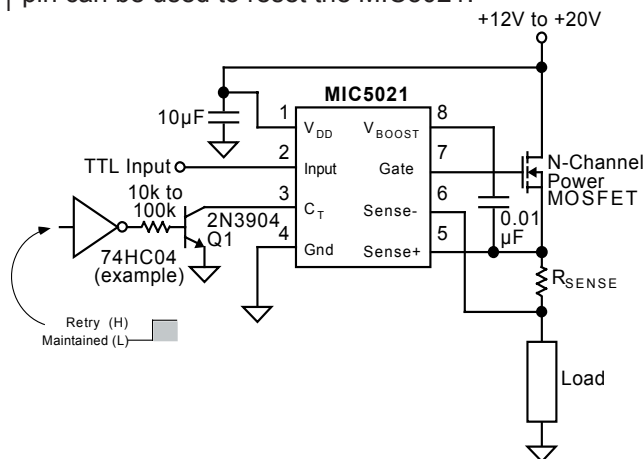
**Figure 10. Lamp Driver with Current Sensing**

A lamp may not fully turn on if the filament does not heat up adequately. Changing the duty cycle, sense resistor, or both to match the filament characteristics can correct the problem.

Soft start can be demonstrated using a #1157 dual filament automotive lamp. The value of  $R_S$  shown in Figure 10 allows for soft start of the higher-resistance filament (measures approx. 2.1Ω cold or 21Ω hot).

### Remote Overcurrent Limiting Reset

In circuit breaker applications where the MIC5021 maintains an off condition after an overcurrent condition is sensed, the  $C_T$  pin can be used to reset the MIC5021.



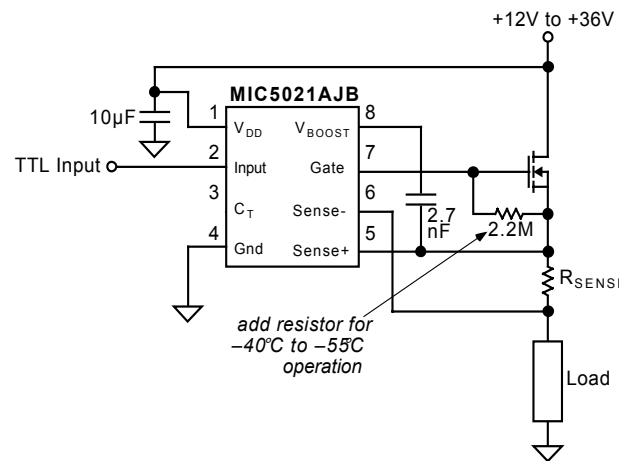
**Figure 11. Remote Control Circuit**

Switching Q1 on pulls  $C_T$  low which keeps the MIC5021 GATE output off when an overcurrent is sensed. Switching Q1 off causes  $C_T$  to appear open. The MIC5021 retries in about 20µs and continues to retry until the overcurrent condition is removed.

For demonstration purposes, a 680Ω load resistor and 3Ω sense resistor will produce an overcurrent condition when the load's supply ( $V_+$ ) is approximately 12V or greater.

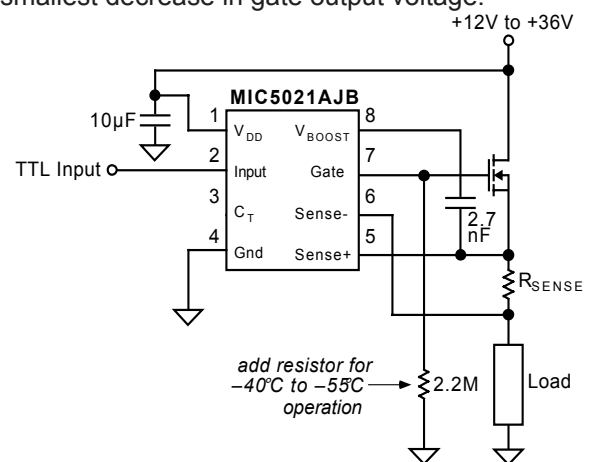
### Low-Temperature Operation

As the temperature of the MIC5021AJB (extended temperature range version—no longer available) approaches  $-55^{\circ}\text{C}$ , the driver's off-state, gate-output offset from ground increases. If the operating environment of the MIC5021AJB includes low temperatures ( $-40^{\circ}\text{C}$  to  $-55^{\circ}\text{C}$ ), add an external 2.2MΩ resistor as shown in Figures 12a or 12b. This assures that the driver's gate-to-source voltage is far below the external MOSFET's gate threshold voltage, forcing the MOSFET fully off.



**Figure 12a. Gate-to-Source Pull Down**

The gate-to-source configuration (refer to Figure 12a) is appropriate for resistive and inductive loads. This also causes the smallest decrease in gate output voltage.

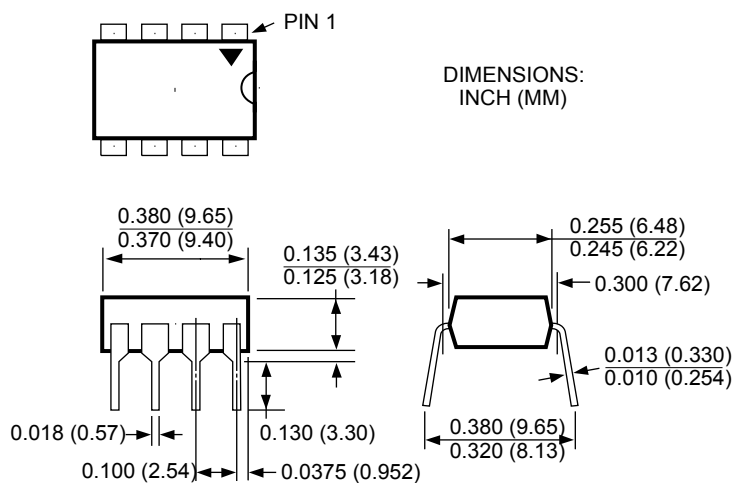


**Figure 12b. Gate-to-Ground Pull Down**

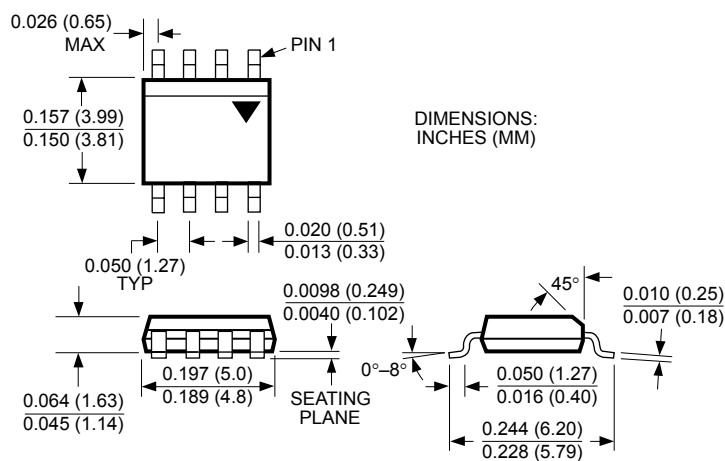
The gate-to-ground configuration (refer to Figure 12b) is appropriate for resistive, inductive, or capacitive loads. This configuration will decrease the gate output voltage slightly more than the circuit shown in Figure 12a.



## Package Information



**8-Pin Plastic DIP (N)**



**8-Pin SOIC (M)**

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