MAX4564

Low-Voltage, Dual-Supply, SPDT Analog Switch with Enable

ABSOLUTE MAXIMUM RATINGS

(Voltages Referenced to GND)
V+ .....-0.3V to +13V
V- .....-13V to +0.3V
V+ to V- .....-0.3V to +13V
$\overline{EN}$, IN, COM, NC, NO (Note 1) ..... (V- - 0.3V) to (V+ + 0.3V)
Continuous Current (any terminal)..... ±20mA
Peak Current, COM, NC, NO
(pulsed at 1ms, 10% duty cycle)..... ±30mA
ESD per Method 3015.7..... >2kV

Continuous Power Dissipation (T_A = +70°C)
SOT23 (derate 5.6mW/°C above +70°C) 444.4mW
µMAX (derate 4.5mW/°C above +70°C) 362mW
Operating Temperature Range
MAX4564E_A .....-40°C to +85°C
Junction Temperature .....+150°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C
Soldering Temperature (reflow) .....+260°C

Note 1: Signals on NO, NC, COM, IN, or $\overline{EN}$ exceeding V+ or V- are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—±5V Supply

(V+ = +4.5V to +6V, V- = -4.5V to -6V, V_{IH} = +2.4V, V_{IL} = +0.8V, GND = 0, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{COM}, V_{NO}, V_{NC}			V-		V+	V
On-Resistance	R_{ON}	V+ = +4.5V, V- = -4.5V, I_{COM} = 1mA; V_{NO}, V_{NC} = ±3.5V	+25°C		40	60	Ω
			E			100	
On-Resistance Match Between Channels (Note 4)	ΔR_{ON}	V+ = +4.5V, V- = -4.5V, I_{COM} = 1mA; V_{NO}, V_{NC} = ±3.5V	+25°C		0.75	3	Ω
			E			4	
On-Resistance Flatness (Note 5)	$R_{FLAT(ON)}$	V+ = +4.5V, V- = -4.5V, I_{COM} = 1mA; V_{NO}, V_{NC} = -3.5V, 0, +3.5V	+25°C		6.5	10	Ω
			E			13	
NO or NC Off-Leakage Current	$I_{NC(OFF)}$ or $I_{NO(OFF)}$	V+ = +5.5V, V- = -5.5V; V_{COM} = +4.5V, -4.5V; V_{NO}, V_{NC} = -4.5, +4.5V	+25°C	-1	0.05	1	nA
			E	-5		5	
COM Off-Leakage Current	$I_{COM(OFF)}$	V+ = +5.5V, V- = -5.5V; V_{COM} = +4.5V, -4.5V; V_{NO}, V_{NC} = -4.5, +4.5V	+25°C	-1	0.05	1	nA
			E	-5		5	
COM On-Leakage Current	$I_{COM(ON)}$	V+ = +5.5V, V- = -5.5V, V_{COM} = +4.5V, -4.5V; V_{NO}, V_{NC} = +4.5V, -4.5V, or unconnected	+25°C	-2	0.05	2	nA
			E	-10		10	

ELECTRICAL CHARACTERISTICS—±5V Supply (continued)

(V+ = +4.5V to +6V, V- = -4.5V to -6V, V_{IH} = +2.4V, V_{IL} = +0.8V, GND = 0, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	V_{NO}, V_{NC} = +3V, -3V, R_L = 1kΩ, C_L = 35pF	+25°C		40	60	ns
			E			75	
Turn-Off Time	t_{OFF}	V_{NO}, V_{NC} = +3V, -3V, R_L = 1kΩ, C_L = 35pF	+25°C		28	40	ns
			E			50	
Transition Time	t_{TRANS}	V_{NC} = +3V, V_{NO} = -3V, V_{NC} = -3V, V_{NO} = +3V, R_L = 1kΩ, C_L = 35pF	+25°C		50	70	ns
			E			85	
Break-Before-Make Time (Note 6)	t_{BBM}	V_{NO}, V_{NC} = +3V, -3V, R_L = 300Ω, C_L = 35pF	+25°C	5	15		ns
Charge Injection	Q	V_{GEN} = 0, R_{GEN} = 0, C_L = 100pF	+25°C		3		pC
-3dB Bandwidth	f_{-3dB}	R_L = 50Ω, C_L = 10pF, f_{0dB} = 1MHz	+25°C		450		MHz
Off-Isolation (Note 7)	V_{ISO}	R_L = 50Ω, C_L = 10pF, f_{IN} = 1MHz	+25°C		-77		dB
Crosstalk (Control Input to Signal Output)		R_L = 50Ω, C_L = 10pF, V+ = +4.5V, V- = -4.5V, f_{IN} = 1MHz, $V_{\overline{EN}}$ = V_{IH}	+25°C		68		mV
Crosstalk (Between Switches)	V_{CT}	R_L = 50Ω, C_L = 10pF, f_{IN} = 1MHz	+25°C		-72		dB
Total Harmonic Distortion	THD	R_L = 600kΩ, C_L = 50pF, f_{IN} = 20kHz	+25°C		0.15		%
Control Input Capacitance	C_{IN}				3		pF
NO or NC Off-Capacitance	C_{OFF}	f_{IN} = 1MHz	+25°C		6		pF
COM Off-Capacitance	$C_{COM(OFF)}$	f_{IN} = 1MHz	+25°C		8		pF
COM On-Capacitance	$C_{COM(ON)}$	f_{IN} = 1MHz	+25°C		14		pF
LOGIC INPUT							
Input Voltage Low	V_{IL}					0.8	V
Input Voltage High	V_{IH}			2.4			V
Input Leakage Current	I_L	V+ = +5.5V, V- = -5.5V, V_{IN} = $V_{\overline{EN}}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	
POWER SUPPLY							
Power-Supply Range	V+			2		6	V
	V-			-2		-6	
Positive Supply Current	I+	V+ = +5.5V, V- = -5.5V, V_{IN} = $V_{\overline{EN}}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	
Negative Supply Current	I-	V+ = +5.5V, V- = -5.5V, V_{IN} = $V_{\overline{EN}}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	

ELECTRICAL CHARACTERISTICS—Single +5V Supply

(V+ = +4.5V to +6V, V- = 0, V_{IH} = +2.4V, V_{IL} = +0.8V, GND = 0, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{COM}, V_{NO}, V_{NC}			0		V+	V
On-Resistance	R_{ON}	V+ = +4.5V, V- = 0, I_{COM} = 1mA; V_{NO}, V_{NC} = +3.5 V	+25°C		72	100	Ω
			E			125	
On-Resistance Match Between Channels (Note 4)	ΔR_{ON}	V+ = +4.5V, V- = 0, I_{COM} = 1mA; V_{NO}, V_{NC} = +3.5 V	+25°C		0.75	5	Ω
			E			7	
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	V_{NO}, V_{NC} = +3V, R_L = 1kΩ, C_L = 35pF	+25°C		62	90	ns
			E			125	
Turn-Off Time	t_{OFF}	V_{NO}, V_{NC} = +3V, R_L = 1kΩ, C_L = 35pF	+25°C		22	60	ns
			E			75	
Transition Time	t_{TRANS}	V_{NC} = +3V, V_{NO} = 0, V_{NC} = 0, V_{NO} = +3V, R_L = 1kΩ, C_L = 35pF	+25°C		68	100	ns
			E			130	
Break-Before-Make Time (Note 6)	t_{BBM}	V_{NO}, V_{NC} = +3V, R_L = 300Ω, C_L = 35pF	E	10	35		ns
LOGIC INPUT							
Input Voltage Low	V_{IL}					0.8	V
Input Voltage High	V_{IH}			2.4			V
Input Leakage Current	I_L	V+ = +5.5V, V- = 0, V_{IN} = V $\overline{EN}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	
POWER SUPPLY							
Power-Supply Range	V+			1.8		12	V
Positive Supply Current	I+	V+ = +5.5V, V- = 0, V_{IN} = V $\overline{EN}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	
Negative Supply Current	I-	V+ = +5.5V, V- = 0, V_{IN} = V $\overline{EN}$ = 0 or +5.5V	+25°C	-1	0.0001	1	µA
			E	-10		10	

ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = +2.7V to +3.3V, V- = 0, V_{IH} = +2.4V, V_{IL} = +0.8V, GND = 0, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

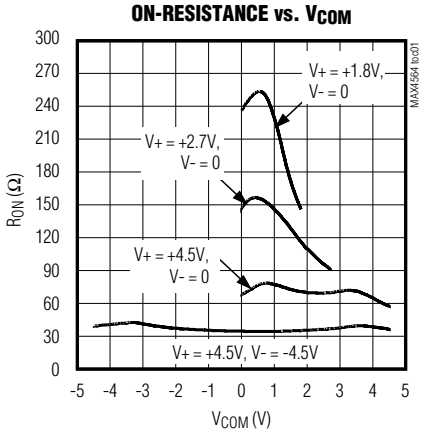
PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{COM}, V_{NO}, V_{NC}			0		V+	V
On-Resistance	R_{ON}	V+ = +2.7V, V- = 0, I_{COM} = 1mA; V_{NO}, V_{NC} = +1.5V	+25°C		160	275	Ω
			E			300	
On-Resistance Match Between Channels (Note 4)	ΔR_{ON}	V+ = +2.7V, V- = 0, I_{COM} = 1mA; V_{NO}, V_{NC} = +1.5V	+25°C		1.5	10	Ω
			E			12	
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	V_{NO}, V_{NC} = +1.5V, R_L = 2kΩ, C_L = 35pF	+25°C		120	250	ns
			E			275	
Turn-Off Time	t_{OFF}	V_{NO}, V_{NC} = +1.5V, R_L = 2kΩ, C_L = 35pF	+25°C		40	110	ns
			E			125	
Break-Before-Make Time (Note 6)	t_{BBM}	V_{NO}, V_{NC} = +1.5V, R_L = 2kΩ, C_L = 35pF	E	10			ns
LOGIC INPUT							
Input Voltage Low	V_{IL}					0.8	V
Input Voltage High	V_{IH}			2.4			V
Input Leakage Current	I_L	V+ = +3.3V, V- = 0, V_{IN} = $V_{\overline{EN}}$ = 0 or +3.3V	+25°C	-1	0.0001	1	µA
			E	-10		10	

Note 2: The algebraic convention is used in this data sheet; the most negative value is shown in the minimum column.
Note 3: SOT-packaged products are 100% tested at +25°C and guaranteed by design at the full-rated temperature.
Note 4: $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$.
Note 5: Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal ranges.
Note 6: Guaranteed by design.
Note 7: Off-Isolation = $20\log_{10}$ (V_{COM} / V_{NO}), V_{NO} = input to off switch.

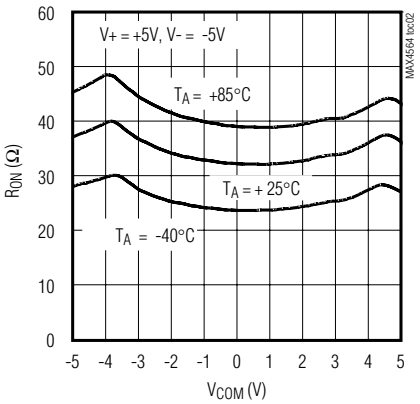
Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

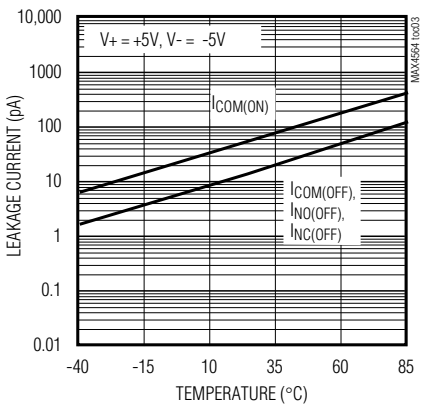
ON-RESISTANCE vs. V_{COM}



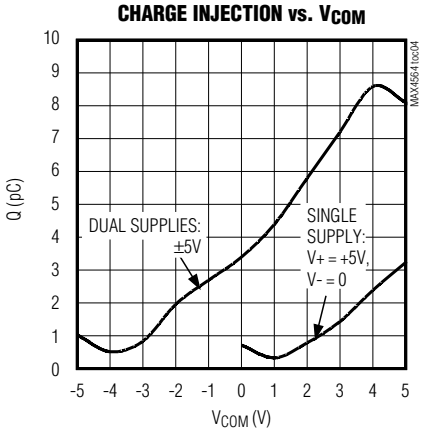
ON-RESISTANCE vs. V_{COM} AND TEMPERATURE (DUAL SUPPLIES)



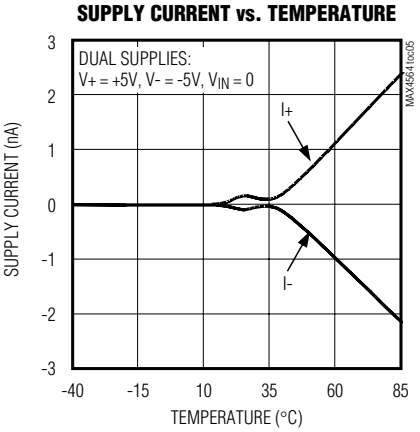
SWITCH LEAKAGE CURRENT vs. TEMPERATURE



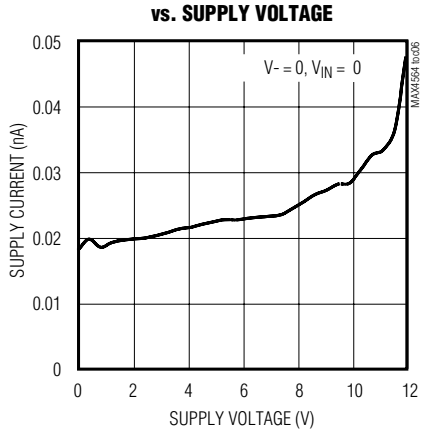
CHARGE INJECTION vs. V_{COM}



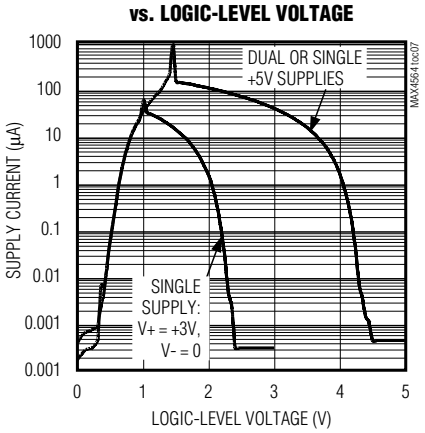
SUPPLY CURRENT vs. TEMPERATURE



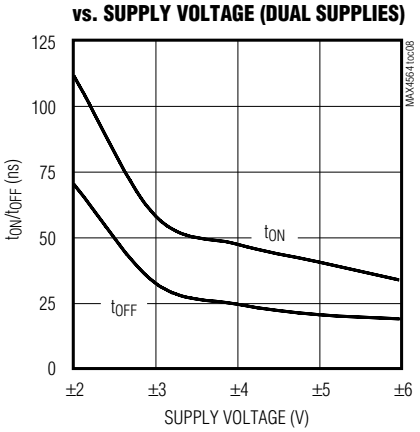
SUPPLY CURRENT vs. SUPPLY VOLTAGE



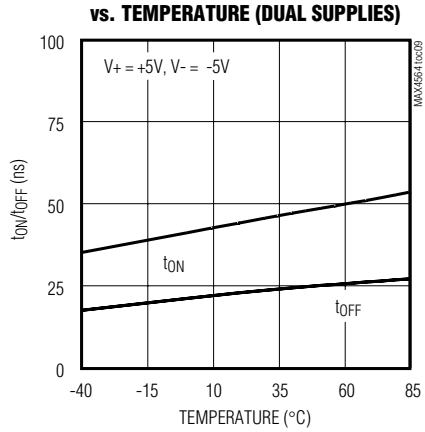
SUPPLY CURRENT vs. LOGIC-LEVEL VOLTAGE



TURN-ON/TURN-OFF TIME vs. SUPPLY VOLTAGE (DUAL SUPPLIES)



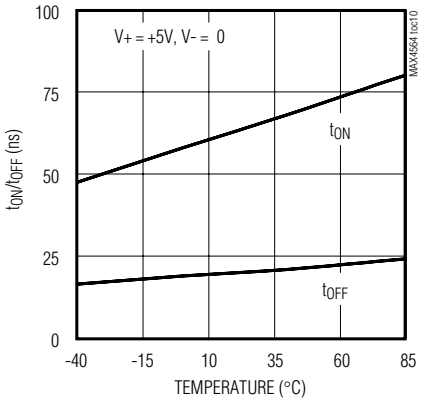
TURN-ON/TURN-OFF TIME vs. TEMPERATURE (DUAL SUPPLIES)



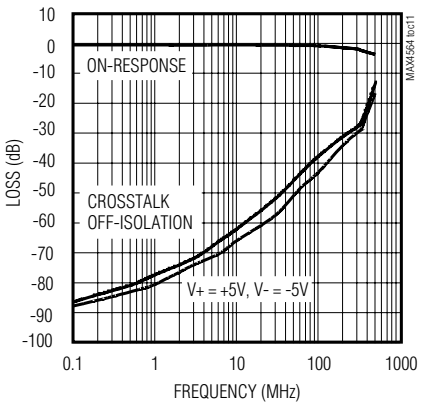
Typical Operating Characteristics (continued)

(T_A = +25°C, unless otherwise noted.)

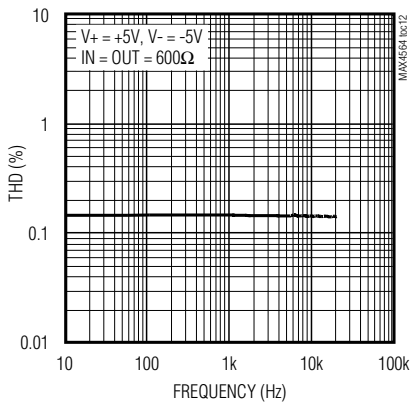
TURN-ON/TURN-OFF TIME vs. TEMPERATURE (SINGLE SUPPLY)



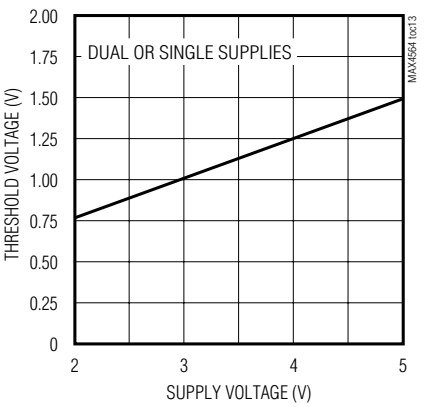
ON-RESPONSE, OFF-ISOLATION, CROSSTALK vs. FREQUENCY



TOTAL HARMONIC DISTORTION vs. FREQUENCY



LOGIC-LEVEL THRESHOLD VOLTAGE vs. SUPPLY VOLTAGE



Pin Description

μMAX	SOT23	NAME	FUNCTION
1	8	COM	Analog Switch Common
2	7	$\overline{EN}$	Device Enable. Drive $\overline{EN}$ low for normal SPDT switch operation. If $\overline{EN}$ is high, both NO and NC are disconnected.
3	6	V-	Negative Supply Voltage
4	5	GND	Ground
5	3	IN	Digital Control Input
6	4	NO	Analog Switch Normally Open
7	1	NC	Analog Switch Normally Closed
8	2	V+	Positive Supply Voltage

Detailed Description

The MAX4564 is a dual-supply SPDT CMOS analog switch. The MAX4564 has break-before-make switching. The CMOS switch construction provides Rail-to-Rail® signal handling while consuming virtually no power. Each of the two switches is independently controlled by a TTL/CMOS-level-compatible digital input.

Applications Information

Overvoltage Protection

Do not exceed the absolute maximum ratings because stresses beyond the listed ratings may cause permanent damage to the device. Proper power-supply sequencing is recommended for all CMOS devices. Always sequence V+ on first, then V-, followed by the logic inputs NO, NC, or COM. If power-supply sequencing is not possible, add two small-signal diodes (D1, D2) in series with supply pins. Adding diodes reduces the analog signal range to one diode drop below V+ and one diode drop above V-, but does not affect the device's low switch resistance and low leakage characteristics.

Test Circuits/ Timing Diagrams

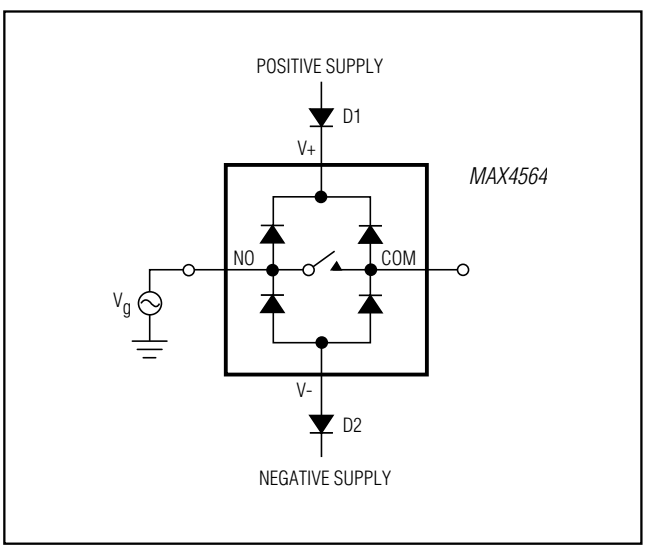


Figure 1. Overvoltage Protection Using Two External Blocking Diodes

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Test Circuits/Timing Diagrams (continued)

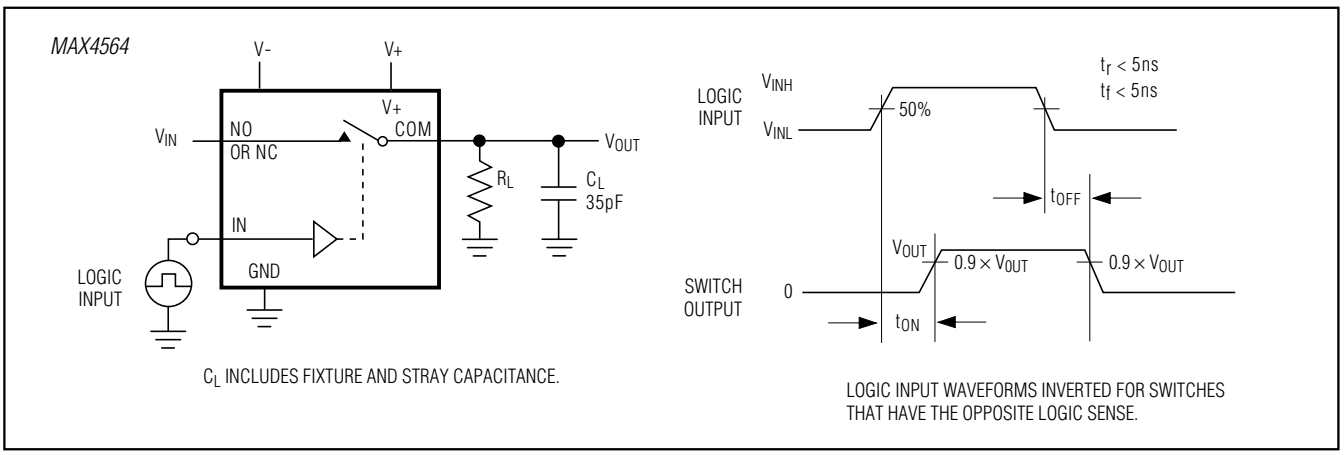


Figure 2. Switching Time

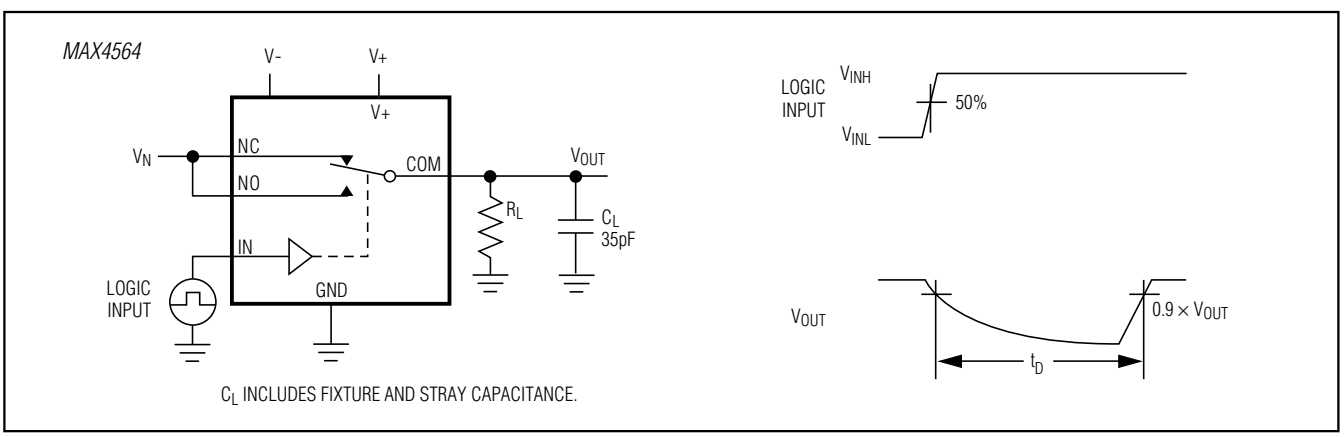


Figure 3. Break-Before-Make Interval

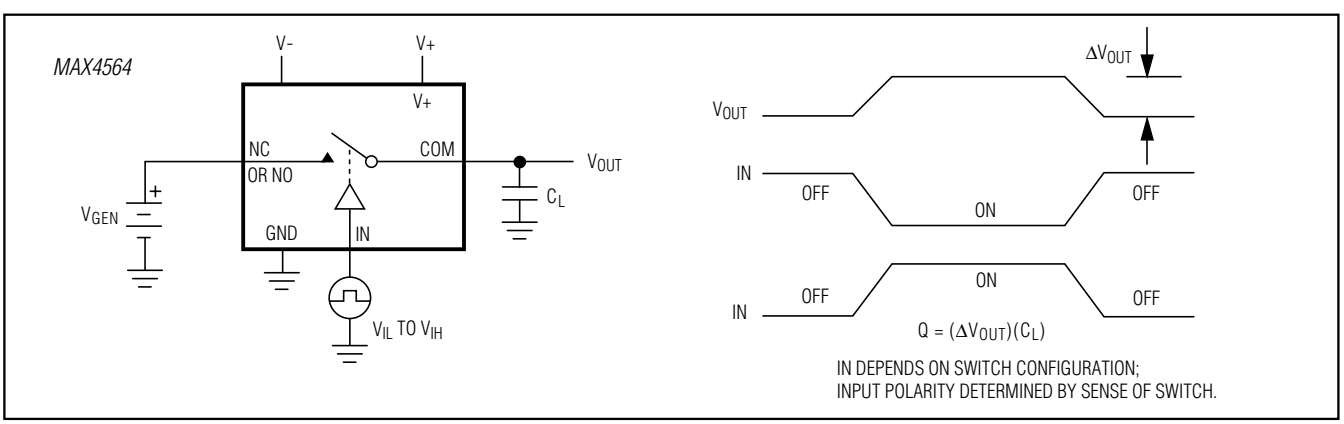


Figure 4. Charge Injection

Test Circuits/Timing Diagrams (continued)

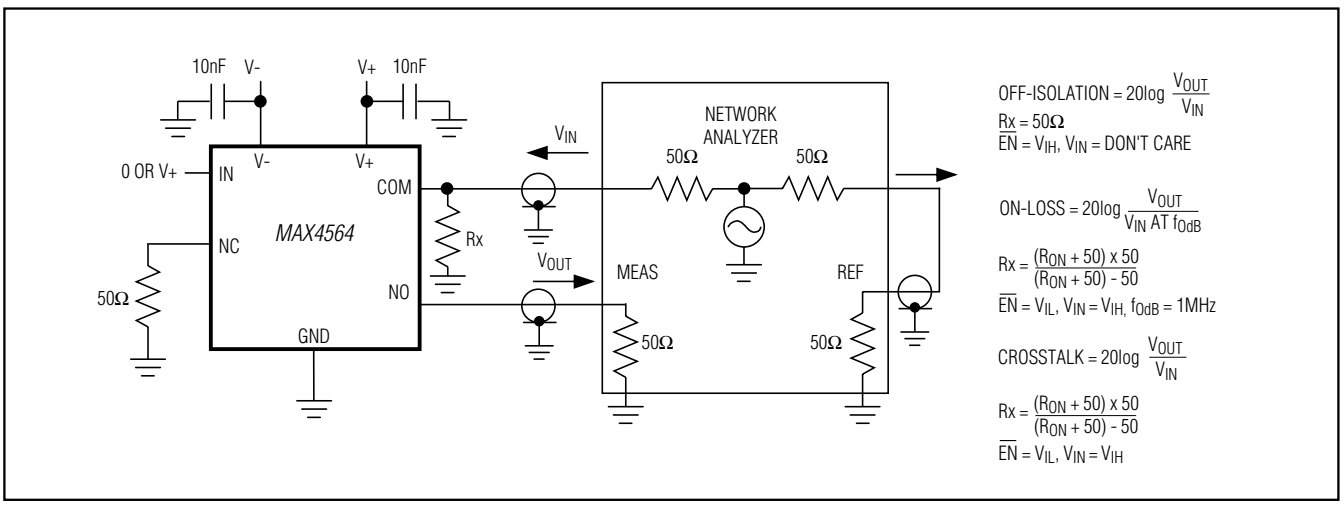


Figure 5. On-Loss, Off-Isolation, and Crosstalk

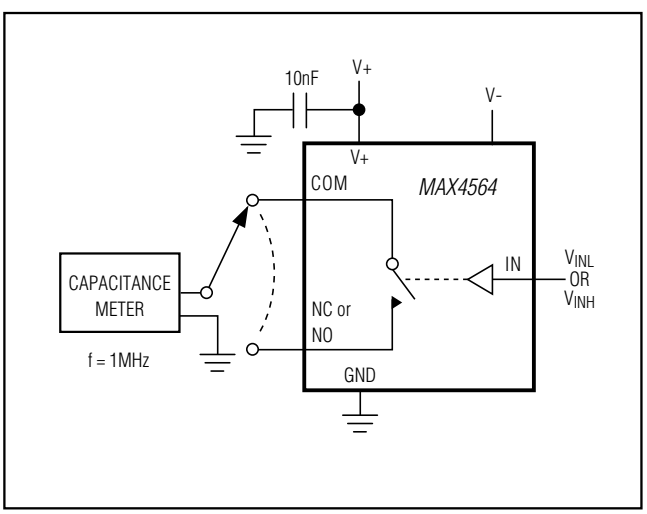


Figure 6. Channel Off/On-Capacitance

Chip Information

PROCESS : CMOS

Package Information

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SOT23	K8SN+1	21-0078	90-0176
8 SO	U8+1	21-0036	90-0092

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
2	10/12	Added lead-free designation to the part numbers in the *Ordering Information*	1