

Low-Cost, Low-Voltage, Quad, SPST, CMOS Analog Switches

ABSOLUTE MAXIMUM RATINGS

(Voltages referenced to GND)

V+	-0.3V to +17V
V _{IN} , V _{COM} , V _{NO} (Note 1)	-0.3V to (V + +0.3V)
Current (any terminal)	30mA
Peak Current (any terminal)	100mA
ESD per Method 3015.7	>2000V
Continuous Power Dissipation (T _A = +70°C)	
Plastic DIP (derate 10.00mW/°C above +70°C)	800mW
Narrow SO (derate 8.00mW/°C above +70°C)	640mW
QSOP (derate 9.52mW/°C above +70°C)	762mW

CERDIP (derate 9.09mW/°C above +70°C)	727mW
Operating Temperature Ranges	
MAX4066C_/MAX4066AC_	0°C to +70°C
MAX4066E_/MAX4066AE_	-40°C to +85°C
MAX4066MJD/MAX4066AMJD	-55°C to +125°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering, 10sec)	+300°C

Note 1: Signals on NO₋, COM₋, or IN₋ exceeding V+ or GND are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +12V Supply

(V+ = 12V ±10%, GND = 0V, V_{INH} = 4.0V, V_{INL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP (Note 2)	MAX	UNITS
ANALOG SWITCH								
Analog Signal Range	V _{COM} , V _{NO}	(Note 3)			0		V+	V
On-Resistance	R _{ON}	V+ = 12V, I _{COM} = 2mA, V _{NO} = 10V	T _A = +25°C		16		45	Ω
			T _A = T _{MIN} to T _{MAX}	C, E		55		
				M		75		
On-Resistance Match Between Channels (Note 4)	ΔR _{ON}	V+ = 12V, I _{COM} = 2mA, V _{NO} = 10V	T _A = +25°C	MAX4066	0.5		4	Ω
				MAX4066A	0.5		2	
			T _A = T _{MIN} to T _{MAX}				6	
On-Resistance Flatness (Note 5)	R _{FLAT(ON)}	V+ = 12V, I _{COM} = 2mA, V _{NO} = 10V, 5V, 1V	T _A = +25°C		2		4	Ω
			T _A = T _{MIN} to T _{MAX}				6	
NO or NC Off Leakage Current (Note 6)	I _{NO(OFF)}	V+ = 12V, V _{COM} = 0V, V _{NO} = 10V	T _A = +25°C	MAX4066	-1		1	nA
				MAX4066A	-0.1		0.1	
			T _A = T _{MIN} to T _{MAX}	C, E	-6		6	
				M	-100		100	
COM Off Leakage Current (Note 6)	I _{COM(OFF)}	V+ = 12V, V _{COM} = 0V, V _{NO} = 10V	T _A = +25°C	MAX4066	-1		1	nA
				MAX4066A	-0.1		0.1	
			T _A = T _{MIN} to T _{MAX}	C, E	-6		6	
				M	-100		100	
COM On Leakage Current (Note 6)	I _{COM(ON)}	V+ = 12V, V _{COM} = 10V, V _{NO} = 10V	T _A = +25°C	MAX4066	-2		2	nA
				MAX4066A	-0.2		0.2	
			T _A = T _{MIN} to T _{MAX}	C, E	-12		12	
				M	-200		200	

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ELECTRICAL CHARACTERISTICS—Single +12V Supply (continued)

(V+ = 12V ±10%, GND = 0V, V_{INH} = 4.0V, V_{INL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
LOGIC INPUT							
Input Current with Input Voltage High	I _{INH}	IN = 5.0V, all others = 0.8V		-0.5	0.005	0.5	μA
Input Current with Input Voltage Low	I _{INL}	IN = 0.8V, all others = 5.0V		-0.5	0.005	0.5	μA
DYNAMIC							
Turn-On Time	t _{ON}	V _{COM} = 10V, Figure 2	T _A = +25°C	25	100	ns	
			T _A = T _{MIN} to T _{MAX}		150		
Turn-Off Time	t _{OFF}	V _{COM} = 10V, Figure 2	T _A = +25°C	15	75	ns	
			T _A = T _{MIN} to T _{MAX}		100		
On-Channel Bandwidth	BW	Signal = 0dbm, Figure 4, 50Ω in and out	T _A = +25°C	100		MHz	
Charge Injection (Note 3)	V _{CTE}	C _L = 1.0nF, V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 3	T _A = +25°C	1	10	pC	
Off Isolation (Note 7)	V _{ISO}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 4	T _A = +25°C	-58		dB	
Crosstalk (Note 8)	V _{CT}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 5	T _A = +25°C	-86		dB	
NO Capacitance	C _(OFF)	f = 1MHz, Figure 6	T _A = +25°C	9		pF	
COM Off Capacitance	C _{COM(OFF)}	f = 1MHz, Figure 6	T _A = +25°C	9		pF	
COM On Capacitance	C _{COM(ON)}	f = 1MHz, Figure 6	T _A = +25°C	22		pF	
SUPPLY							
Power-Supply Range						V	
Supply Current	I ₊	V _{IN} = 0V or V ₊ , all channels on or off	T _A = T _{MIN} to T _{MAX}	-1	0.001	1	μA
Total Harmonic Distortion	THD		T _A = T _{MIN} to T _{MAX}	0.03		%	

MAX4066/MAX4066A

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ELECTRICAL CHARACTERISTICS—Single +5V Supply

(V+ = 5V ±10%, V- = 0V ±10%, GND = 0V, VINH = 2.4V, VINL = 0.8V, TA = TMIN to TMAX, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP (Note 2)	MAX	UNITS
ANALOG SWITCH								
Analog Signal Range	V _{COM} , V _{NO}	(Note 3)			0		V+	V
On-Resistance	R _{ON}	V+ = 4.5V, I _{COM} = -1.0mA, V _{NO} = 3.5V	T _A = +25°C		45		75	Ω
			T _A = T _{MIN} to T _{MAX}	C, E	52		100	
				M			125	
On-Resistance Match Between Channels (Note 4)	ΔR _{ON}	V+ = 5V, I _{COM} = -1.0mA, V _{NO} = 3V	T _A = +25°C		0.3		4	Ω
			T _A = T _{MIN} to T _{MAX}				12	
On-Resistance Flatness (Notes 3, 5)	R _{FLAT(ON)}	V+ = 5V, I _{COM} = -1.0mA, V _{NO} = 1V, 3V	T _A = +25°C		4		6	Ω
			T _A = T _{MIN} to T _{MAX}				8	
NO Off Leakage Current (Note 6)	I _{NO(OFF)}	V+ = 5.5V, V _{COM} = 0V, V _{NO} = 4.5V	T _A = +25°C	MAX4066	-1		1	nA
				MAX4066A	-0.1		0.1	
			T _A = T _{MIN} to T _{MAX}	C, E	-6		6	
				M	-100		100	
COM Off Leakage Current (Note 6)	I _{COM(OFF)}	V+ = 5.5V, V _{COM} = 0V, V _{NO} = 4.5V	T _A = +25°C	MAX4066	-1		1	nA
				MAX4066A	-0.1		0.1	
			T _A = T _{MIN} to T _{MAX}	C, E	-6		6	
				M	-100		100	
COM On Leakage Current (Note 6)	I _{COM(ON)}	V+ = 5.5V, V _{COM} = 5V, V _{NO} = 4.5V	T _A = +25°C	MAX4066	-2		2	nA
				MAX4066A	-0.2		0.2	
			T _A = T _{MIN} to T _{MAX}	C, E	-12		12	
				M	-200		200	
DYNAMIC								
Turn-On Time	t _{ON}	V _{NO} = 3V	T _A = +25°C		43		125	ns
			T _A = T _{MIN} to T _{MAX}				175	
Turn-Off Time	t _{OFF}	V _{NO} = 3V	T _A = +25°C		18		75	ns
			T _A = T _{MIN} to T _{MAX}				125	
On-Channel Bandwidth	BW	Signal = 0dBm, 50Ω in and out, Figure 4			100			MHz
Charge Injection (Note 3)	Q	V _{GEN} = 0V, R _{GEN} = 0V, C _L = 1.0nF, Figure 3	T _A = +25°C		2		10	pC
SUPPLY								
Positive Supply Current	I+	V+ = 5.5V, V _{IN} = 0V or V+, all channels on or off			-1		1	μA

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ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = 2.7V to 3.3V $\pm 10\%$, GND = 0V, V_{INH} = 2.4V, V_{INL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{COM} , V _{NO}	(Note 3)		0		V+	V
Channel On-Resistance	R _{ON}	V+ = 3V, I _{COM} = -1.0mA, V _{NO} = 1.5V	T _A = +25°C			170	Ω
			T _A = T _{MIN} to T _{MAX}			225	
DYNAMIC							
Turn-On Time (Note 3)	t _{ON}	V+ = 3V, V _{NO} or V _{NC} = 1.5V	T _A = +25°C		80	185	ns
			T _A = T _{MIN} to T _{MAX}			230	
Turn-Off Time (Note 3)	t _{OFF}	V+ = 3V, V _{NO} or V _{NC} = 1.5V	T _A = +25°C		28	150	ns
			T _A = T _{MIN} to T _{MAX}			200	
Charge Injection (Note 3)	Q	C _L = 1.0nF, V _{GEN} = 0V, R _{GEN} = 0V	T _A = +25°C		2	10	pC
SUPPLY							
Positive Supply Current	I+	V+ = 3.6V, V _{IN} = 0V or V+, all channels on or off		-1	0.001	1	μA

Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.

Note 3: Guaranteed by design.

Note 4: $\Delta R_{ON} = R_{ON}(\text{max}) - R_{ON}(\text{min})$.

Note 5: Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal range.

Note 6: Leakage parameters are 100% tested at maximum-rated hot temperature and guaranteed by correlation at +25°C.

Note 7: Off Isolation = $20\log_{10}(V_{COM} / V_{NO})$, V_{COM} = output, V_{NO} = input to off switch.

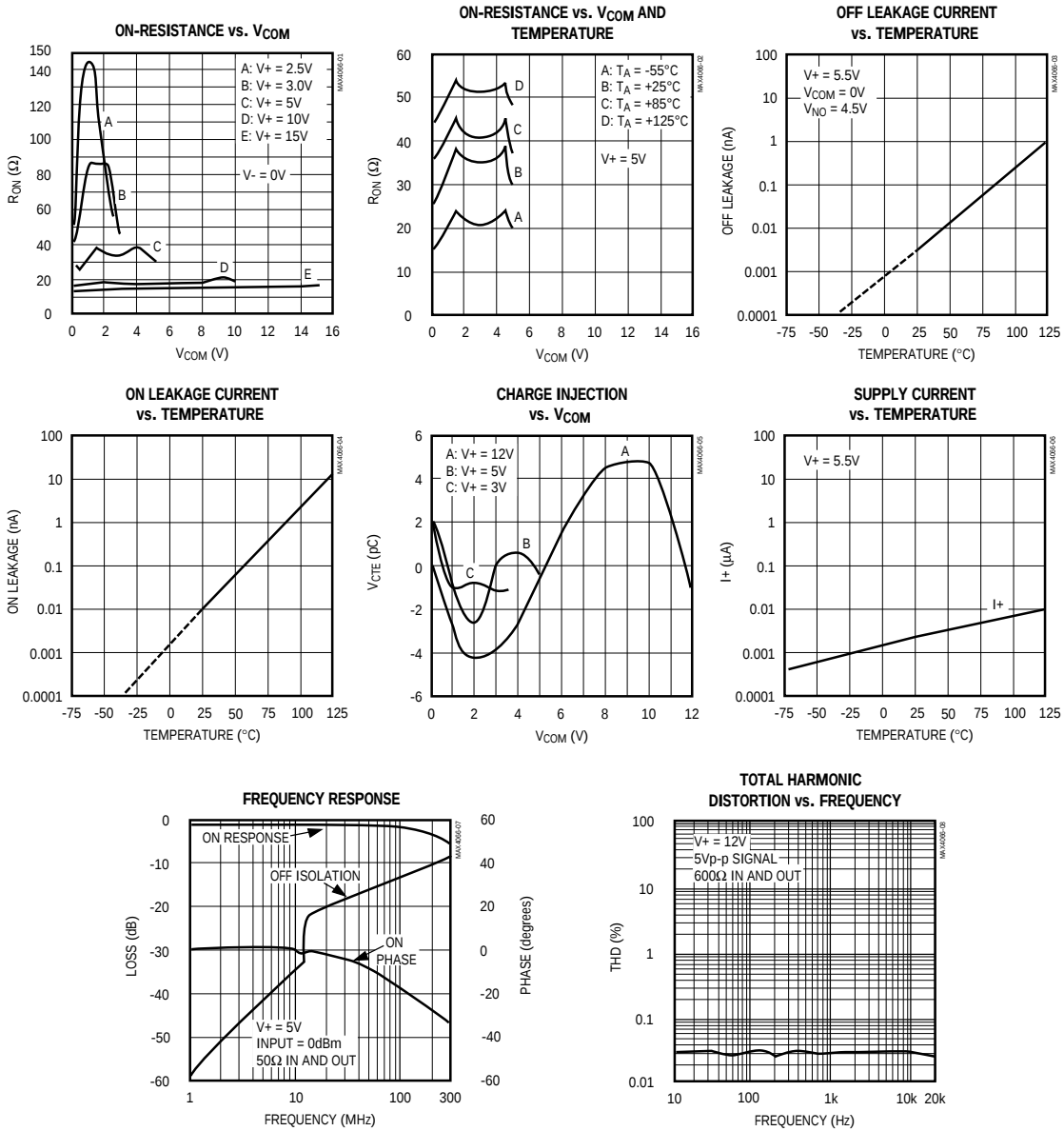
Note 8: Between any two switches.

MAX4066/MAX4066A

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Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)



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Pin Description

PIN		NAME	FUNCTION
DIP/SO	QSOP		
1, 3, 8, 11	1, 4, 9, 13	NO1–NO4	Analog Switch Normally Open Terminal (bidirectional)
2, 4, 9, 10	2, 5, 10, 12	COM1–COM4	Analog Switch Common Terminal (bidirectional)
—	3, 11	N.C.	Not internally connected
13, 5, 6, 12	15, 6, 7, 14	IN1–IN4	Logic Control Inputs
7	8	GND	Ground
14	16	V+	Positive Supply Voltage

MAX4066/MAX4066A

Applications Information

Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings may cause permanent damage to the devices. Always sequence V+ on first, followed by the logic inputs. If power-supply sequencing is not possible, add two small signal diodes in series with supply pins for overvoltage protection (Figure 1). Adding diodes reduces the analog signal range to 1V below V+ and 1V above GND, but low switch resistance and low leakage characteristics are unaffected. Device operation is unchanged, and the difference between V+ and GND should not exceed 17V.

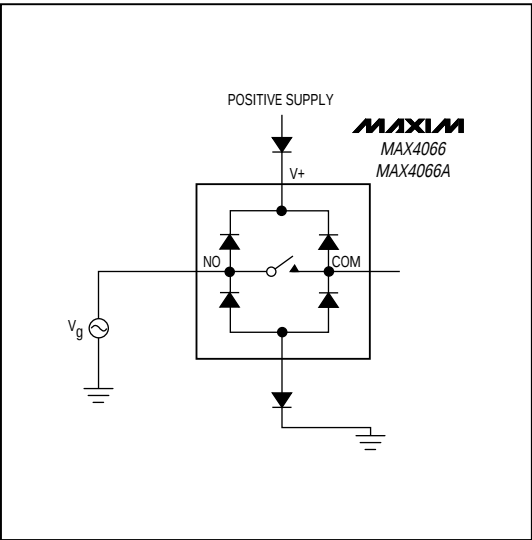


Figure 1. Overvoltage Protection Using Two External Blocking Diodes

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Test Circuits/Timing Diagrams

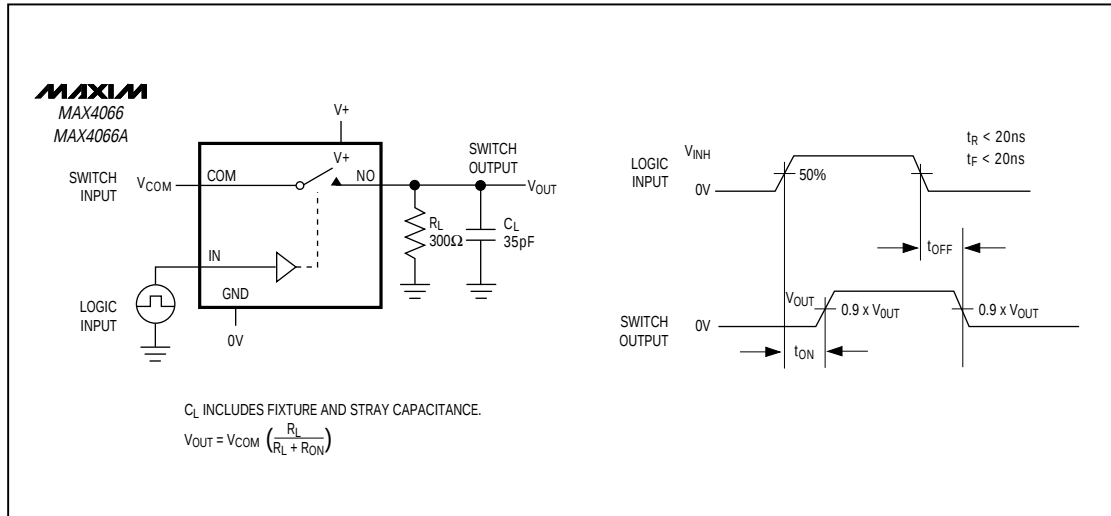


Figure 2. Switching Time

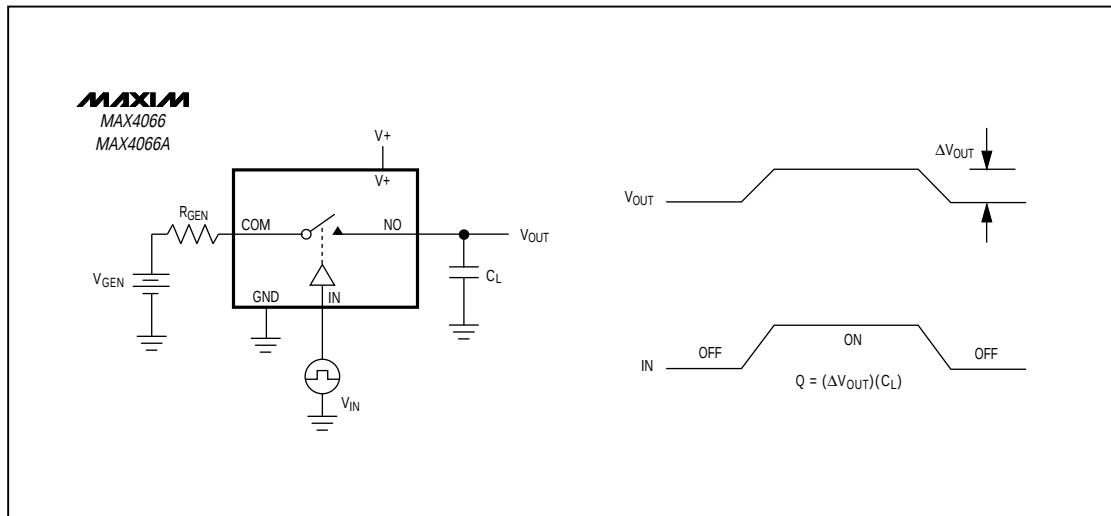


Figure 3. Charge Injection

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Test Circuits (continued)

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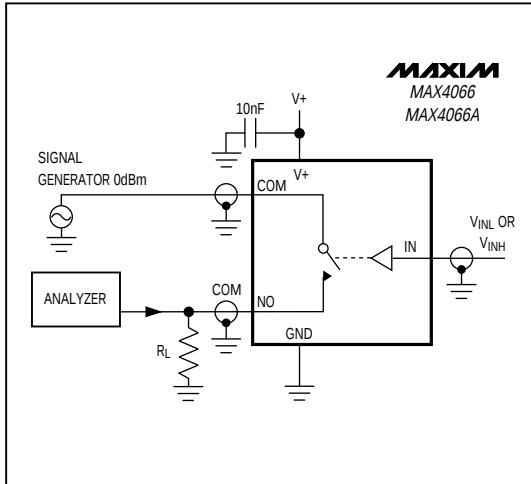


Figure 4. Off Isolation/On-Channel Bandwidth

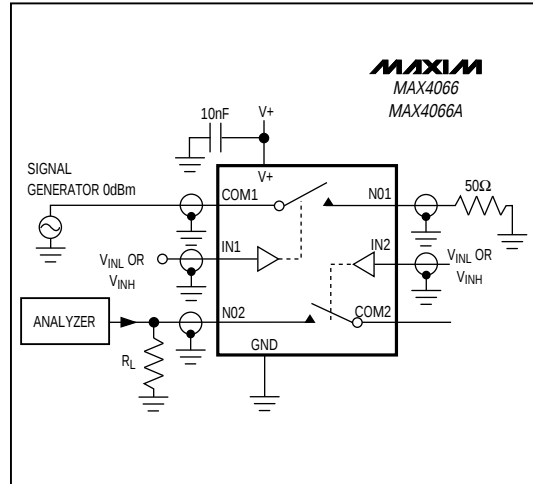


Figure 5. Crosstalk

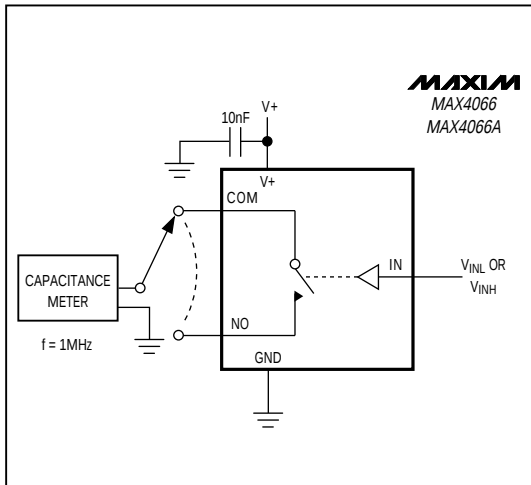


Figure 6. Channel Off/On Capacitance

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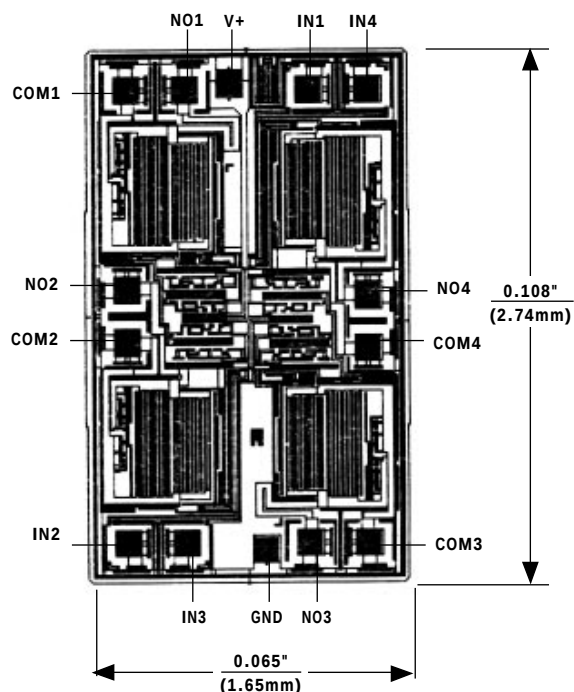
_Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
MAX4066EPD	-40°C to +85°C	14 Plastic DIP
MAX4066ESD	-40°C to +85°C	14 Narrow SO
MAX4066MJD	-55°C to +125°C	14 CERDIP**
MAX4066ACPD	0°C to +70°C	14 Plastic DIP
MAX4066ACSD	0°C to +70°C	14 Narrow SO
MAX4066ACEE	0°C to +70°C	16 QSOP
MAX4066AC/D	0°C to +70°C	Dice*
MAX4066AEPD	-40°C to +85°C	14 Plastic DIP
MAX4066AESD	-40°C to +85°C	14 Narrow SO
MAX4066AEEE	-40°C to +85°C	16 QSOP
MAX4066AMJD	-55°C to +125°C	14 CERDIP**

* Contact factory for dice specifications.

** Contact factory for availability.

Chip Topography



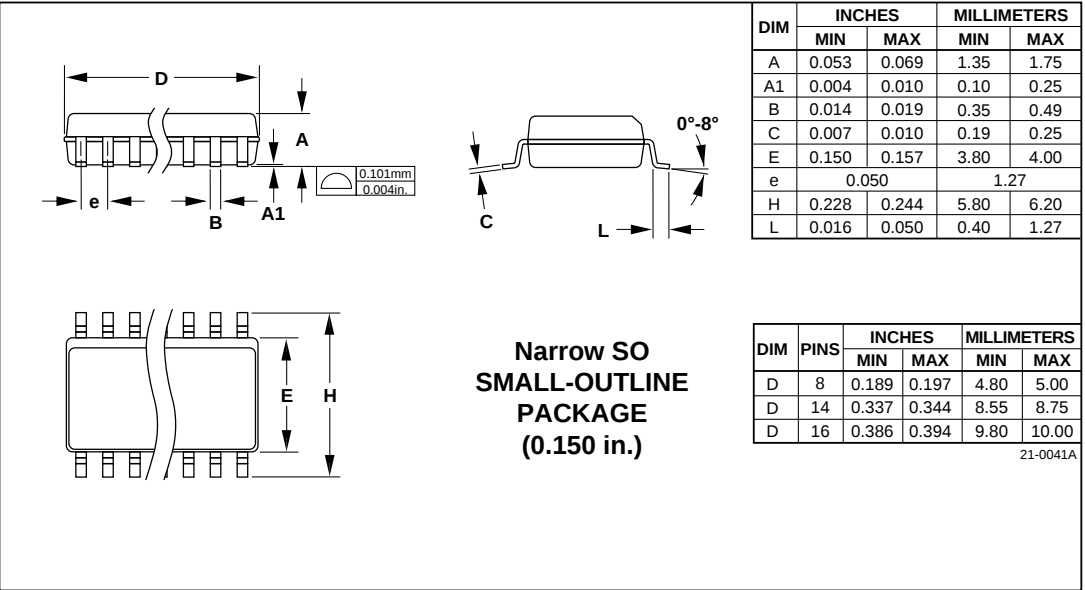
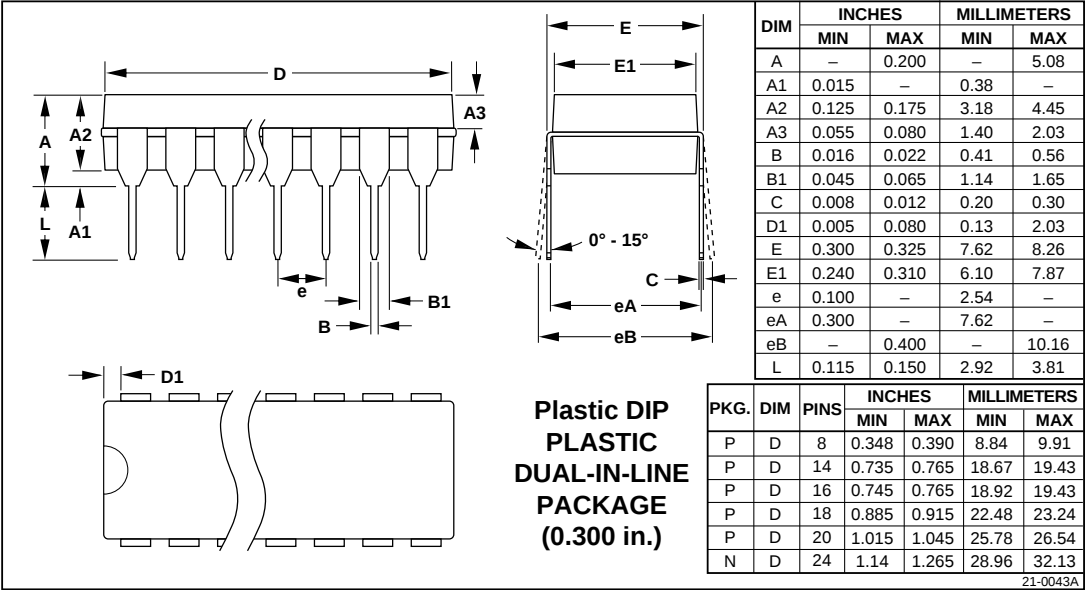
TRANSISTOR COUNT: 69

SUBSTRATE CONNECTED TO V+

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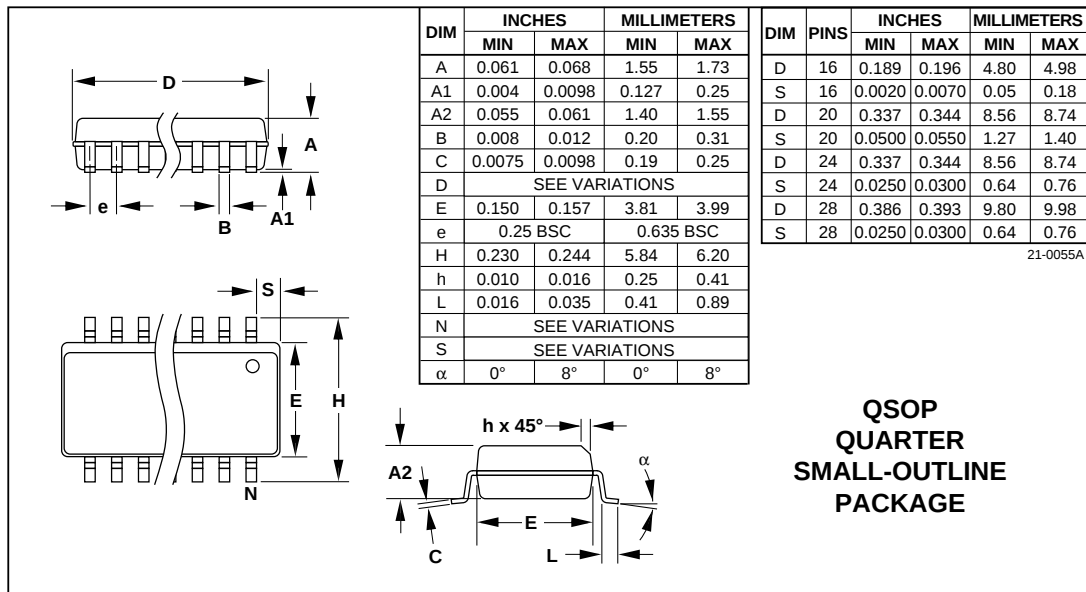
Package Information

MAX4066/MAX4066A



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Package Information (continued)



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