ABSOLUTE MAXIMUM RATINGS

IN_, ON_, OUT_, ISET, FLT_ to GND	0.3V to +6V
IN_ to OUT_ (when disabled) (Note 1)	6V to +6V
IN_ to OUT_ (when enabled) (Note 2)	1.6A to +1.6ARMS
FLT Sink Current	20mA

Continuous Power Dissipation ($T_A = +70^{\circ}C$) 10-Pin TDFN 3mm x 3mm	
(derate 24.4mW/°C above +70°C)	1952mW
Operating Temperature Range	40°C to +85°C
Junction Temperature	+160°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Note 1: Reverse current (current from OUT_ to IN_) is blocked when disabled.

Note 2: Forward current (current from IN_ to OUT_) is internally limited. Reverse current, from OUT_ to IN_, is not limited when the device is enabled and must be kept below 1.5A_{RMS} to prevent permanent device damage. When the MAX1558/MAX1558H are disabled, the switch turns off and reverse current is internally blocked.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND, **T_A = 0°C to +85°C**, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 3)

PARAMETER	SYMBOL	CON	NDITIONS	MIN	ТҮР	MAX	UNITS	
Supply Voltage Range				2.75		5.50	V	
		$V_{IN} = 5V, T_A = +25$	5°C		55	75		
Switch On-Resistance	Ron	$V_{IN_{-}} = 3.3V, T_{A} = +$	25°C		64		mΩ	
		$V_{IN_{}}$ = 4.4V, T_{A} = 0°C to +85°C				105		
Standby Supply Current		Both switches disat		3	6	μA		
Quiescent Supply Current		Both switches enab	oled		45	75	μA	
			Vouta = Voutb = 0V		0.03	10		
OUT_ Off-Leakage Current		Switches disabled	$V_{OUTA} = V_{OUTB} = 5V$		0.03		μA	
Reverse Leakage Current		V _{IN} = 0V, V _{OUTA} = switches disabled	$V_{OUTB} = 5V$, both		0.03		μA	
Undervoltage-Lockout Threshold	VUVLO	Rising edge, 3% hy	vsteresis	2.3	2.5	2.7	V	
Continuous Load Current		$R_{ISET} = 26k\Omega$		1.2			А	
			$R_{ISET} = 26k\Omega$	1.20	1.4	1.60		
Current-Limit Threshold		V _{IN} V _{OUT} _ = 0.5V	$R_{ISET} = 39k\Omega$	0.80	0.925	1.05	A	
		0.5V	$R_{ISET} = 60k\Omega$	0.50	0.6	0.70		
			$R_{ISET} = 26k\Omega$	1.45	2.0	2.60		
Peak Short-Circuit Current Limit	ISHORT	$V_{OUT} = 0V$ (I_{OUT} pulsing)	$R_{ISET} = 39k\Omega$		1.40		A(PEAK)	
		(IOUT_ puising)	$R_{ISET} = 60k\Omega$		0.90			
			$R_{ISET} = 26k\Omega$		0.55			
RMS Short-Circuit Current Limit	ISHORT	V _{OUT} = 0V (I _{OUT} pulsing)	$R_{ISET} = 39k\Omega$		0.37		ARMS	
		(1001_puising)	$R_{ISET} = 60k\Omega$	0.23			7	
Short-Circuit Continuous Current- Limit Transition Threshold		(Note 4)			1		V	
Fault-Blanking Timeout Period		From ILIMIT condition	on to 50% of V _{FLT_}	8	20	40	ms	
Turn-On Delay	ton	$R_{OUT} = 10\Omega, C_{OUT}$ include rise time (free V _{OUT} = 10% V _{IN})	om ON asserted to	0.5	1.4	4.0	ms	

MAX1558/MAX1558H

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND, $T_A = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 3)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Output Rise Time	trise	C_{OUT} = 1µF, R_{OUT} = 10 Ω , (from 10% to 90% of V_{OUT})		4		ms
Turn-Off Delay from ON	tOFF	$R_{OUT} = 10\Omega$, $C_{OUT} = 1\mu$ F, does not include rise time (from ON deasserted to $V_{OUT} = 90\% V_{IN}$)		100	1000	μs
Output Fall Time	^t FALL	$C_{OUT_} = 1\mu$ F, $R_{OUT_} = 10\Omega$ (from 90% to 10% of $V_{OUT_}$)		3		ms
Thermal-Shutdown Threshold		10°C hysteresis		+160		°C
ONA/ONB Logic Input High	VIH	$V_{IN_{}} = 2.7V \text{ to } 4.0V$	1.6			v
Voltage	VIH	$V_{IN_{}} = 4.0V$ to 5.5V	2.0			v
ONA/ONB Logic Input Low	VIL	$V_{IN} = 2.7V$ to 4.0V			0.6	v
Voltage	VIL	V_{IN} = 4.0V to 5.5V			0.8	v
Logic Input Current		$V_{ON} = 0V \text{ or } V_{IN}$	-1		+1	μA
FLT_ Output Low Voltage		I _{SINK} = 1mA			0.4	V
FLT_ Output High Leakage Current		$V_{IN} = V_{FLT} = 5.5V$			1	μA
Autorestart Current		In latched-off state, V _{OUT} = 0V	10	25	50	mA
Autorestart Threshold		In latched-off state, rising	0.4	0.5	0.6	V
Autorestart Delay		In latched-off state, V _{OUT} > 1V	8	20	40	ms

ELECTRICAL CHARACTERISTICS

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND, T_A = -40°C to +85°C, unless otherwise noted.)

PARAMETER	SYMBOL	COND	ITIONS	MIN	ТҮР	MAX	UNITS
Supply Voltage Range				2.7		5.5	V
Switch On-Resistance	Deu	$V_{IN} = 5V, T_A = +25^{\circ}C$			75	mΩ	
Switch On-Resistance	R _{ON}	$V_{IN} = 4.4V, T_A = 0^{\circ}C$	to +85°C			105	11152
Standby Supply Current		Both switches disable	d			6	μA
Quiescent Supply Current		Both switches enabled	d			75	μA
OUT_Off-Leakage Current		Switches disabled, VC	OUTA = VOUTB = 0V			10	μA
Undervoltage-Lockout Threshold	VUVLO	Rising edge, 3% hyste	eresis	2.3		2.7	V
Continuous Load Current		$R_{ISET} = 26k\Omega$		1.2			А
			$R_{ISET} = 26k\Omega$	1.20		1.60	
Current-Limit Threshold		V _{IN} V _{OUT} _ = 0.5V	$R_{ISET} = 39k\Omega$	0.80		1.05	А
			$R_{ISET} = 60 k\Omega$	0.50		0.70	
Peak Short-Circuit Current Limit	ISHORT	V _{OUT} = 0V (I _{OUT} pulsing)	/OUT_ = 0V BIGGT = 26k0			2.60	A(PEAK)
Fault-Blanking Timeout Period		From ILIMIT condition	to 50% of V _{FLT_}	8		40	ms

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND, T_A = -40°C to +85°C, unless otherwise noted.)

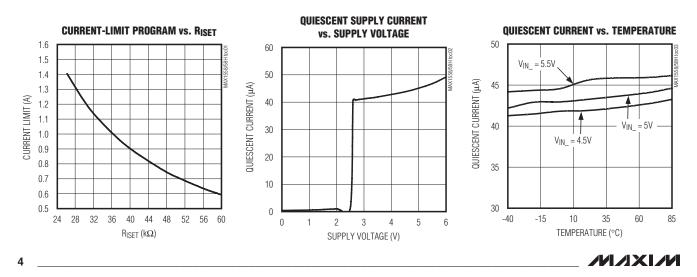
PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Turn-On Delay	ton	$R_{OUT} = 10\Omega$, $C_{OUT} = 1\mu$ F, does not include rise time (from ON asserted to $V_{OUT} = 10\% V_{IN}$)	0.5		4.0	ms
Turn-Off Delay from ON	tOFF	$R_{OUT} = 10\Omega$, $C_{OUT} = 1\mu$ F, does not include rise time (from ON deasserted to $V_{OUT} = 90\% V_{IN}$)			1000	μs
ONA/ONB Logic Input High	VIH	V_{IN} = 2.7V to 4.0V	1.6			V
Voltage	VIH	V _{IN} _ = 4.0V to 5.5V	2.0			v
ONA/ONB Logic Input Low	VIL	V _{IN} _ = 2.7V to 4.0V			0.6	v
Voltage	VIL	V _{IN} _ = 4.0V to 5.5V			0.8	v
Logic Input Current		$V_{ON} = 0V \text{ or } V_{IN}$	-1		+1	μA
FLT_ Output Low Voltage		I _{SINK} = 1mA			0.4	V
FLT_ Output High Leakage Current		$V_{IN} = V_{\overline{FLT}} = 5.5V$			1	μA
Autorestart Current		In latched-off state, V _{OUT} = 0V	10		50	mA
Autorestart Threshold		In latched-off state, rising	0.4		0.6	V
Autorestart Delay		In latched-off state, V_{OUT} > 1V	8		40	ms

Note 3: Specifications from 0°C to -40°C are guaranteed by design but not 100% tested.

Note 4: The output voltage at which the device transitions from short-circuit current limit to continuous current limit. See the *Output-Current Fault Protection* section.

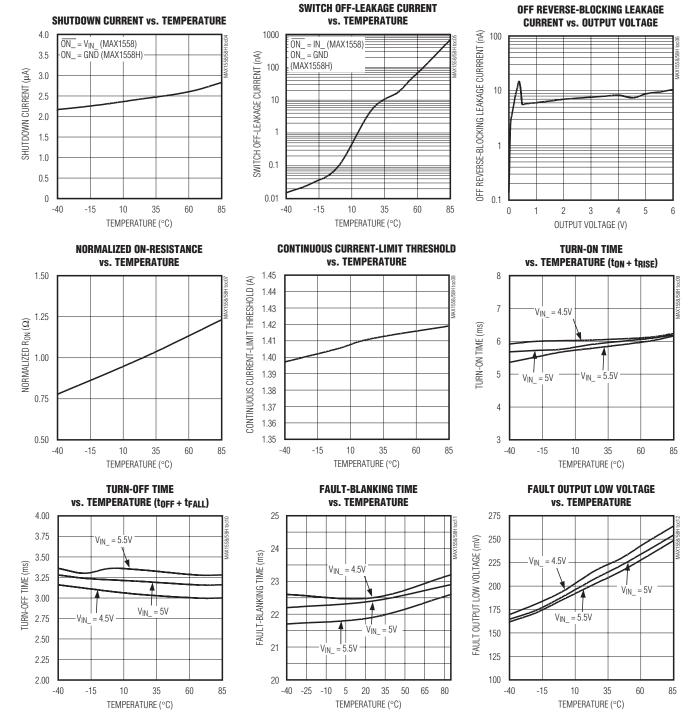
Typical Operating Characteristics

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND. Typical values are at T_A = +25°C, unless otherwise noted.)



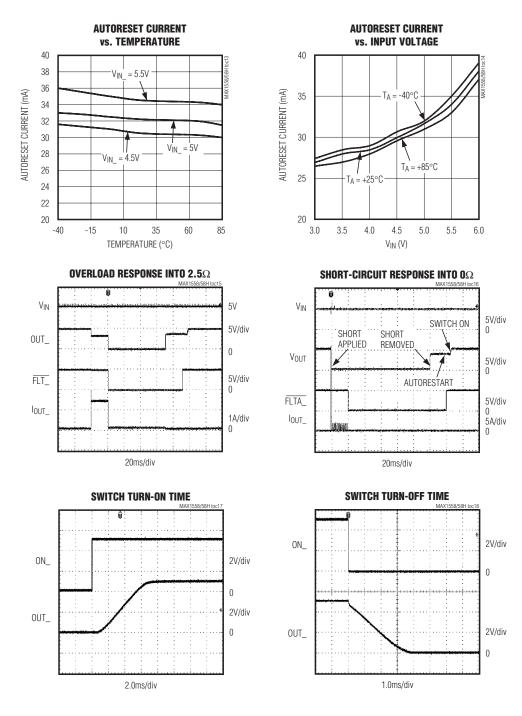


 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND. Typical values are at T_A = +25°C, unless otherwise noted.)



Typical Operating Characteristics (continued)

 $(V_{INA} = V_{INB} = 5V, V_{\overline{ONA}} = V_{\overline{ONB}} = 0V (MAX1558), V_{ONA} = V_{ONB} = 5V (MAX1558H), R_{ISET} = 26k\Omega$ to GND. Typical values are at T_A = +25°C, unless otherwise noted.)





_Pin Description

PIN	NAME	FUNCTION
1	INA	Power Input for OUTA. Connect INA and INB together and bypass with a 0.1µF capacitor to ground. Load conditions may require additional bulk capacitance to prevent the input from being pulled down.
2	ONA *(ONA)	Control Input for Switch A. Can be driven higher than IN_ without damage. A logic low turns switch A on for the MAX1558. A logic high turns switch A on for the MAX1558H.
3	ISET	Current-Limit Adjust for Both Channels. Connect a resistor between $26k\Omega$ to $60k\Omega$ from ISET to ground to program the current limit. See <i>Setting the Current Limit</i> in the <i>Applications Information</i> section.
4	ONB *(ONB)	Control Input for Switch B. Can be driven higher than IN_ without damage. A logic low turns switch B on for the MAX1558. A logic high turns switch B on for the MAX1558H.
5	INB	Power Input for OUTB. Connect INB and INA together and bypass with a 0.1µF capacitor to ground. Load conditions may require additional bulk capacitance to prevent the input from being pulled down.
6	OUTB	Power Output for Switch B. Connect a 1µF capacitor from OUTB to ground. The load condition may require additional bulk capacitance.
7	FLTB	Fault Indicator Output for Switch B. This open-drain output goes low when switch B is in thermal shutdown or UVLO or in a sustained (>20ms) current-limit or short-circuit condition.
8	GND	Ground
9	FLTA	Fault Indicator Output for Switch A. This open-drain output goes low when switch A is in thermal shutdown or UVLO or in a sustained (>20ms) current-limit or short-circuit condition.
10	OUTA	Power Output for Switch A. Connect a 1µF capacitor from OUTA to ground. The load condition may require additional bulk capacitance.
	PAD	Exposed Pad, Internally Connected to GND. Connect exposed pad to a large copper trace for maximum power dissipation.

*() are for the MAX1558H only.

Detailed Description

The MAX1558/MAX1558H are dual current-limited switches designed for USB applications. They have two independent switches, each with its own enable control input and autoreset function. Each switch has an error-flag output to notify the USB controller when current-limit, short-circuit, undervoltage-lockout, or thermal-shutdown conditions occur (Figure 2). The MAX1558/MAX1558H operate from a 2.7V to 5.5V supply. Each output can supply up to 1.2A. Current limit for both channels is set with one external resistor.

The MAX1558/MAX1558H have independent thermal shutdown for each switch in the event of a prolonged overload. An autoreset function monitors the output and automatically turns the switch on when the overload is removed. Separate current limiting and thermal-shutdown circuits permit each switch to operate independently, improving system robustness.

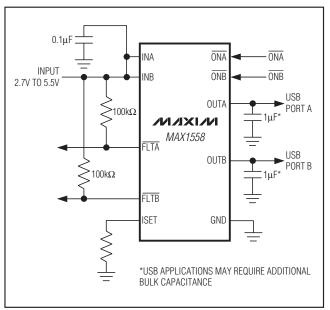


Figure 1. Typical Application Circuit



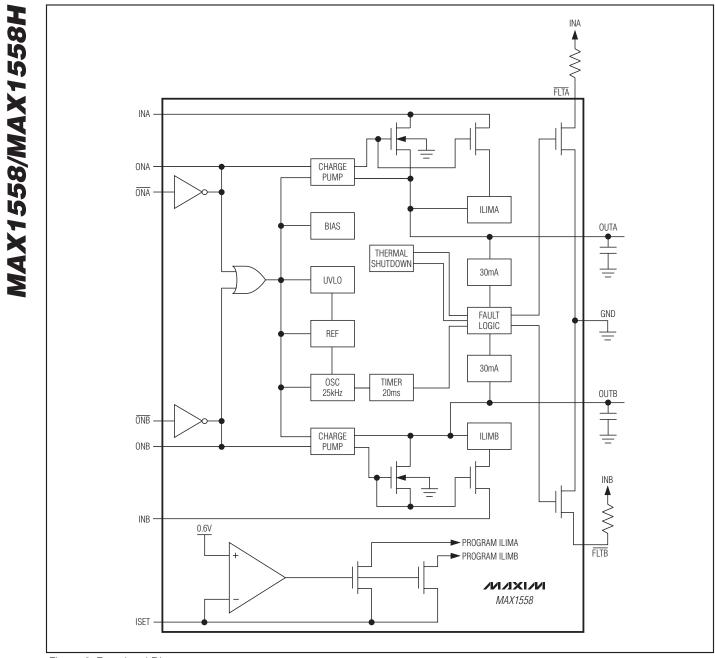


Figure 2. Functional Diagram

8

Autoreset Mode

Undervoltage Lockout and Input Voltage Requirements

The MAX1558/MAX1558H include a UVLO circuit to prevent erroneous switch operation when the input voltage is low during startup and brownout conditions. Operation is inhibited when V_{IN} is less than 2.5V.

Output-Current Fault Protection

The current limit for both switch outputs is programmed by a resistor between $26k\Omega$ and $60k\Omega$ connected from ISET to ground. See the Current-Limit Program vs. RISET curve in the *Typical Operating Characteristics* and *Setting the Current Limit* in the *Applications Information* section. The MAX1558 reduces the gate drive to the MOSFET switch when the programmed current-limit value is reached. If the current-limit program value is reached, the part reduces gate drive to regulate at the current limit. If the current limit persists for 20ms (typ), the output turns off and the fault flag is set until the fault is removed, as detected by the autoreset circuit.

When the the output drops below 1V (typ), the current limit is set 30% above the programmed level and pulsed. This lowers the RMS current for a short-circuit condition, which reduces system load current and power dissipation. If a short circuit persists for 20ms, the output turns off and the fault flag is set. The autoreset circuit automatically restarts the part if the fault is removed.

Fault Blanking

The MAX1558/MAX1558H switches can reach current limit in normal operation when powering up or driving heavy capacitive loads. To differentiate these conditions from short circuits or sustained overloads, the MAX1558/MAX1558H have independent fault-blanking circuits in each switch. When a load transient causes the device to enter the current limit, an internal counter monitors the duration of the fault. For load faults exceeding the 20ms fault-blanking time, the offending switch turns off, the corresponding FLT_ signal asserts low, and that channel enters autoreset mode (see the *Autoreset Mode* section). Only current-limit and short-circuit faults are blanked. Thermal-overload faults and input-voltage drops below the UVLO threshold immediately cause the switch to turn off and FLT_ to assert low.

Fault blanking allows the MAX1558 to handle USB loads that may not be fully compliant with USB specifications. USB loads with additional bypass capacitance and/or large startup currents can be successfully powered even while protecting the upstream power source. No fault is indicated if the switch is able to bring up the load within the 20ms blanking period.

If an output fault is detected for more than the 20ms blanking time, the output latches off and the \overline{FLT}_{-} output goes low. The MAX1558/MAX1558H then source 30mA to the faulted output and monitor the output voltage to determine when the overload is removed. If the voltage on the output rises above 0.5V for more than 20ms, the fault resets and the output turns on. The MAX1558 can also be reset from fault manually by toggling \overline{ON}_{-} (ON_ for the MAX1558H) for that channel.

Reverse Current Blocking

The USB specification does not allow an output device to source current back into the USB port. However, the MAX1558/MAX1558H are designed to safely power noncompliant devices. When disabled, each output is switched to a high-impedance state, blocking reversecurrent flow from the output back to the input. However, during normal operation with the device enabled, the MAX1558/MAX1558H are bidirectional switches.

Thermal Shutdown

The MAX1558/MAX1558H feature independent thermal shutdown for each switch channel, allowing one switch to deliver power even if the other switch has a fault condition. When the junction temperature exceeds $+160^{\circ}$ C, the switch turns off and the FLT_ output goes low immediately; fault blanking does not occur during thermal limit. When the junction cools by 10°C, the switch turns on again. If the fault-overload condition continues, the switch cycles on and off, resulting in a pulsed output, saving battery power.

Fault Indicators

The MAX1558/MAX1558H provide an open-drain fault output (\overline{FLT}_{-}) for each switch. For most applications, connect \overline{FLT}_{-} to IN_ through a 100k Ω pullup resistor. \overline{FLT}_{-} goes low when any of the following conditions occur:

- The input voltage is below the UVLO threshold.
- The switch junction temperature exceeds the +160°C thermal-shutdown temperature limit.
- The switch is in current-limit or short-circuit limit mode after the 20ms fault-blanking period is exceeded.
- The switch is in autoreset mode.

After the fault condition is removed, the FLT_ output deasserts after a 20ms delay. Ensure that the MAX1558/MAX1558H have adequate input bypass capacitance to prevent glitches from triggering FLT_ outputs. Glitches that cause the input supply to drop below 2.5V may cause the undervoltage lockout to trigger, turning the outputs off and asserting the FLT signals.



Applications Information

Setting the Current Limit

A resistor from ISET to ground programs the currentlimit value for both outputs. Use a resistor between $26k\Omega$ and $86k\Omega$ to set the current limit according to the formula:

ILIM(TYPICAL) = 36400 / RISET (Amps)

Do not use RISET values below $26k\Omega$ because the maximum current rating of the device may be exceeded. RISET values larger than $60k\Omega$ are not recommended and do not provide a lower limit current than 500mA.

If the output drops below 1V (typ), the MAX1558 shifts to a short-circuit current-limit threshold that is 30% above the programmed level given by the ILIM(TYPICAL) formula. If the short-circuit threshold is exceeded, the switch shuts off immediately (no 20ms delay) and ramps the current back up in approximately 3ms. If the short persists and the current ramps all the way up to the short-circuit limit again, the switch again turns off. If the short still persists, the output pulses this way for 20ms, at which time the switch turns off and autoreset mode begins.

Input Capacitor

INA and INB provide the power for all control and charge-pump circuitry and must be connected together externally. Connect a capacitor from IN_ to ground to limit the input-voltage drop during momentary output short-circuit conditions. A 0.1μ F ceramic capacitor is required for local decoupling; higher capacitor values further reduce the voltage drop at the input. When driving inductive loads, a larger capacitance prevents voltage spikes from exceeding the MAX1558/MAX1558Hs' absolute maximum ratings.

Output Capacitor

Place a 1µF or greater capacitor at each output for noise immunity. When starting up into very large capacitive loads, the switch may pulse the output current at the short-circuit current-limit program level until the output voltage rises above 1V. Then, the capacitor continues to charge at the full, continuous current-limit program level. There is no limit to the output capacitor size, but to prevent a startup fault assertion, the capacitor must charge up within the fault-blanking delay period. Typically, starting up into a 500μ F or smaller capacitor does not trigger a fault output. In addition to bulk capacitance, small-value (0.1µF or greater) ceramic capacitors improve the output's resilience to electrostatic discharge (ESD).

Driving Inductive Loads A wide variety of devices (mice, keyboards, cameras, and printers) can load the USB port. These devices com-

and printers) can load the USB port. These devices commonly connect to the port with cables, which can add an inductive component to the load. This inductance causes the output voltage at the USB port to ring during a load step. The MAX1558/MAX1558H are capable of driving inductive loads, but avoid exceeding the devices' absolute maximum ratings. Usually the load inductance is relatively small, and the MAX1558/MAX1558Hs' input includes a substantial bulk capacitance from an upstream regulator as well as local bypass capacitors, limiting overshoot. If severe ringing occurs due to large load inductance, clamp the MAX1558/MAX1558Hs' output below +6V and above -0.3V.

Turn-On and Turn-Off Behavior

In the absence of faults, the MAX1558/MAX1558Hs' internal switches turn on and off slowly under the control of the ON_ inputs. Transition times for both edges are approximately 4ms. The slow charge-pump switch drive minimizes load transients on the upstream power source. Under thermal fault and UVLO, the power device turns off rapidly (100ns) to protect the power device.

Layout and Thermal Dissipation

To optimize the switch response time to output shortcircuit conditions, keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors no more than 5mm from device leads. All IN_ and OUT_ pins must be connected with short traces to the power bus. Wide power-bus planes provide superior heat dissipation through the switch IN_ and OUT_ pins. While the switches are on, power dissipation is small and the package temperature change is minimal. Calculate the power dissipation for this condition as follows:

$P = (I_{OUT})^2 R_{ON}$

For the maximum operating current (I_{OUT} = 1.2A) and the maximum on-resistance of the switch (125m Ω), the power dissipation is:

$$P = (1.2A)^2 \times 0.125\Omega = 180 \text{mW}$$
 per switch

The worst-case power dissipation occurs when the switch is in current limit and the output is greater than 1V. The instantaneous power dissipated in each switch is the voltage drop across the switch multiplied by the current limit. The fault-blanking circuit turns the output off if the fault persists for 20ms, while the autoreset circuit can turn it back on after 20ms in the off state. Thus, the average worst-case power is approximately 50% of the instantaneous value.

$$P = 0.5 \times (I_{LIM}) \times (V_{IN} - V_{OUT})$$

/N/XI/N

For a 5V input and 1V output, the maximum possible power dissipation per switch is:

 $P = 0.5 \times (1.5A) \times (5V - 1V) = 3W$

Since the package power dissipation is limited to 1952mW, the MAX1558/MAX1558H die temperature exceeds the thermal-shutdown threshold and the switch turns off. After the die temperature cools by 10°C, the switch turns on again. If the fault-overload condition continues, the switch cycles on and off with a duty cycle and period that is a function of the ambient temperature and PC board layout.

A short across the output for more than 20ms places the part in autoreset mode. With V_{IN} = 5V and an autoreset test current of 30mA, the power dissipation for a short-circuited output is given by:

$$P = (30mA) \times 5V = 0.15W$$

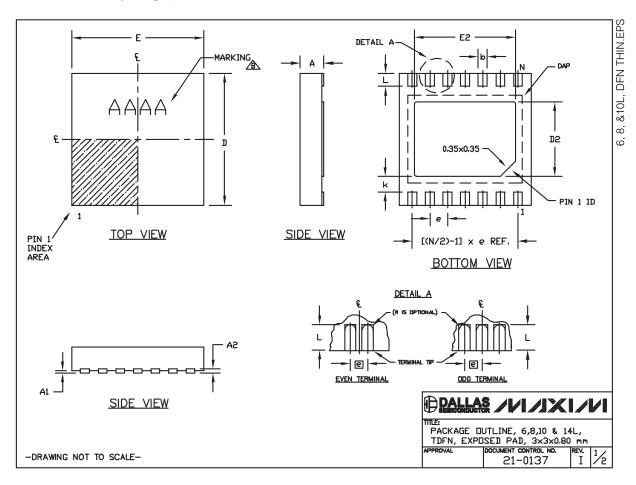
_Chip Information

TRANSISTOR COUNT: 2932 PROCESS: BICMOS



Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to <u>www.maxim-ic.com/packages</u>.)



Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)

COMMON	DIMENS	SIONS		PACKAGE VA	RIAT	IONS					
SYMBOL	MIN.	MAX.		PKG. CODE	Ν	D2	E2	е	JEDEC SPEC	b	[(N/2)-1] x e
А	0.70	0.80		T633-2	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF
D	2.90	3.10		T833-2	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
E	2.90	3.10		T833-3	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
A1	0.00	0.05		T1033-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF
L	0.20	0.40		T1033-2	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF
k	0.25	MIN.		T1433-1	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
A2	0.20	REF.		T1433-2	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
NOTES:											
1. ALL E 2. COPL 3. WARP 4. PACK 5. DRAW 6. "N" E 7. NUME	ANARITY AGE SH AGE LEI ING CO S THE BER OF	′SHALL IALL NC NGTH/P NFORMS TOTAL N LEADS	NOT EXC T EXCEEL ACKAGE N TO JEDI NUMBER C SHOWN A	. ANGLES IN CEED 0.0B m) 0.10 mm. WIDTH ARE CC EC M0229, E SF LEADS. IRE FOR REFI RIENTATION R	m. DNSID XCEP EREN	DERED AS S T DIMENSIO CE ONLY.	NS "D2" AN		C(S). ND T1433-1 & T	1433–2.	

MAX1558/MAX1558H

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
2	3/08	Changed "UL Listing Pending" to "UL Recognized: UL # E211395"	1

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14

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