

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	LMV321	4.0
		LMV358	4.0
		LMV324	4.5
ESD MM	Machine Model ESD Protection	LMV321	350
		LMV358	350
		LMV324	250
	Differential Input Voltage	±Supply Voltage	V
V ⁺ -V ⁻	Supply Voltage	5.5	V
	Output Short Circuit to V ⁺	(Note 5)	
	Output Short Circuit to V ⁻	(Note 6)	
T _{ST}	Storage Temperature	-65 to +150	°C
T _J	Maximum Junction Temperature	+150	°C

Notes: 4. Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.
5. Shorting output to V⁺ will adversely affect reliability.
6. Shorting output to V⁻ will adversely affect reliability.

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V ⁺ -V ⁻	Supply Voltage	2.7 to 5.5	V
T _A	Operating Ambient Temperature Range	-40 to +125	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

2.7V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = +25°C, V⁺ = 2.7V, V⁻ = 0V, V_{CM} = 1.0V, V_O = V⁺/2 and R_L > 1 MΩ.

Symbol	Parameter	Test Conditions	Min (Note 8)	Typ (Note 7)	Max (Note 8)	Unit
V _{OS}	Input Offset Voltage			1.7	7	mV
TCV _{OS}	Input Offset Voltage Average Drift			5		μV/°C
I _B	Input Bias Current			10		nA
I _{OS}	Input Offset Current			5	50	nA
CMRR	Common Mode Rejection Ratio	0V ≤ V _{CM} ≤ 1.7V	50	63		dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V ⁺ ≤ 5V, V _O = 1V	50	60		dB
V _{CMR}	Input Common-Mode Voltage Range	For CMRR ≥ 50dB	0	-0.2		V
				1.9	1.7	
V _O	Output Swing	R _L = 10 kΩ to 1.35V	V ⁺ - 100	V ⁺ - 20		mV
				20	100	
I _S	Supply Current	LMV321 Single amplifier		110	140	μA
		LMV358 Both amplifiers		190	340	μA
		LMV324 All four amplifiers		340	680	μA

2.7V AC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_A = +25°C, V⁺ = 2.7V, V⁻ = 0V, V_{CM} = 1.0V, V_O = V⁺/2 and R_L > 1 MΩ.

GBWP	Gain-Bandwidth Product	C _L = 200 pF		1		MHz
Φ _m	Phase Margin			60		Deg
G _m	Gain Margin			10		dB
e _n	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{nV}{\sqrt{H_z}}$

Electrical Characteristics (cont.) (@T_A = +25°C, unless otherwise specified.)

5V DC Electrical Characteristics

 Unless otherwise specified, all limits guaranteed for T_A = +25°C, V⁺ = 5V, V⁻ = 0V, V_{CM} = 2.0V, V_O = V⁺/2 and R_L > 1 MΩ.

Symbol	Parameter	Test Conditions	Min (Note 8)	Typ (Note 7)	Max (Note 8)	Unit
V _{OS}	Input Offset Voltage	T _A = +25°C T _A = full range		1.7 7	9	mV
TCV _{OS}	Input Offset Voltage Average Drift			5		μV/°C
I _B	Input Bias Current	T _A = +25°C T _A = full range		15 250	500	nA
I _{OS}	Input Offset Current	T _A = +25°C T _A = full range		5 50	150	nA
CMRR	Common Mode Rejection Ratio	0V ≤ V _{CM} ≤ 4.0V	50	65		dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V ⁺ ≤ 5V V _O = 1V, V _{CM} = 1V	50	60		dB
V _{CMR}	Input Common-Mode Voltage Range	For CMRR ≥ 50dB	0 4.2	-0.2 4.0		V
A _V	Large Signal Voltage Gain	R _L = 2 kΩ (Note 9) T _A = +25°C T _A = full range	15 10	100		V/mV
V _O	Output Swing	R _L = 2 kΩ to 2.5V High level T _A = +25°C T _A = full range Low level T _A = +25°C T _A = full range R _L = 10 kΩ to 2.5V High level T _A = +25°C T _A = full range Low level T _A = +25°C T _A = full range	V ⁺ - 300 V ⁺ - 400 50 400 V ⁺ - 100 V ⁺ - 200 10 280	V ⁺ - 50 300 180		mV
I _O	Output Short Circuit Current	Sourcing, V _O = 0V Sinking, V _O = 5V	5 10	60 90		mA
I _S	Supply Current	LMV321 Single amplifier LMV358 Both amplifiers LMV324 All four amplifiers T _A = +25°C T _A = full range T _A = +25°C T _A = full range		110 190 340 1100	140 340 680	μA
θ _{JA}	Thermal Resistance Junction-to-Ambient	SOT353 (Note 10) SOT25 (Note 10) TSSOP-14 (Note 10) MSOP-8 (Note 10) SO-8 (Note 10) SO-14 (Note 10)		330 250 100 203 150 83		°C/W

5V AC Electrical Characteristics

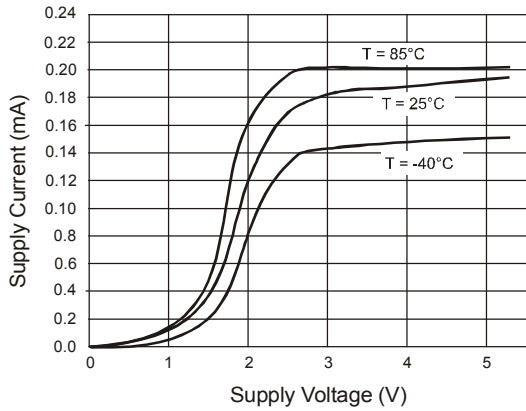
 Unless otherwise specified, all limits guaranteed for T_A = 25°C, V⁺ = 5V, V⁻ = 0V, V_{CM} = 2.0V, V_O = V⁺/2 and R_L > 1 MΩ.

Boldface limits apply at the temperature extremes.

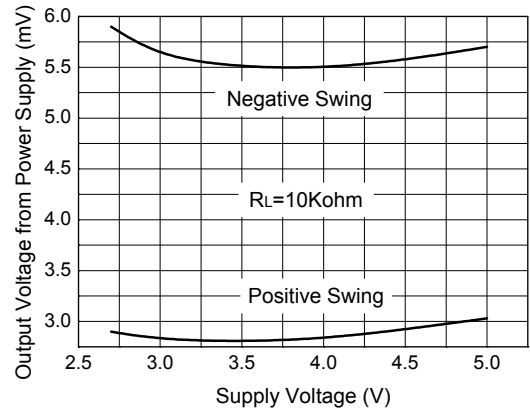
SR	Slew Rate	(Note 11)		1		V/μs
GBWP	Gain-Bandwidth Product	C _L = 200pF		1		MHz
Φ _m	Phase Margin			60		Deg
G _m	Gain Margin			10		dB
e _n	Input-Referred Voltage Noise	f > 50 kHz		23		$\frac{nV}{\sqrt{Hz}}$

- Notes:
7. Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not guaranteed on shipped production material.
 8. All limits are guaranteed by testing or statistical analysis.
 9. R_L is connected to V⁻. The output voltage is 0.5V ≤ V_O ≤ 4.5V.
 10. All numbers are typical, and apply for packages soldered directly onto a PC board in still air.
 11. Connected as voltage follower with 3V step input. Number specified is the slower of the positive and negative slew rates.

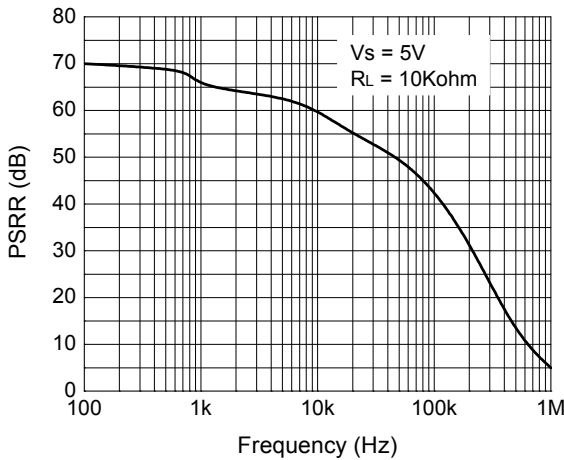
Typical Performance Characteristics ($V_S = +5V$, single supply, @ $T_A = +25^\circ C$, unless otherwise specified.)



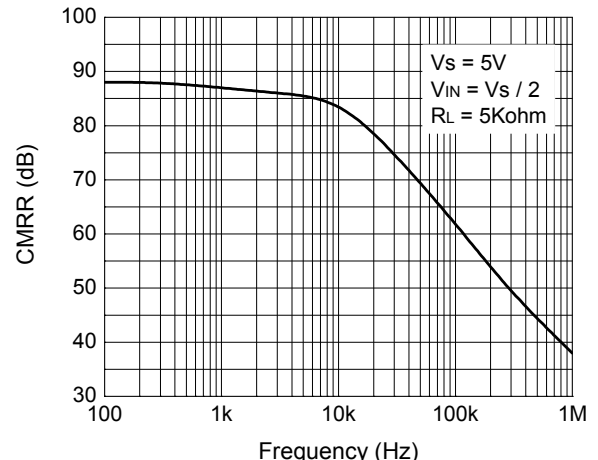
Supply Current vs. Supply Voltage



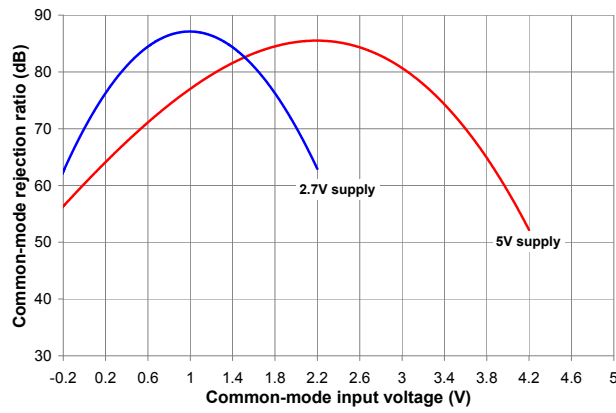
Output Voltage Swing vs. Supply Voltage



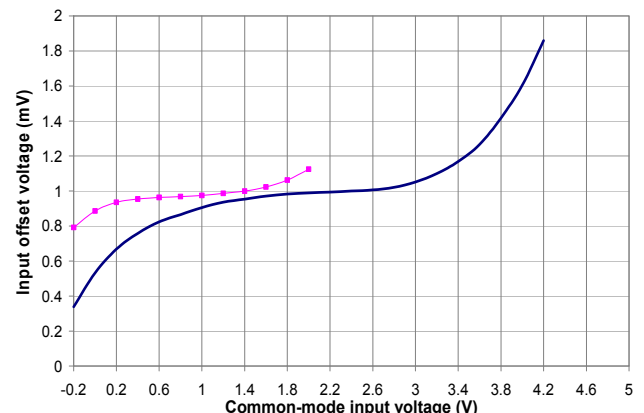
PSRR vs. Frequency



CMRR vs. Frequency

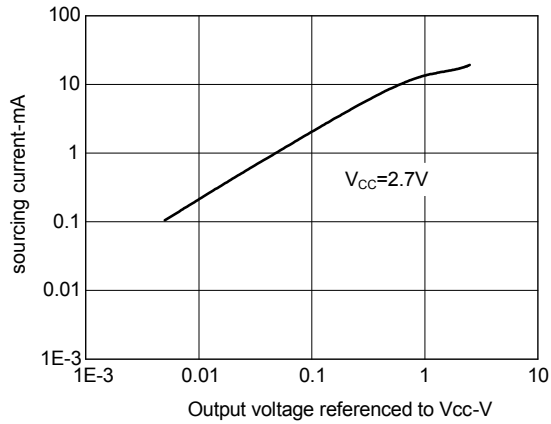


CMRR vs. Input Common Mode Voltage

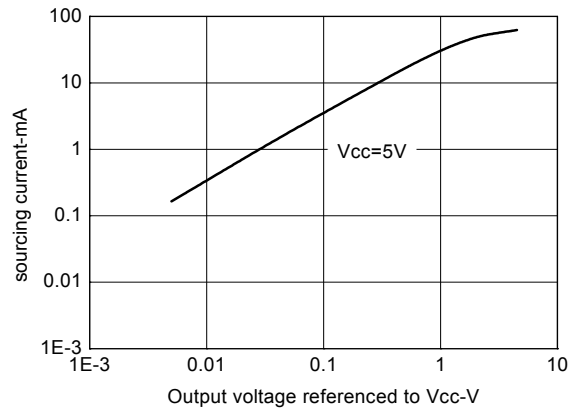


ΔV_{os} vs. CMR

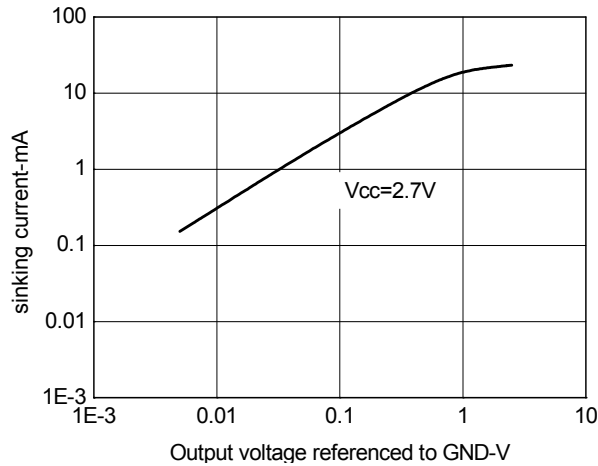
Typical Performance Characteristics (cont.)



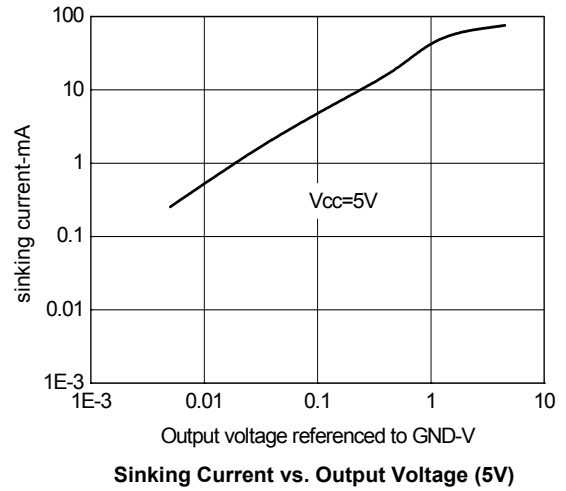
Sourcing Current vs. Output Voltage (2.7V)



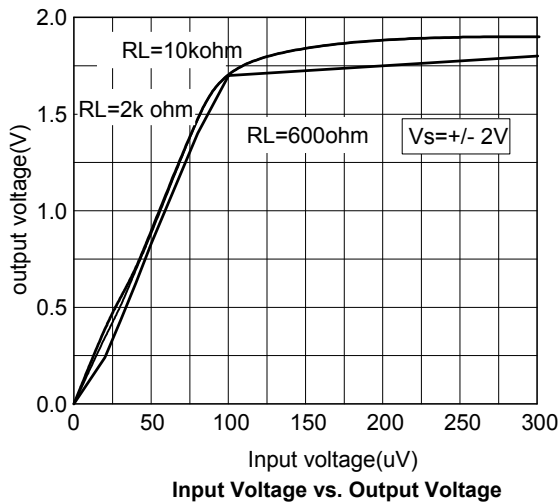
Sourcing Current vs. Output Voltage (5V)



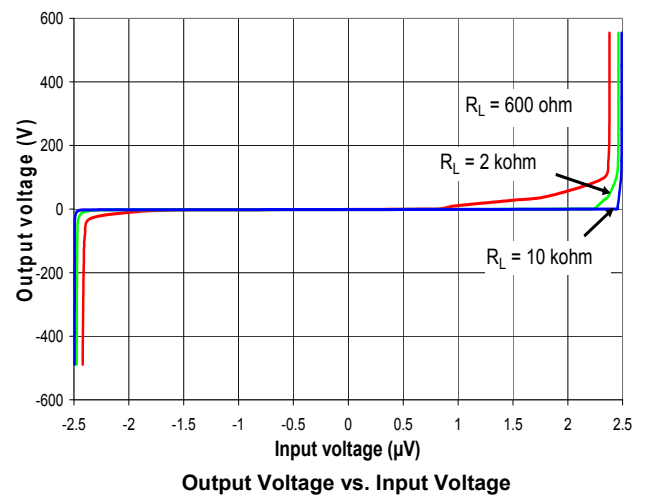
Sinking Current vs. Output Voltage (2.7V)



Sinking Current vs. Output Voltage (5V)

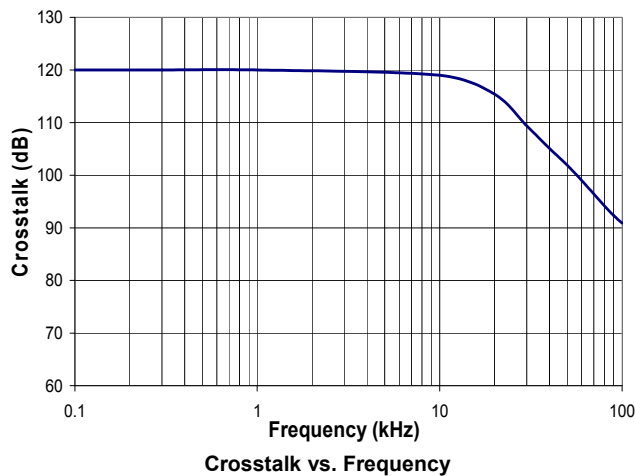
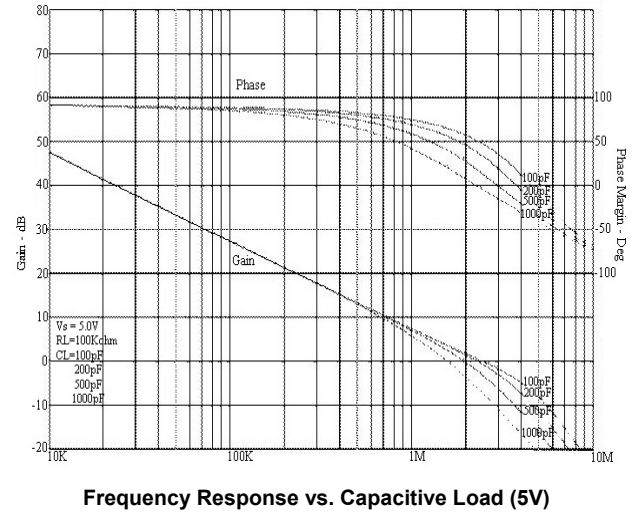
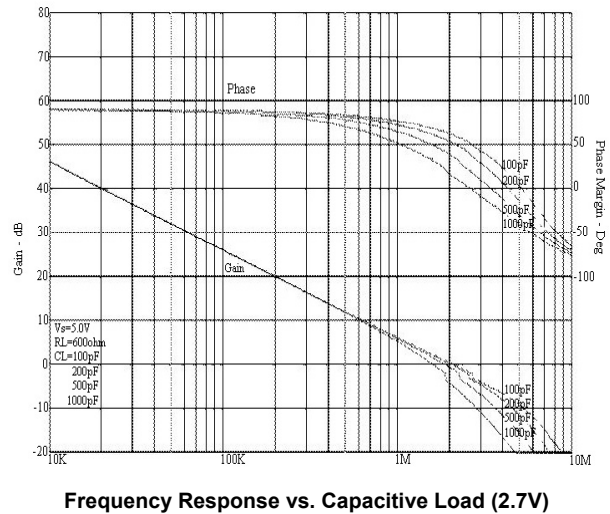
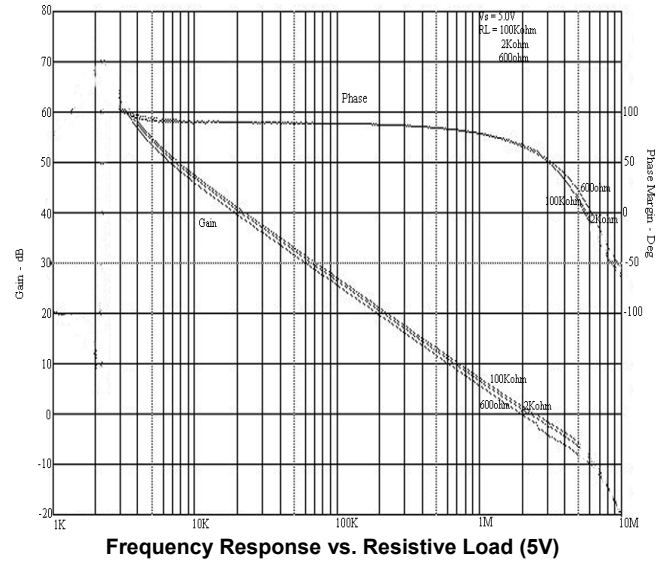
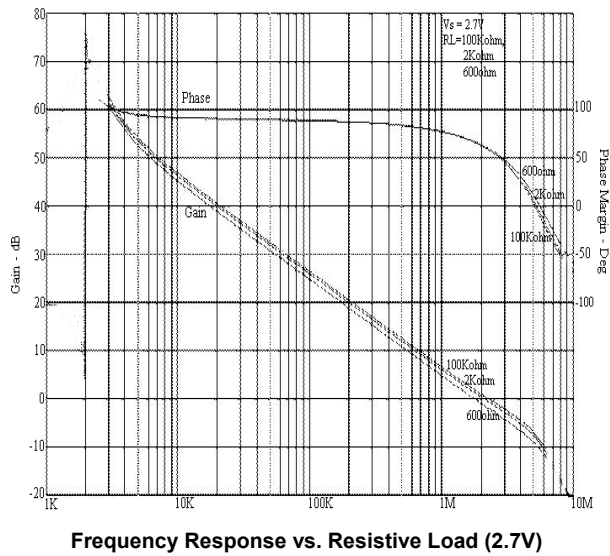


Input Voltage vs. Output Voltage

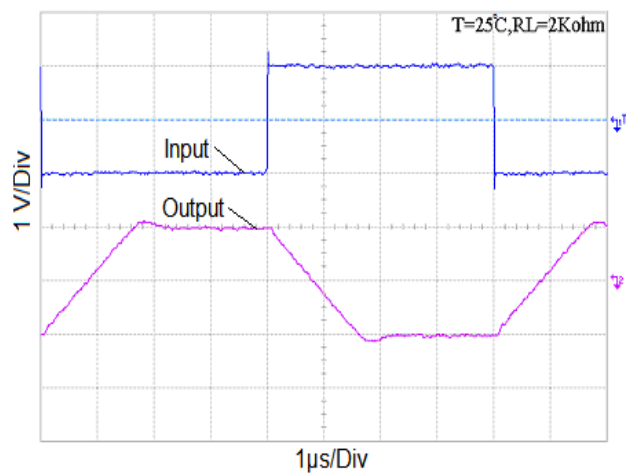


Output Voltage vs. Input Voltage

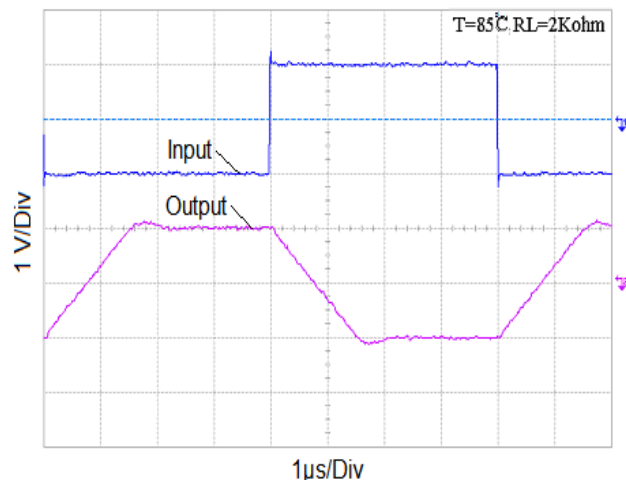
Typical Performance Characteristics (cont.)



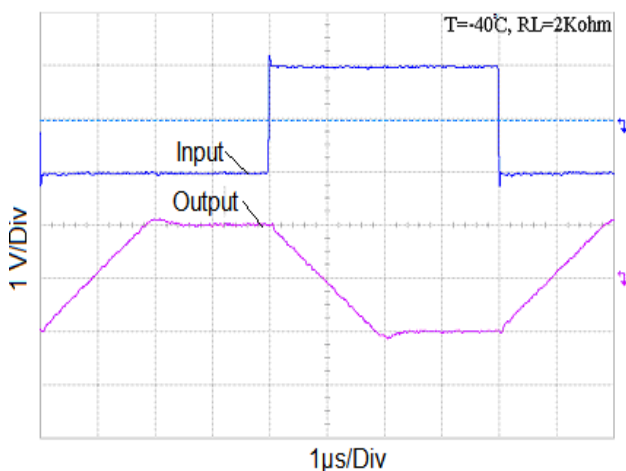
Typical Performance Characteristics (cont.)



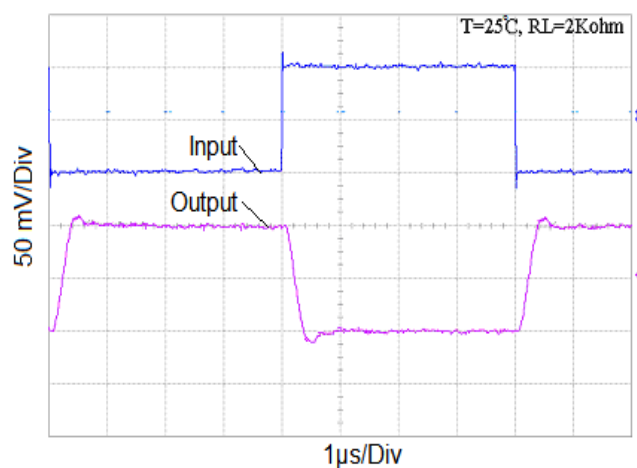
Inverting Large Signal Pulse Response



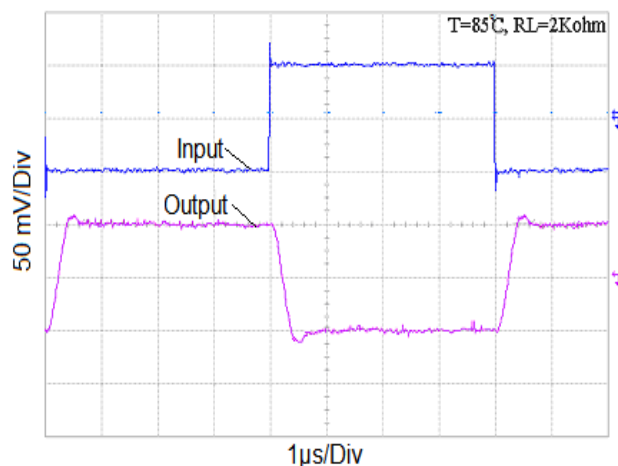
Inverting Large Signal Pulse Response



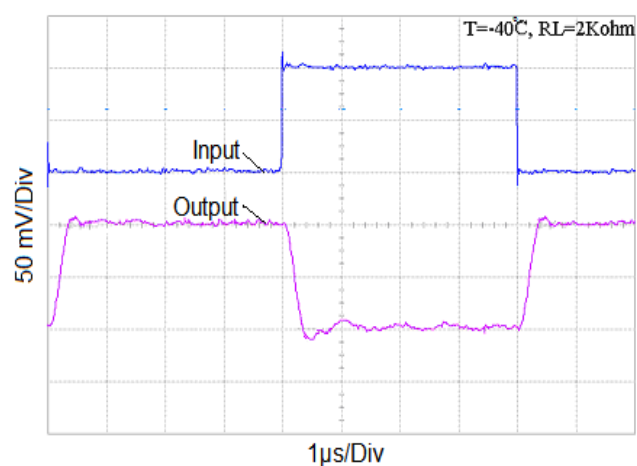
Inverting Large Signal Pulse Response



Inverting Small Signal Pulse Response

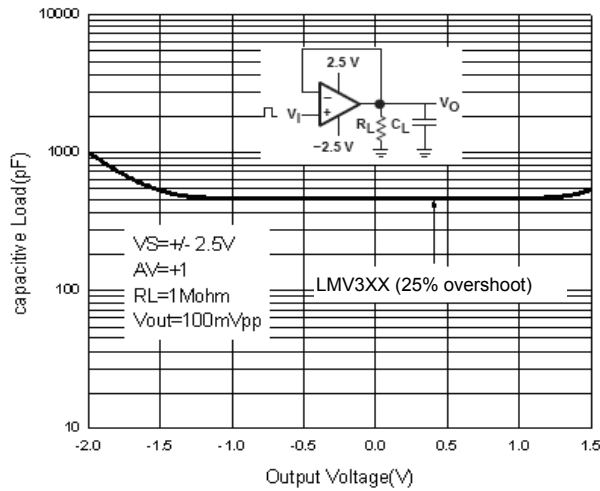


Inverting Small Signal Pulse Response

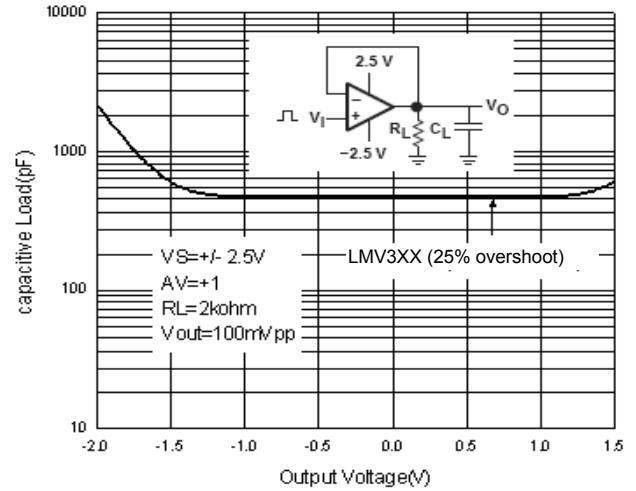


Inverting Small Signal Pulse Response

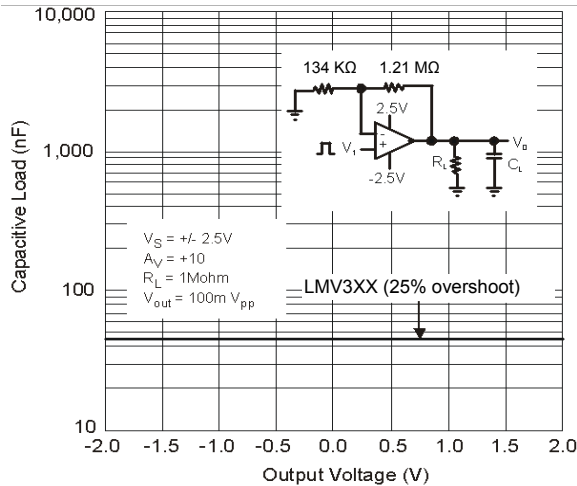
Typical Performance Characteristics (cont.)



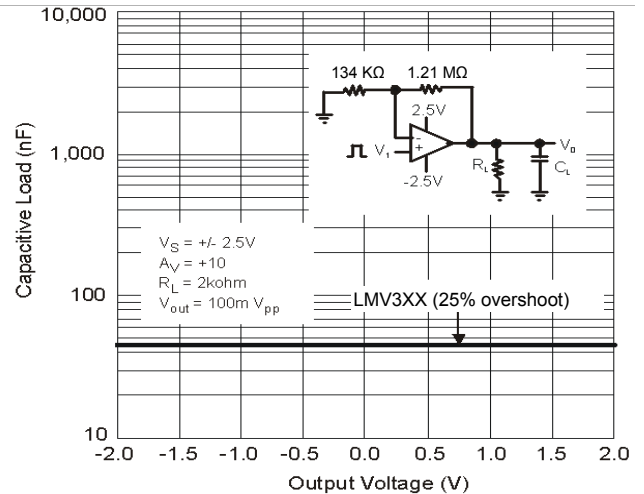
Stability vs. Capacitive Load



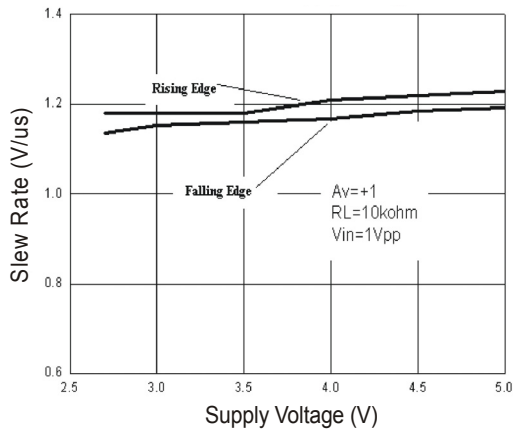
Stability vs. Capacitive Load



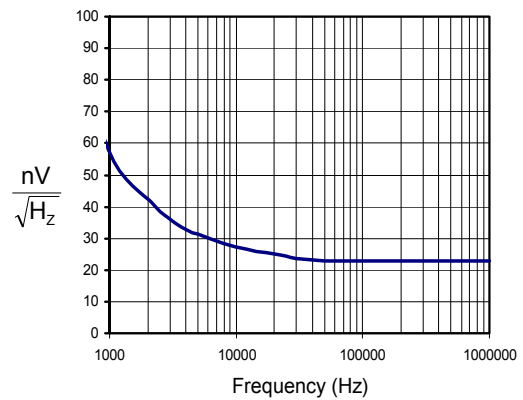
Stability vs. Capacitive Load



Stability vs. Capacitive Load

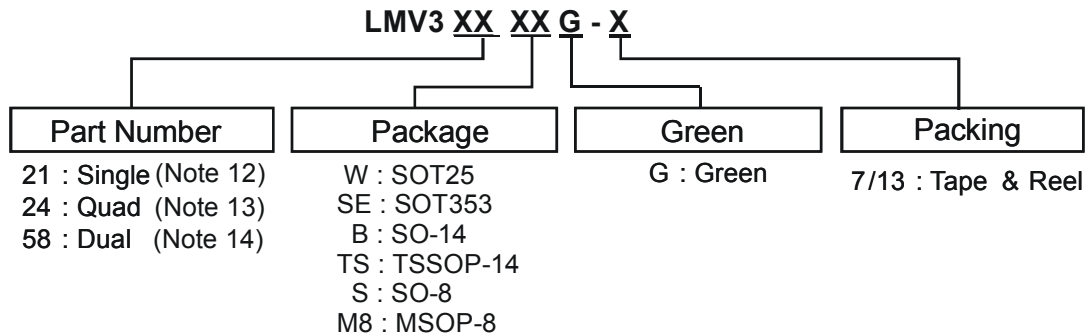


Slew Rate vs. Supply Voltage



Input Voltage Noise

Ordering Information



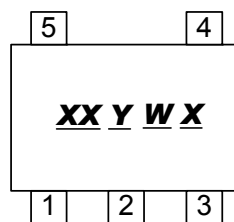
Part Number	Package Code	Packaging	7"/13" Tape and Reel	
			Quantity	Part Number Suffix
LMV321WG-7	W	SOT25	3000/Tape & Reel	-7
LMV321SEG-7	SE	SOT353	3000/Tape & Reel	-7
LMV324BG-13	B	SO-14	2500/Tape & Reel	-13
LMV324TSG-13	TS	TSSOP-14	2500/Tape & Reel	-13
LMV358SG-13	S	SO-8	2500/Tape & Reel	-13
LMV358M8G-13	M8	MSOP-8	2500/Tape & Reel	-13

Notes: 12. LMV321 is only available for SOT25 and SOT353.
13. LMV324 is only available for SO-14 and TSSOP-14.
14. LMV358 is only available for SO-8 and MSOP-8.

Marking Information

SOT25/SOT353

(Top View)

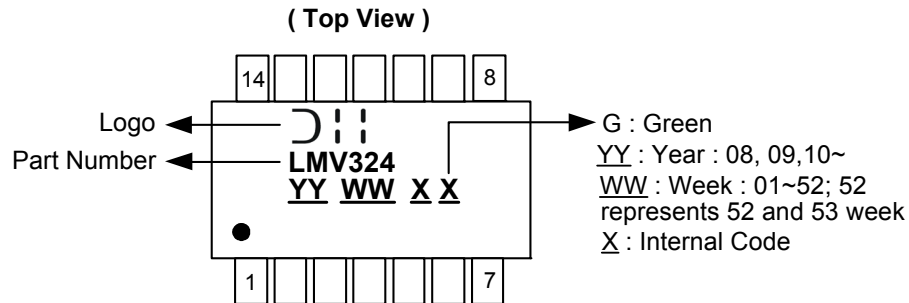


XX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week;
z represents 52 and 53 week
X : A~Z : Green

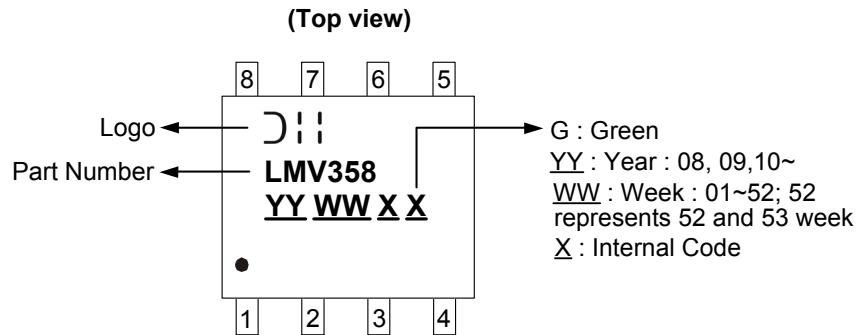
Device	Package type	Identification Code
LMV321W	SOT25	BX
LMV321SE	SOT353	BY

Marking Information (cont.)

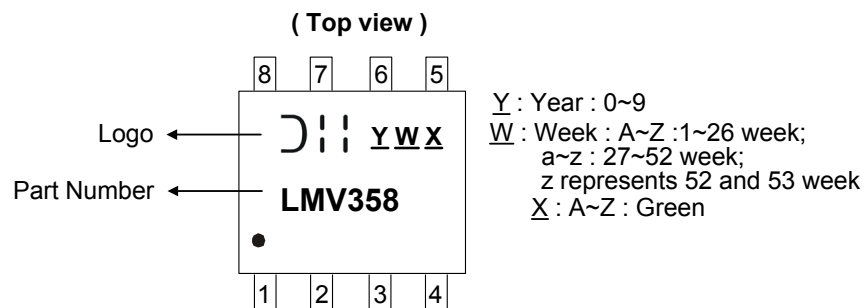
SO-14 / TSSOP-14



SO-8



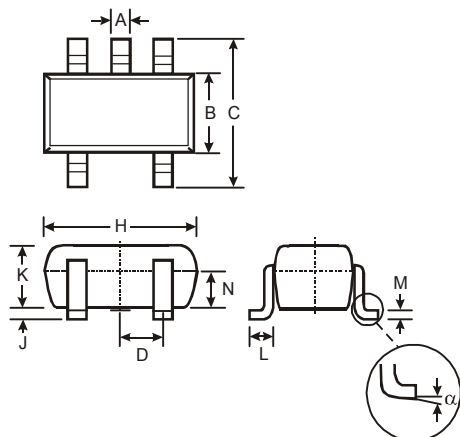
MSOP-8



Package Outline Dimensions (All dimensions in mm.)

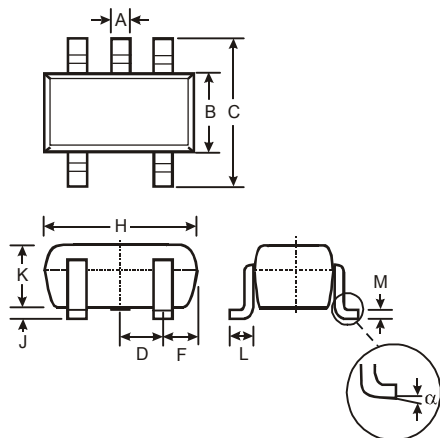
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.

SOT25



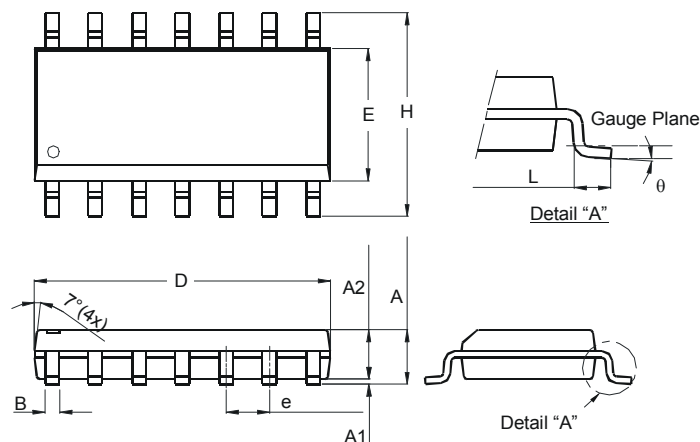
SOT25			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	—
All Dimensions in mm			

SOT353



SOT353			
Dim	Min	Max	Typ
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
α	0°	8°	-
All Dimensions in mm			

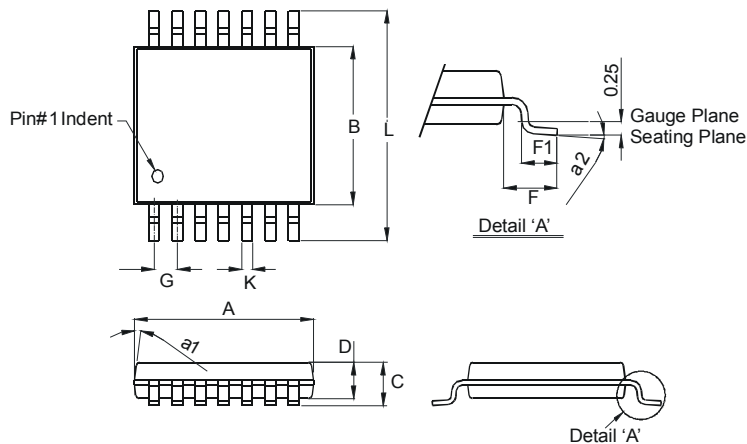
SO-14



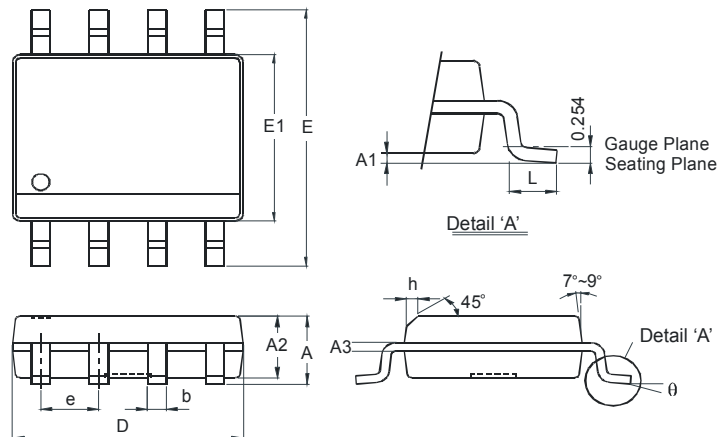
SO-14		
Dim	Min	Max
A	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
B	0.33	0.51
D	8.53	8.74
E	3.80	3.99
e	1.27 Typ	
H	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

Package Outline Dimensions (cont.) (All dimensions in mm.)

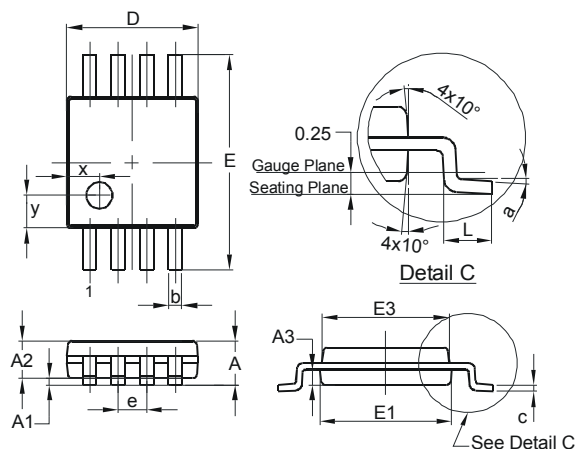
 Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.

TSSOP-14


TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
A	4.9	5.10
B	4.30	4.50
C	—	1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		

SO-8


SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

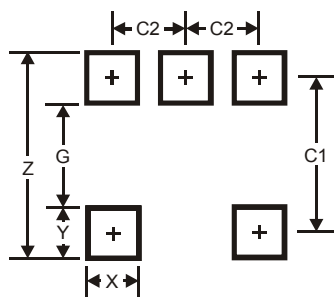
MSOP-8


MSOP-8			
Dim	Min	Max	Typ
A	-	1.10	-
A1	0.05	0.15	0.10
A2	0.75	0.95	0.86
A3	0.29	0.49	0.39
b	0.22	0.38	0.30
c	0.08	0.23	0.15
D	2.90	3.10	3.00
E	4.70	5.10	4.90
E1	2.90	3.10	3.00
E3	2.85	3.05	2.95
e	-	-	0.65
L	0.40	0.80	0.60
a	0°	8°	4°
x	-	-	0.750
y	-	-	0.750
All Dimensions in mm			

Suggested Pad Layout

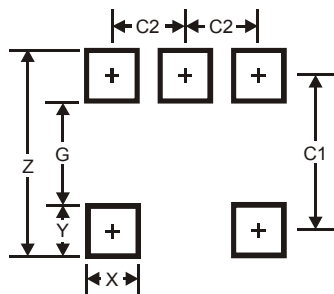
Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

SOT25



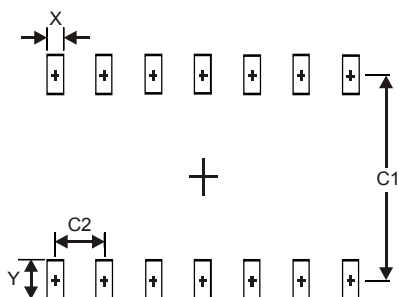
Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

SOT353



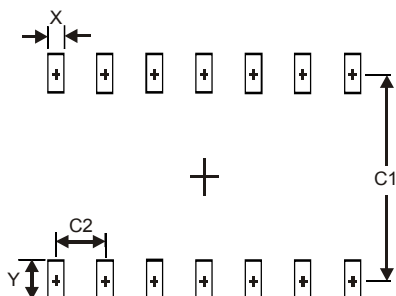
Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

SO-14



Dimensions	Value (in mm)
X	0.60
Y	1.50
C1	5.4
C2	1.27

TSSOP-14

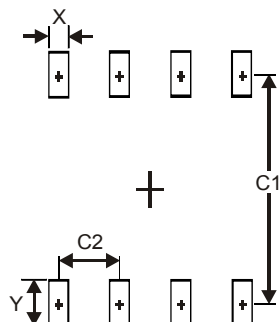


Dimensions	Value (in mm)
X	0.45
Y	1.45
C1	5.9
C2	0.65

Suggested Pad Layout (cont.)

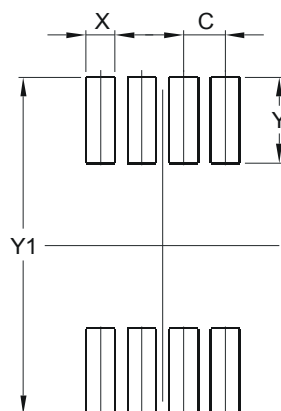
Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

SO-8



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

MSOP-8



Dimensions	Value (in mm)
C	0.650
X	0.450
Y	1.350
Y1	5.300

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