Characteristics ECMF02-2BF3

#### 1 Characteristics

Table 1. Absolute maximum ratings ( $T_{amb} = 25$  °C)

	- Carlo				
Symbol		Value	Unit		
V <sub>PP</sub>	Peak pulse voltage <sup>(1)</sup>	IEC 61000-4-2 contact discharge IEC 61000-4-2 air discharge	10 20	kV	
T <sub>j</sub>	Maximum junction temperature		125	°C	
T <sub>op</sub>	Operating temperature range		- 30 to + 85	°C	
T <sub>stg</sub>	Storage temperature range		- 55 to 150	°C	

<sup>1.</sup> Measurements done on IEC 61000-4-2 test bench. For further details see Application note AN3353.

Figure 3. Electrical characteristics (definitions)

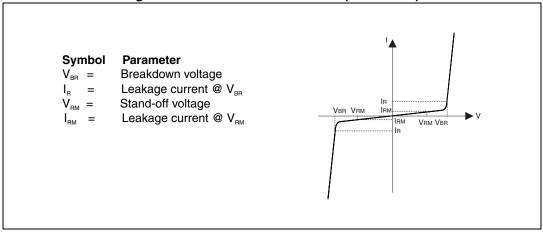


Table 2. Electrical characteristics (values,  $T_{amb} = 25$  °C)

Symbol	Test conditions	Min.	Тур.	Max.	Unit
$V_{BR}$	I <sub>R</sub> = 1 mA	6			V
$I_{RM}$	V <sub>RM</sub> = 3 V per line			100	nA
R <sub>DC</sub>	DC serial resistance		3	4	Ω



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# 2 Application schematics

Figure 4. USB2.0 application

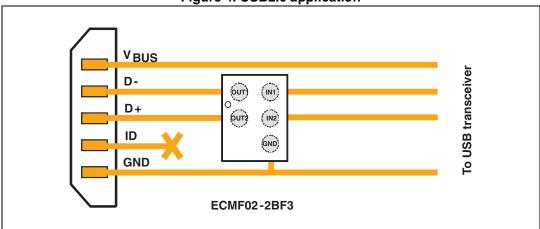
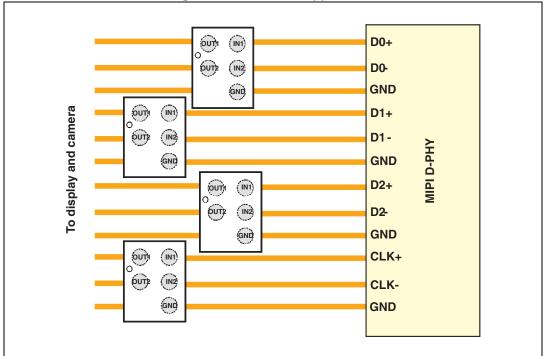


Figure 5. MIPI D-PHY application





Measurement curves ECMF02-2BF3

#### 3 Measurement curves

Figure 6. SDD21 differential attenuation measurement (Z  $_{0~diff}$  = 100  $\Omega)$ 

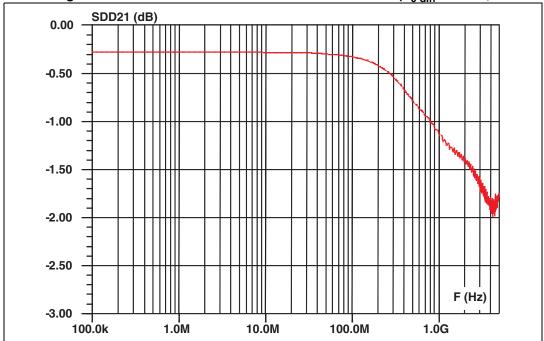
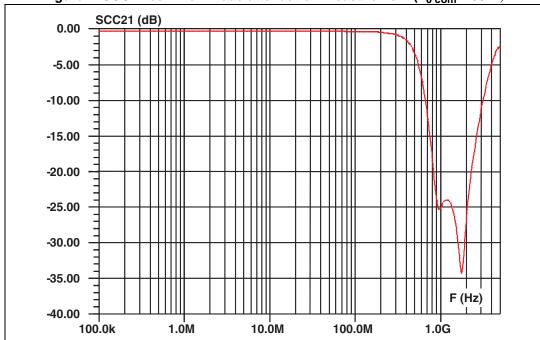


Figure 7. SCC21 common mode attenuation measurement (Z $_{0 \text{ com}}$  = 50  $\Omega$ )



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ECMF02-2BF3 Measurement curves

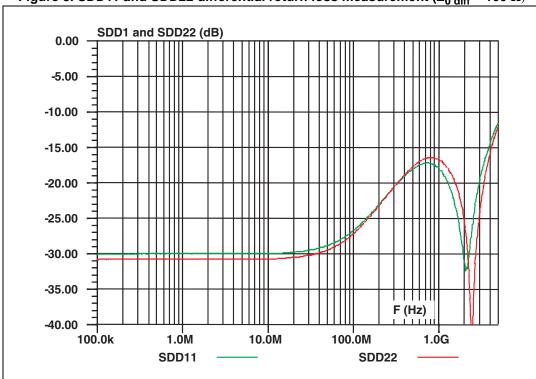


Figure 8. SDD11 and SDD22 differential return loss measurement (Z  $_{\rm 0~diff}$  = 100  $\Omega)$ 

Measurement curves ECMF02-2BF3

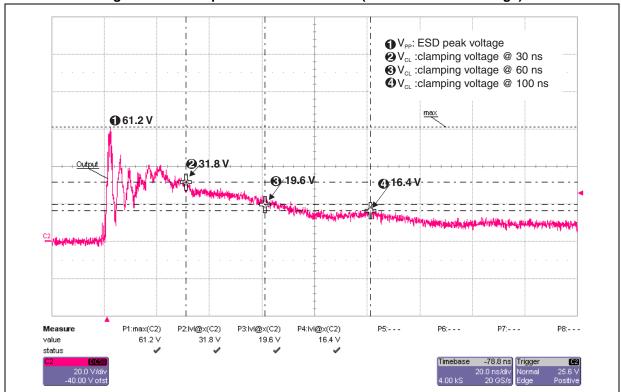
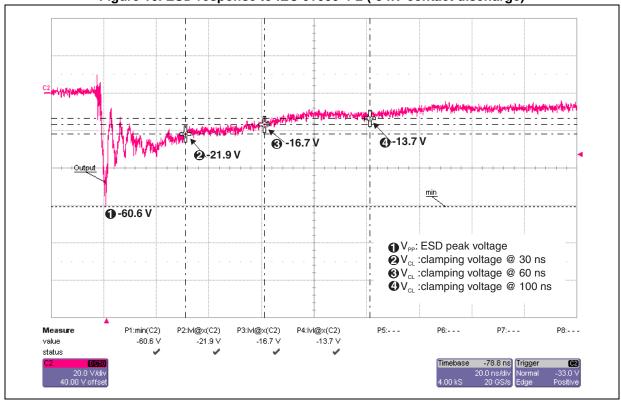


Figure 9. ESD response to IEC 61000-4-2 (+8 kV contact discharge)





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## 4 High speed differential standard compliance tests

#### 4.1 USB2.0 compliance tests



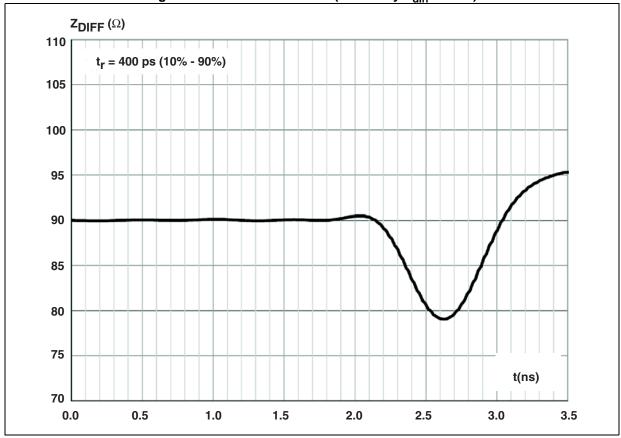
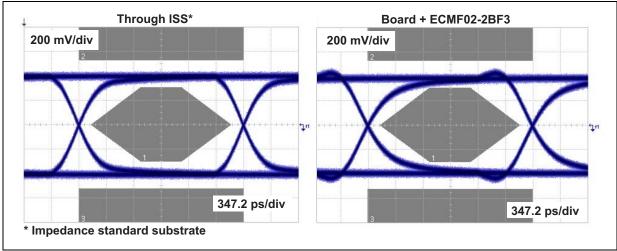


Figure 12. Eye diagram with USB2.0 template

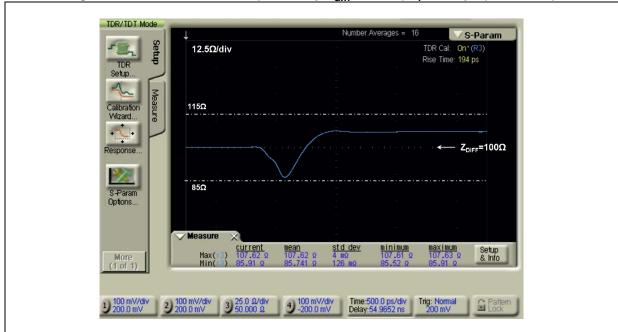




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### 4.2 HDMI1.4 compliance tests

Figure 13. TDR measurement (loaded by  $Z_{diff}$  = 100  $\Omega$ ),  $t_r$  = 194 ps (10% - 90%)





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## 5 PCB layout recommendations

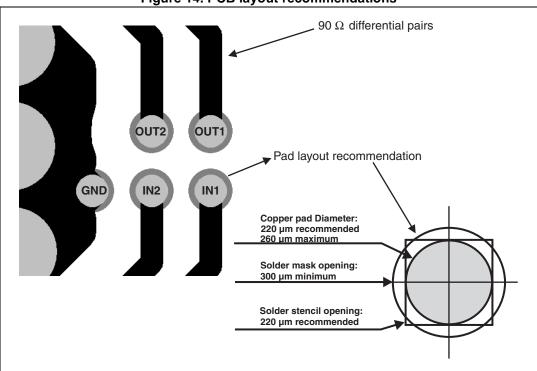


Figure 14. PCB layout recommendations



**Package information** ECMF02-2BF3

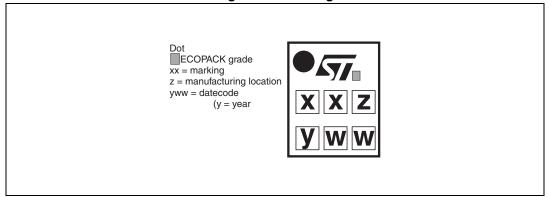
#### **Package information** 6

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

 $400 \ \mu m \pm 40$  $505 \mu m \pm 55$ 100 µm ± 40 255 µm ± 40 1230 µm ± 40 µm 215 µm 215 µm  $830 \mu m \pm 30 \mu m$ 

Figure 15. Package dimensions

Figure 16. Marking



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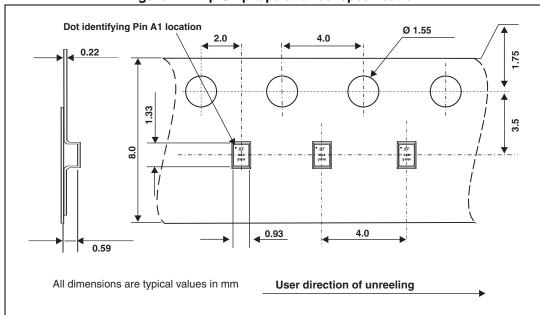


Figure 17. Flip Chip tape and reel specification

Note:

More information is available in the application notes:

AN2348, "IPAD™ 400 µm Flip Chip: package description and recommendations for use" AN1751, "EMI filters: recommendations and measurements"

### 7 Ordering information

Function
ESD common mode filter

Number of lines
02 = 2 lines
Number of ESD protected lines
2 = 2 ESD protected lines
Version

Package
F3 = WLCSP 0.4 mm pirch

Figure 18. Ordering information scheme

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
ECMF02-2BF3	KE	Flip Chip	1.15 mg	5000	Tape and reel 7"



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Revision history ECMF02-2BF3

# 8 Revision history

**Table 4. Document revision history** 

Date	Revision	Changes
09-Feb-2012	1	Initial release.
07-Mar-2014	2	Updated Figure 13.

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