

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	40	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current Vos = 10V (Note 5)		T _A = +25°C T _A = +100°C	lo	15 11	А
Continuous Drain Current, VGS = 10V (Note 6)	Steady State	T _C = +25°C T _C = +100°C	lD	85 60	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	85	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	340	Α
Avalanche Current, L = 0.1mH			I _{AS}	20	Α
Avalanche Energy, L = 0.1mH			Eas	20	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	55	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	83.3	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	1.8	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

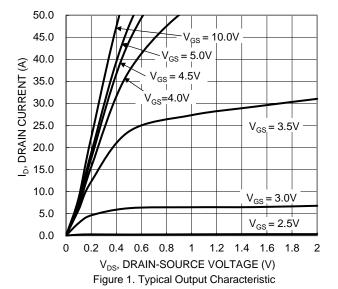
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 32V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	Vgs = ±20V, Vps = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	5.4	6.5	mΩ	Vgs = 10V, ID = 20A	
Static Drain-Source On-Resistance	RDS(ON)	_	8.4	9.8		V _{GS} = 4.5V, I _D = 20A	
Diode Forward Voltage	VsD	_	_	1.2	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	1,895	_		$V_{DS} = 30V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss	_	485	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	20.9				
Gate Resistance	Rg	_	0.62	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	12.4	_		V _{DS} = 30V, I _D = 20A	
Total Gate Charge (Vgs = 10V)	Qg	_	29.1	_	~C		
Gate-Source Charge	Qgs	_	5.9	_	nC		
Gate-Drain Charge	Qgd		3.5	_			
Turn-On Delay Time	t _D (ON)		5.4	_		$V_{DD}=30V,V_{GS}=10V,$ $I_{D}=20A,R_{G}=3\Omega$	
Turn-On Rise Time	t _R		4.5	_			
Turn-Off Delay Time	tD(OFF)	_	16.2	_	ns		
Turn-Off Fall Time	tr		3.5	_			
Body Diode Reverse Recovery Time	t _{RR}	_	30.6	_	ns		
Body Diode Reverse Recovery Charge	Qrr	_	28.1	_	nC	I _F = 20A, di/dt = 100A/μs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect. Notes:

8. Guaranteed by design. Not subject to product testing.





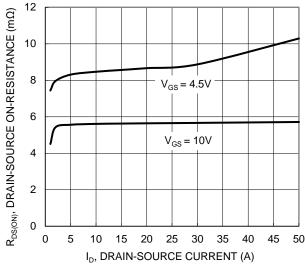


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

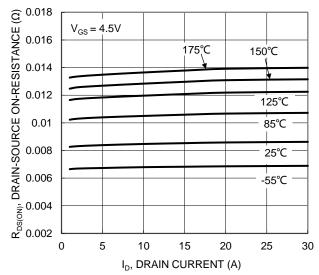


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

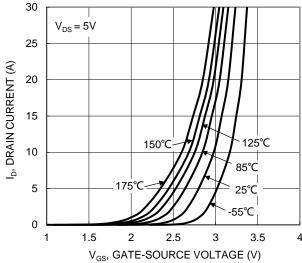


Figure 2. Typical Transfer Characteristic

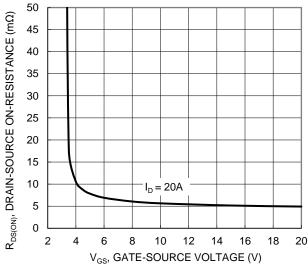


Figure 4. Typical Transfer Characteristic

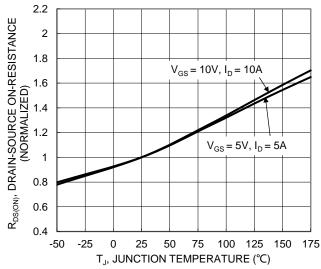


Figure 6. On-Resistance Variation with Temperature



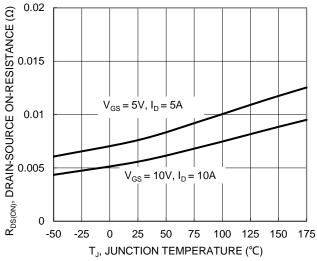
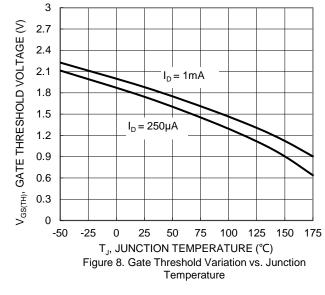


Figure 7. On-Resistance Variation with Temperature



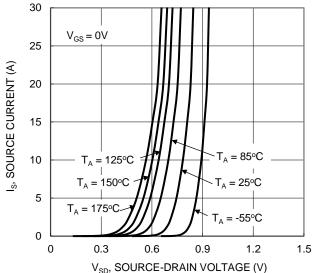
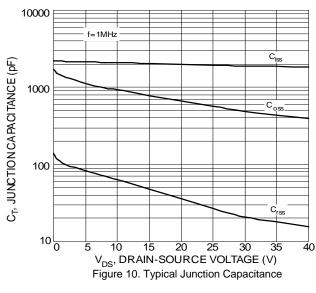
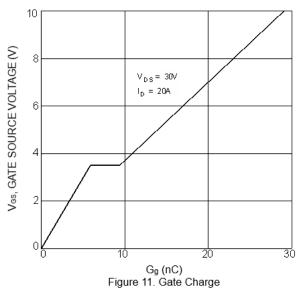


Figure 9. Diode Forward Voltage vs. Current





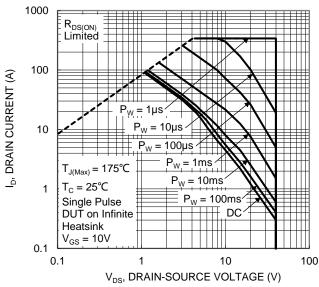


Figure 12. SOA, Safe Operation Area



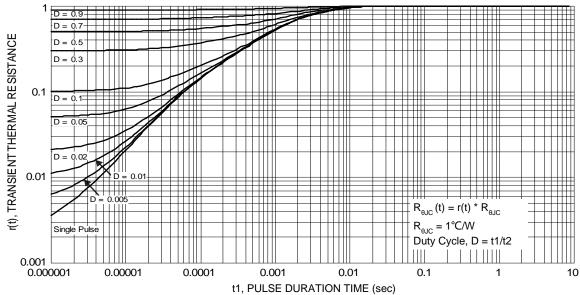


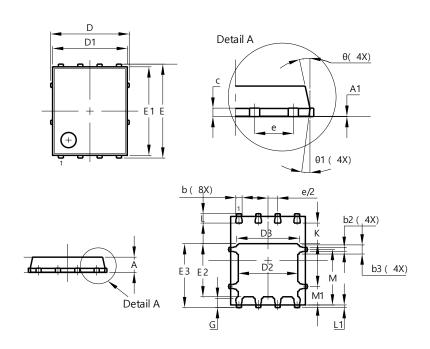
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

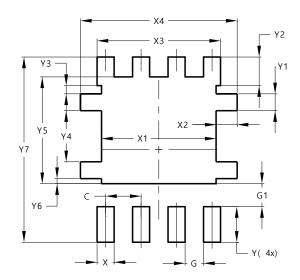


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	1		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	5.15 BSC				
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е		6.15 BSC			
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			

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