

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage		V _{GSS}	±20	V	
Drain Current (Note 6) V _{GS} = -10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-3.8 -2.9	А
Pulsed Drain Current (Note 7)			I _{DM}	-11	A
Avalanche Current, L = 0.1mH			I _{AS}	-14.3	A
Avalanche Energy, L = 0.1mH			E _{AS}	10.2	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P_{D}	1.08	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	$R_{ heta JA}$	115	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-800	nA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	_	-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D		_	65 99	mΩ	$V_{GS} = -10V, I_D = -3.8A$
Static Drain-Source Off-Resistance	R _{DS(ON)}	_				$V_{GS} = -4.5V$, $I_D = -3.0A$
Forward Transfer Admittance	Y _{fs}	-	3.6	_	S	$V_{DS} = -5V, I_{D} = -2.7A$
Diode Forward Voltage	V _{SD}	_	_	-1.26	V	$V_{GS} = 0V, I_{S} = -2.7A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	563	_	pF	.,
Output Capacitance	Coss	_	48	_	pF	$V_{DS} = -25V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	41	_	pF	-1 = 1.0WH2
Gate Resistance	R _G	_	10.3	_	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$
SWITCHING CHARACTERISTICS (Note 9)						
Total Gate Charge	Q_g	_	5.2	_		$V_{DS} = -15V$, $V_{GS} = -4.5V$, $I_{D} = -3.8A$
		_	11	_	nC	V _{DS} = -15V, V _{GS} = -10V, I _D = -3.8A
Gate-Source Charge	Q_{gs}	_	1.7	_		
Gate-Drain Charge	Q_{gd}	_	1.9	_		
Turn-On Delay Time	t _{D(ON)}	_	4.8	_	ns	$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -1A$, $R_{G} = 6.0\Omega$
Rise Time	t _R	_	5.0	_		
Turn-Off Delay Time	t _{D(OFF)}	_	31			
Fall Time	t _F	_	15	_		

Notes:

- 6. Device mounted on FR-4 PCB on 2 oz., 0.5 inch² copper pads and $t \le 5$ sec.
- 7. Pulse width ≤10µs, Duty Cycle ≤1%.
- 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to production testing.



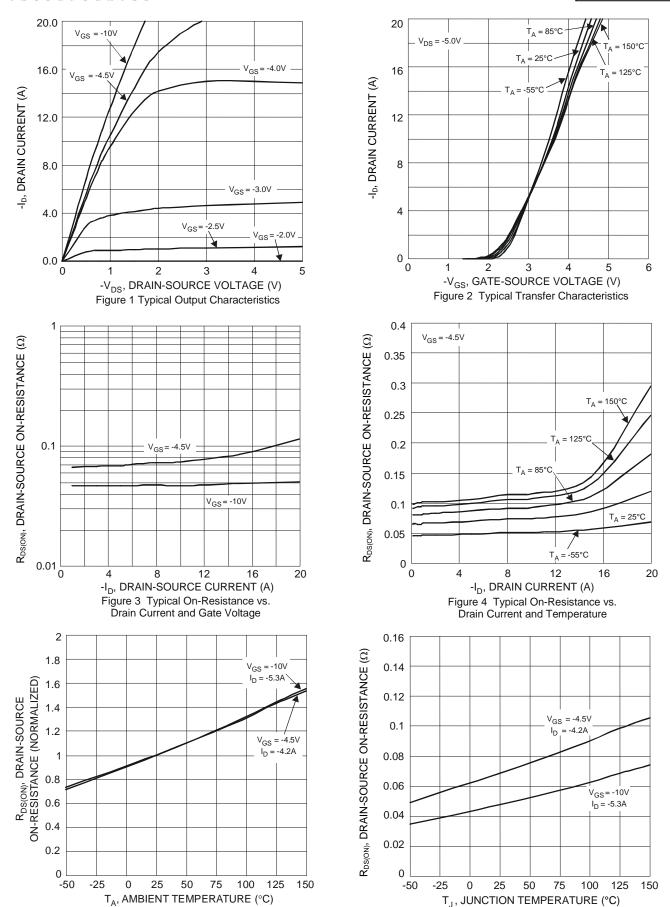


Figure 5 On-Resistance Variation with Temperature

Figure 6 On-Resistance Variation with Temperature



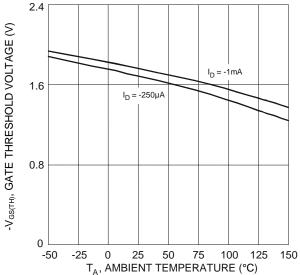
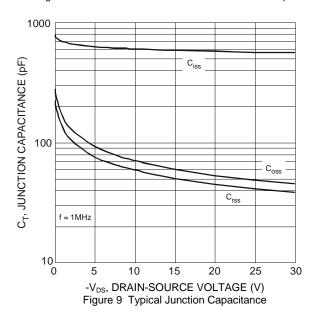


Figure 7 Gate Threshold Variation vs. Ambient Temperature



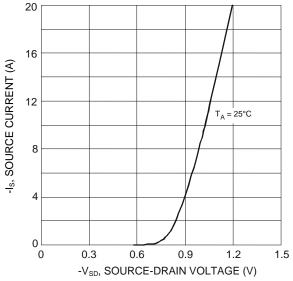
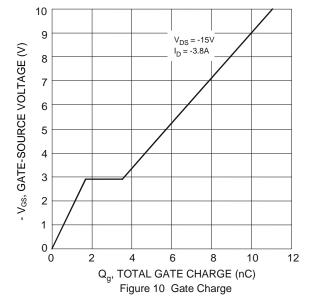


Figure 8 Diode Forward Voltage vs. Current

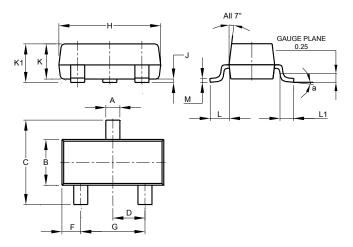




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

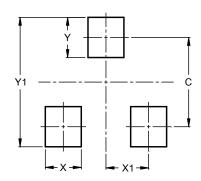


SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
M	0.085	0.150	0.110		
а	0°	8°			
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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