

DMP3017SFG

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Dunin Comment (Alata C) V 40V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-11.5 -9.4	А
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-15.2 -12.1	А
Maximum Continuous Body Diode Forward Current	Is	-3.0	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-80	Α
Avalanche Current (Note 7) L = 1mH			I _{AR}	-14	Α
Repetitive Avalanche Energy (Note 7) L = 1mH			E _{AR}	104	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	PD	0.94	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Pau	137	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	R ₀ JA	82	°C/W	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	5	2.2	W	
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	PD	1.3	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D.	60	°C/W	
Thermal Resistance, Junction to Ambient (Note o)	t < 10s	R _θ JA	36	°C/W	
Thermal Resistance, Junction to Case (Note 6)		Rejc	3.0	°C/W	
Operating and Storage Temperature Range		TJ, T _{STG}	-55 to +150	°C	

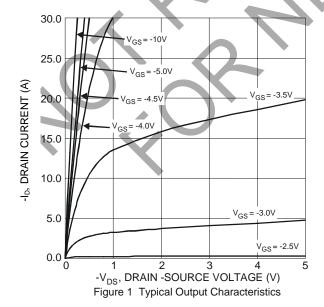


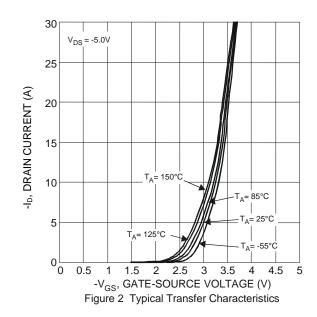
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-1.0	_	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	,	_	8.5	10	mΩ	$V_{GS} = -10V, I_D = -11.5A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	15	18		$V_{GS} = -4.5V, I_D = -8.5A$	
Forward Transfer Admittance	Y _{fs}	_	24	_	S	$V_{DS} = -5V, I_{D} = -11.5A$	
DYNAMIC CHARACTERISTICS (Note 9)				•			
Input Capacitance	C _{iss}	_	2246	_	pF		
Output Capacitance	Coss	_	352		pF	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	294	1-1	pF	1 = 1.0IVIDZ	
Gate Resistance	Rg	_	5.1	12	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = -5V)	Qg	_	20.5	-	nC		
Total Gate Charge (V _{GS} = -10V)	Qg		41		nC		
Gate-Source Charge	Q _{gs}	-	7.6	<u> </u>	nC	$V_{DS} = -15V$, $I_D = -11.5A$	
Gate-Drain Charge	Q _{gd}	-//	8.0		nC		
Turn-On Delay Time	t _{D(ON)}	6	7.5	-(ns		
Turn-On Rise Time	t _R		15.4	/_	ns	$V_{DD} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	45.6	→ '	ns	$R_G = 6\Omega$, $I_D = -11.5A$	
Turn-Off Fall Time	tF	7 -	36.8	V- /	ns		
BODY DIODE CHARACTERISTICS							
Diode Forward Voltage	V _{SD}	_	-0.7) –	V	V _{GS} = 0V, I _S = -1A	
Reverse Recovery Time (Note 9)	t _{RR}	1	20	_	ns	44.54	
Reverse Recovery Charge (Note 9)	Q _{RR}	1-0	9.5	_	nC	I _S = -11.5A, dI/dt = 100A/μs	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- Severe involved united stricts and Telephone and duty cycles to keep T₁ = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

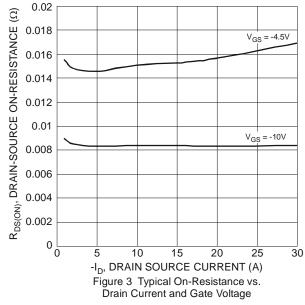


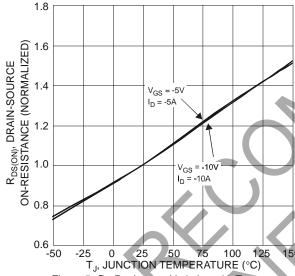


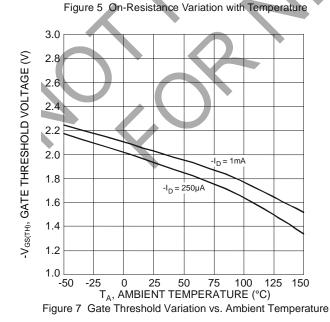


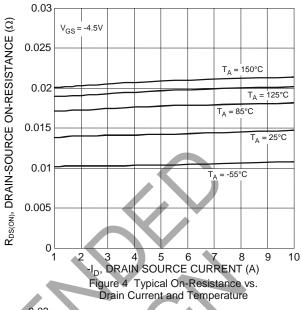
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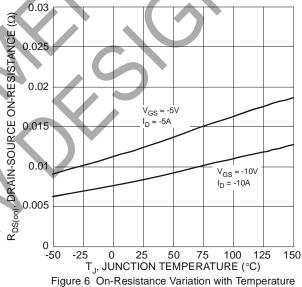
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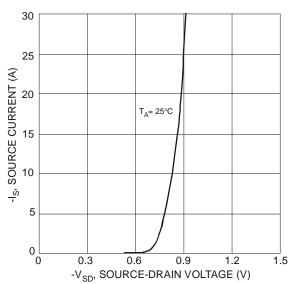








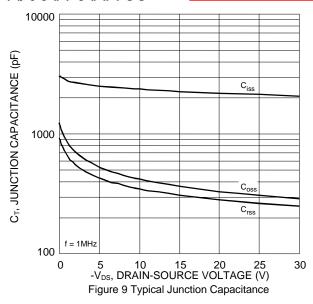


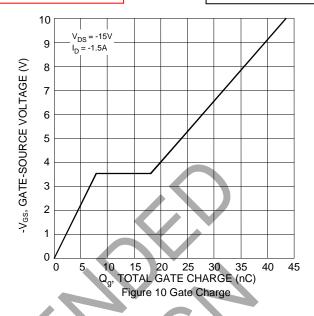


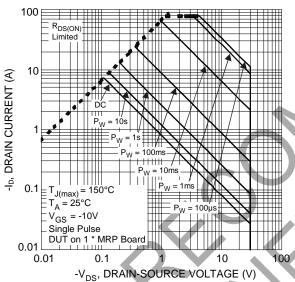


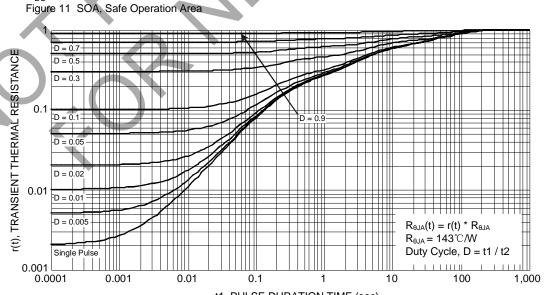
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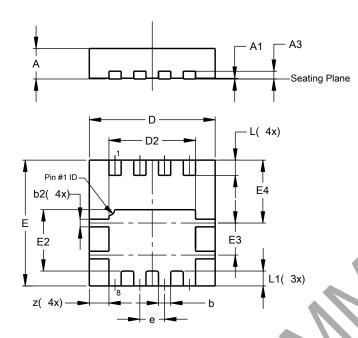




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

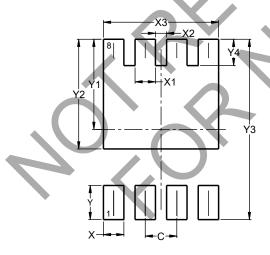


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	_	_	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
ם	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
ш	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
е	_	_	0.65		
L	0.35	0.45	0.40		
L1	_	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
Χ	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP3013SFV</u>

DMP3017SFG

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