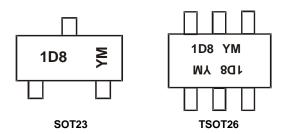


Marking Information



1D8 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F= 2018) M = Month (ex: 9 = September)

Date Code Key

	,												
Year	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Code	В	С	D	Е	F	G	Н		J	K	L	М	N
Mon	th	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Cod	le	1	2	3	4	5	6	7	8	9	0	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) SOT23	sous Drain Current (Note 6) SOT23 $\begin{array}{c} Steady \\ State \end{array}$ $\begin{array}{c} T_A = +25^{\circ}C \\ T_A = +70^{\circ}C \end{array}$		I _D	470 370	mA
Continuous Drain Current (Note 6) TSOT26	Steady State	T _A = +25°C T _A = +70°C	l _D	630 500	mA
Maximum Continuous Body Diode Forward Current	(Note 6)		I _S	0.5	А
Single Pulse Drain-to-Source Avalanche Energy (for relay coils/inductive loads of 80Ω or higher) (T _J initial = +85°C)			Ez	200	mJ
Peak Power Dissipation, Drain-to-Source (non-repetitive current square pulse 1.0ms duration) (T _J initial = +85°C)			P _{PK}	20	W
Load Dump Pulse, Drain-to-Source, $R_{SOURCE} = 0.5\Omega$, $t = 300ms$) (for relay coils/inductive loads of 80Ω or higher) (T _J Initial = +85°C)			E _{LD1}	60	V
Inductive Switching Transient 1, Drain-to-Source (Waveform: $R_{SOURCE} = 10\Omega$, $t = 2.0ms$) (for relay coils/inductive loads of 80Ω or higher) (T _J Initial = +85°C)			E _{LD2}	100	V
Inductive Switching Transient 2, Drain-to-Source (Waveform: $R_{SOURCE} = 4.0\Omega$, $t = 50\mu s$) (for relay coils/inductive loads of 80Ω or higher) (T _J Initial = +85°C)			E _{LD3}	300	V
Reverse Battery, 10 Minutes (Drain-to-Source) (for relay coils/inductive loads of 80Ω or higher)			Rev-Bat	-14	V
Dual Voltage Jump Start, 10 Minutes (Drain-to-Source)			Dual-Volt	28	V
ESD Human Body Model (HBM)			ESD	4,000	V



Thermal Characteristics (SOT23) ($@T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	390	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	321	°C/W
Total Power Dissipation (Note 6)		P _D	610	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	208	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Thermal Characteristics (TSOT26) ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	820	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	154	°C/W
Total Power Dissipation (Note 6)		P_D	1090	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	116	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

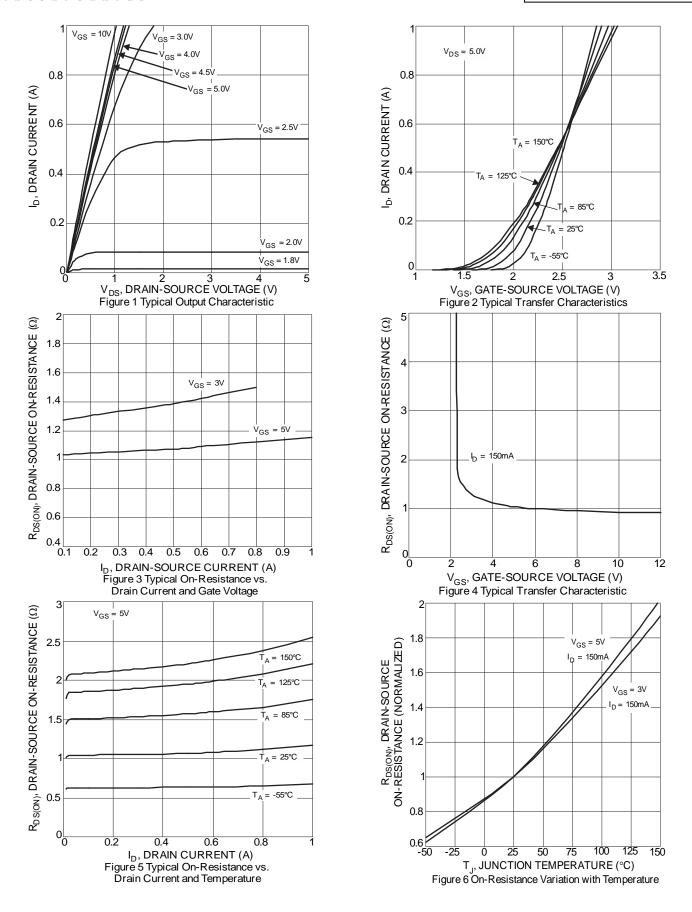
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage		60			٧	$V_{GS} = 0V$, $I_D = 10mA$	
Zero Gate Voltage Drain Current	I _{DSS}			50 0.5	μΑ	$V_{DS} = 60V, V_{GS} = 0V$ $V_{DS} = 12V, V_{GS} = 0V$	
Gate-Source Leakage		_		±90 ±60	μΑ	$V_{GS} = \pm 5V, V_{DS} = 0V$ $V_{GS} = \pm 3V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1.3		2.0	V	$V_{DS} = V_{GS}$, $I_D = 1mA$	
Static Drain-Source On-Resistance	,		1.1 1.4	1.8	Ω	$V_{GS} = 5V, I_D = 0.15A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_		2.4		V _{GS} = 3V, I _D = 0.15A	
Forward Transfer Admittance	Y _{fs}	80	_	_	ms	V _{DS} =12V, I _D = 0.15A	
Diode Forward Voltage	V _{SD}		_	1.2	V	V _{GS} = 0V, I _S = 0.15A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		12.9	_	pF		
Output Capacitance	Coss		17		pF	V _{DS} = 12V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		0.84		pF	1 - 1.00112	
Total Gate Charge	Qg		0.74		nC	V 5V V 40V	
Gate-Source Charge	Q _{gs}		0.19	_	nC	$V_{GS} = 5V, V_{DS} = 12V,$ $I_{D} = 150 \text{mA}$	
Gate-Drain Charge	Q _{gd}		0.16	_	nC	1D = 130111A	
Turn-On Delay Time	t _{D(ON)}	_	131	_	ns		
Turn-On Rise Time	t _R		301		ns	Vpp = 12V, Vgs = 5V	
Turn-Off Delay Time	t _{D(OFF)}		582	_	ns	ν _{OD} = 12V, VGS = 5V	
Turn-Off Fall Time	t _F	_	440	_	ns		

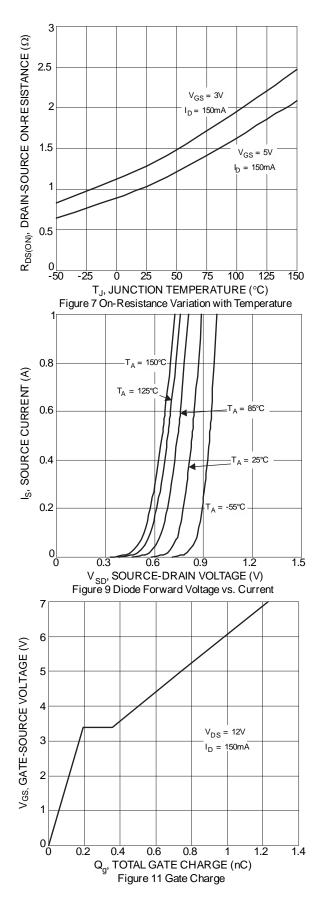
Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.









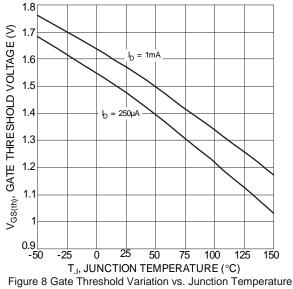


Figure 8 Gate Threshold Variation vs. Junction Temperature

100

Ciss

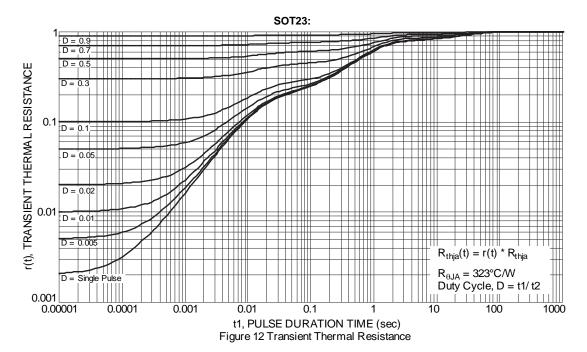
Ciss

Coss

O

The state of the state





TSOT26: r(t), TRANSIENT THERMAL RESISTANCE D = 0.3 0.1 D = 0.1 D = 0.05 0.01 $R_{thja}(t) = r(t) * R_{thja}$ $R_{\theta JA} = 154$ °C/W Duty Cycle, D = t1/t2 D = Single Pulse 0.001 0.00001 0.0001 0.001 10 1000 0.01 0.1 100 t1, PULSE DURATION TIME (sec)

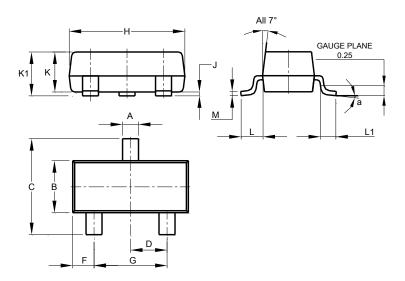
Figure 13 Transient Thermal Resistance



Package Outline Dimensions

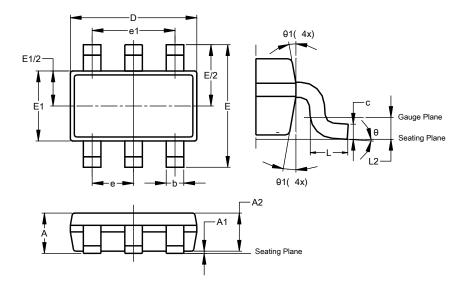
Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	All Dimensions in mm					

TSOT26



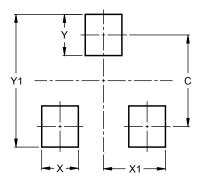
TSOT26							
Dim	Min	Max	Тур				
Α	-	1.00	_				
A1	0.010	0.100	_				
A2	0.840	0.900	_				
D	2.800	3.000	2.900				
Е	2	2.800 BSC					
E1	1.500	1.700	1.600				
q	0.300	0.450	_				
С	0.120	0.200	_				
е	0.950 BSC						
e1	1.900 BSC						
L	0.30	0.50	-				
L2	0.250 BSC						
θ	0°	8°	4°				
θ1	4°	12°	_				
All Dimensions in mm							



Suggested Pad Layout

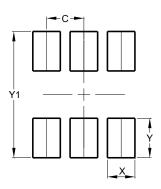
Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com