

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V_{GS} = -10V	Steady State	T _C = +25°C T _C = +100°C	ID	20 13	A
Maximum Body Diode Forward Current (Note 5)			ls	4	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	30	A
Avalanche Current (Note 6)			I _{AR}	14.2	A
Avalanche Energy (Note 6)			E _{AR}	10	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	T _C = +25°C	D	42	W	
Total Power Dissipation (Note 5)	T _C = +100°C	P _D	17		
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	44	°C (M)		
Thermal Resistance, Junction to Case (Note 5)		R _{θJC}	3	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

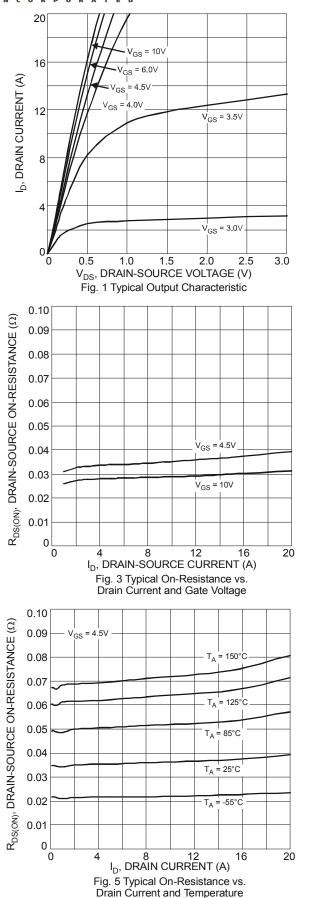
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

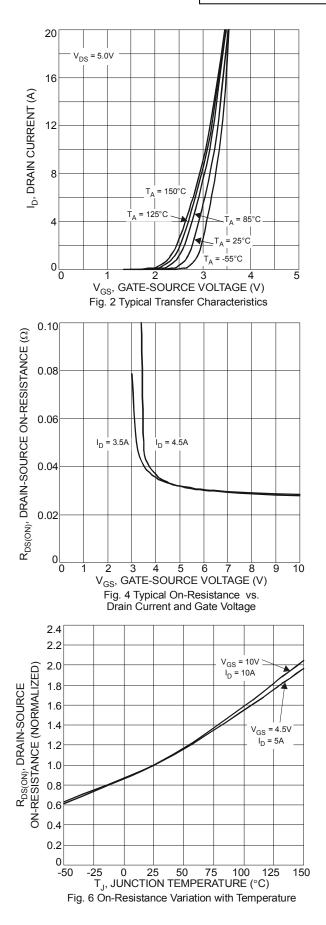
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	_		1	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_		±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	_		30	40	mΩ	V _{GS} = 10V, I _D = 20A
	R _{DS(ON)}	_	35	50	1115.2	V _{GS} = 4.5V, I _D = 12A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	1287	_	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	Coss	_	57	_		
Reverse Transfer Capacitance	C _{rss}		44	—		
Gate Resistance	R _G	_	1.2	_	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 10V)	Qg		22.4	—		V _{DS} = 30V, I _D = 4.3A
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	10.4	_	nC	
Gate-Source Charge	Qgs	_	4.9	_	nc	
Gate-Drain Charge	Q _{gd}	_	3.0	_		
Turn-On Delay Time	t _{D(on)}		6.6	_		V_{GS} = 10V, V_{DD} = 30V, R_G = 6 Ω , I _D = 4.3A
Turn-On Rise Time	tr		8.1	_	nS	
Turn-Off Delay Time	t _{D(off)}		20.1		ns	
Turn-Off Fall Time	t _f	_	4.0	_]	
Body Diode Reverse Recovery Time	t _{rr}	_	18		nS	I _S = 4.3A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{rr}		11.9	_	nC	I _S = 4.3A, dl/dt = 100A/µs

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
6. UIS in production with L = 0.1mH, T_J = +25°C.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing. Notes:

DMN6040SK3

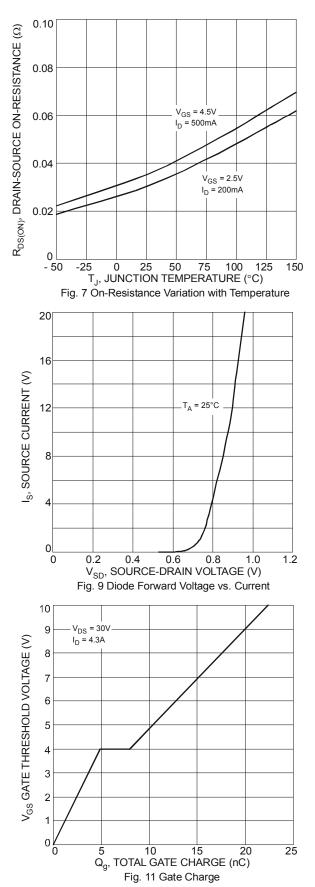


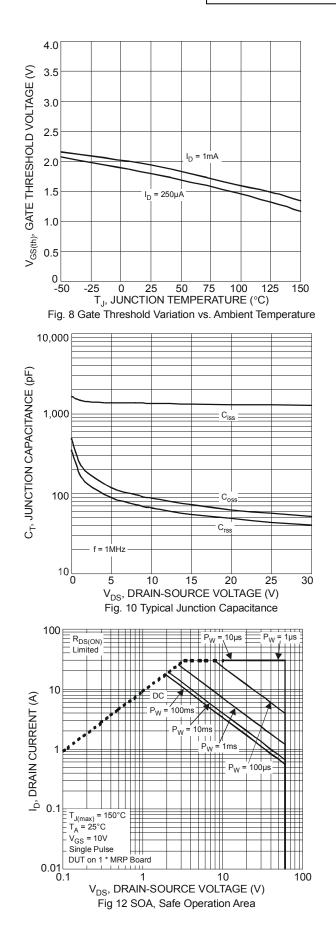




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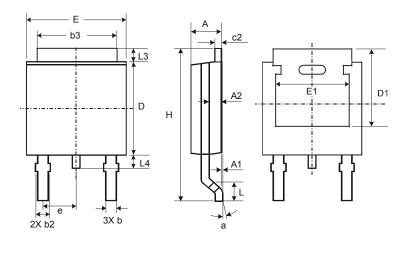


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Package Outline Dimensions

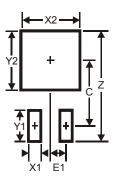
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
C	6.9
E1	2.3



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