

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	50	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 7)	ID	500	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	370	mW
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	344	°C/W
Total Power Dissipation (Note 7)	PD	540	mW
Thermal Resistance, Junction to Ambient (Note 7)	R _{0JA}	236	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

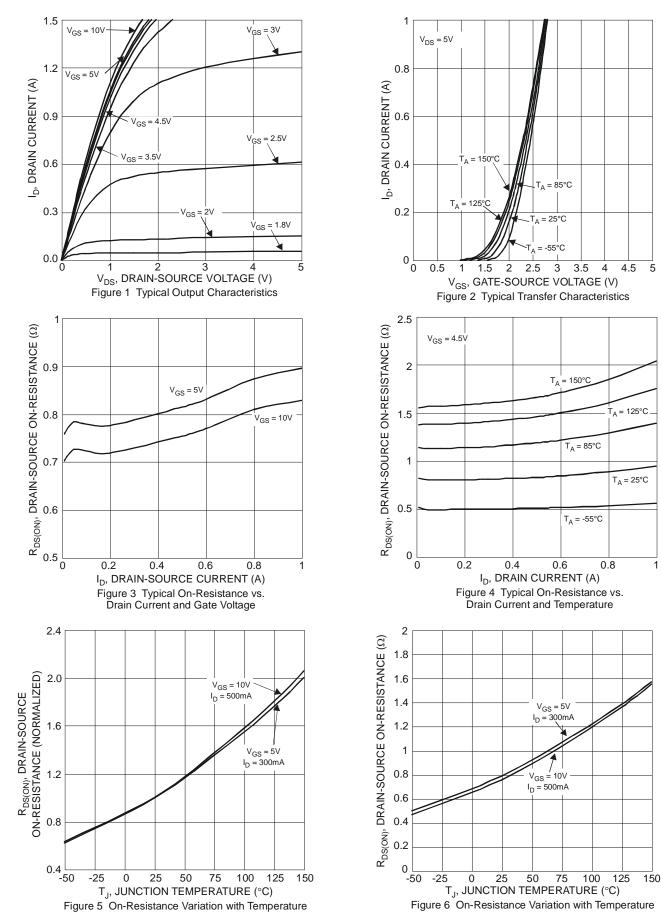
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	I I					
Drain-Source Breakdown Voltage	BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1.0	μA	$V_{DS} = 50V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	—	10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	0.8		1.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}			1.6 2.5 4.5	Ω	
Source-Drain Diode Forward Voltage	V _{SD}	_	_	1.4	V	$V_{GS} = 0V, I_{S} = 500mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	46	_	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	_	5.3	_	pF	
Reverse Transfer Capacitance	Crss	_	4.0	_	pF	
Total Gate Charge	Qg	_	0.6	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_D = 250mA$
Gate-Source Charge	Q _{gs}	_	0.2	_	nC	
Gate-Drain Charge	Q _{gd}	_	0.1	_	nC	
Turn-On Delay Time	t _{D(on)}	_	2.7	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$ $R_G = 25\Omega, I_D = 200mA$
Turn-On Rise Time	tr		2.5	_	ns	
Turn-Off Delay Time	t _{D(off)}		19	_	ns	
Turn-Off Fall Time	t _f	_	11	_	ns	

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate
Short duration pulse test used to minimize self-heating effect.

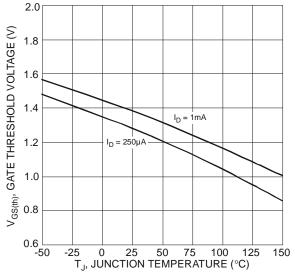
9. Guaranteed by design. Not subject to product testing.

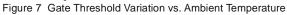


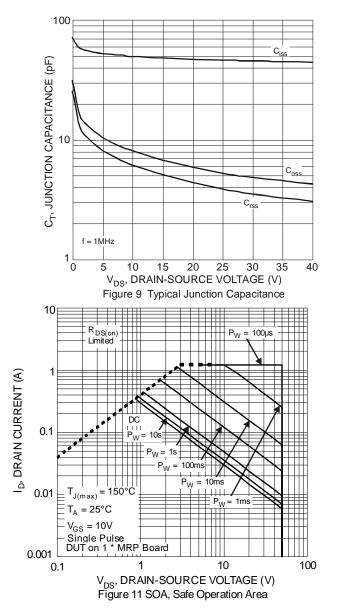


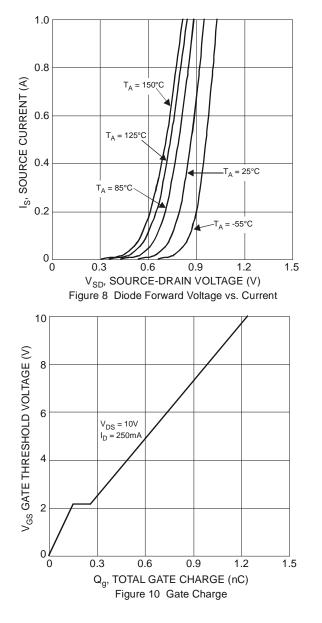
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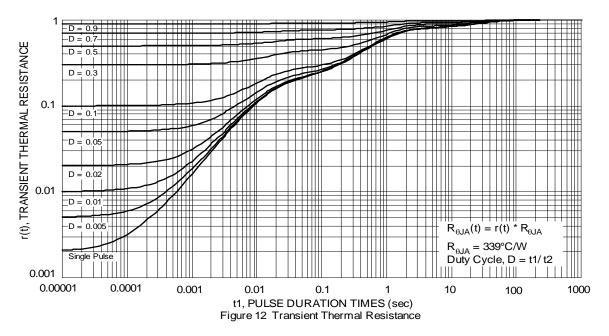








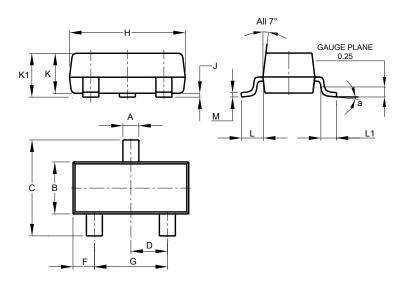






Package Outline Dimensions

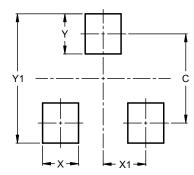
Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



	SOT23					
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
К	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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