

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	±12	V
Drain Current (Note 5)	I _D	5.9	Α
Pulsed Drain Current (Note 6)	I _{DM}	21	A

Thermal Characteristics

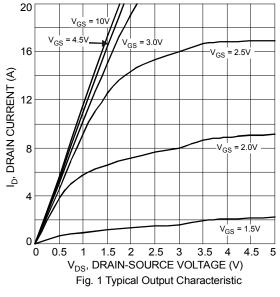
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

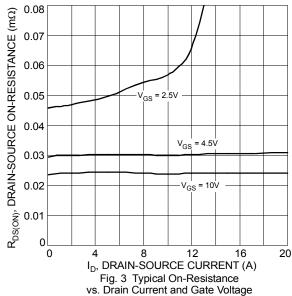
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	V _{GS} = 0V, I _D = 250μA	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.45	_	1.4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		_	24	29	mΩ	V _{GS} = 4.5V, I _D = 5.0A	
Static Drain-Source On-Resistance	R _{DS(ON)}		42	50		V _{GS} = 2.5V, I _D = 3.1A	
			68	100		V _{GS} = 2.0V, I _D = 1.5A	
Forward Transfer Admittance	Y _{fs}	_	8	_	S	V _{DS} =5V, I _D = 2.1A	
Diode Forward Voltage (Note 7)	V_{SD}	_	0.9	1.4	V	$V_{GS} = 0V, I_S = 2.0A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	532	_	pF	V _{DS} = 10V, V _{GS} = 0V _f = 1.0MHz	
Output Capacitance	Coss	_	144	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	117	_	pF		
Gate Resistance	R _G	_	1.3	_	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz	
SWITCHING CHARACTERISTICS (Note 8)							
Total Gate Charge	Qg	_	6.7	_		$V_{DS} = 10V, V_{GS} = 4.5V, I_{D} = 5.0A$	
Gate-Source Charge	Q_{gs}	_	0.8		nC	$V_{DS} = 10V$, $V_{GS} = 4.5V$, $I_D = 5.0A$ $V_{DS} = 10V$, $V_{GS} = 4.5V$, $I_D = 5.0A$	
Gate-Drain Charge	Q _{gd}	_	3.0	_			

- 5. Device mounted on FR-4 PCB, on 2oz Copper pad layout with $R_{\theta JA} = 90^{\circ}$ C/W.
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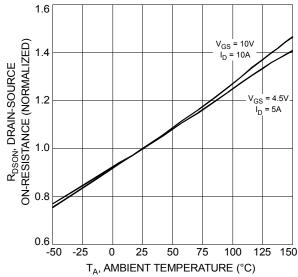
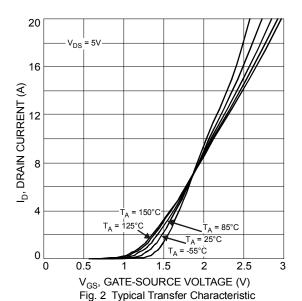
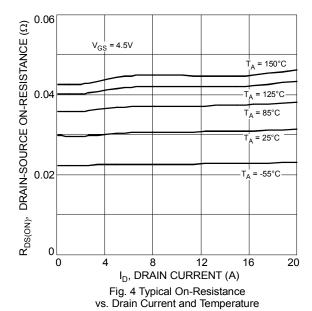


Fig. 5 On-Resistance Variation with Temperature





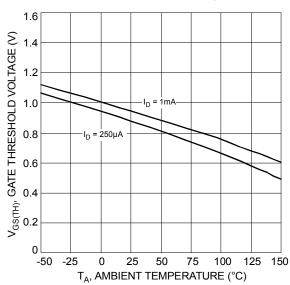
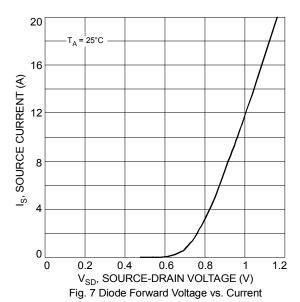
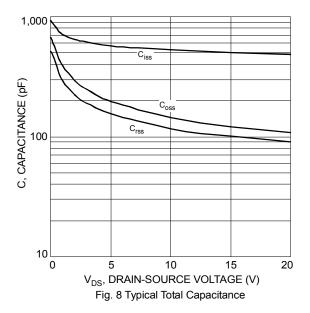


Fig. 6 Gate Threshold Variation vs. Ambient Temperature







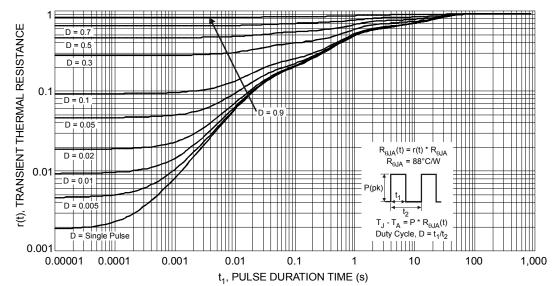
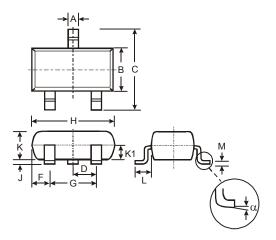


Fig. 9 Transient Thermal Response



Package Outline Dimensions

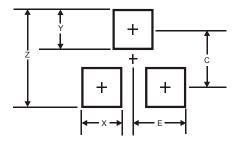
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.903	1.10	1.00		
K1	-	-	0.400		
L	0.45	0.61	0.55		
M	0.085	0.18	0.11		
α	0°	8°	-		
All	All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Е	1.35



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