

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	٧
Average Forward Current	I _{F(AV)}	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	40	Α

Characteristic	Symbol	Ratings	Unit
Human Body Mode ESD Protection	ESD HBM	4000	V
Machine Model ESD Protection	ESD MM	400	V
Charged Device Model	ESD CDM	1	kV

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Ambient (Note 6)	R _{ÐJA}	60	_	°C/W
Thermal Resistance Junction to Ambient (Note 7)	R _{OJA}	180	_	°C/W
Thermal Resistance Junction to Ambient (Note 8)	Reja	110	_	°C/W
Thermal Resistance Junction to Ambient (Note 9)	Reja	55	_	°C/W
Thermal Resistance Junction to Soldering (Note 10)	Reus	10	_	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to	+125	°C

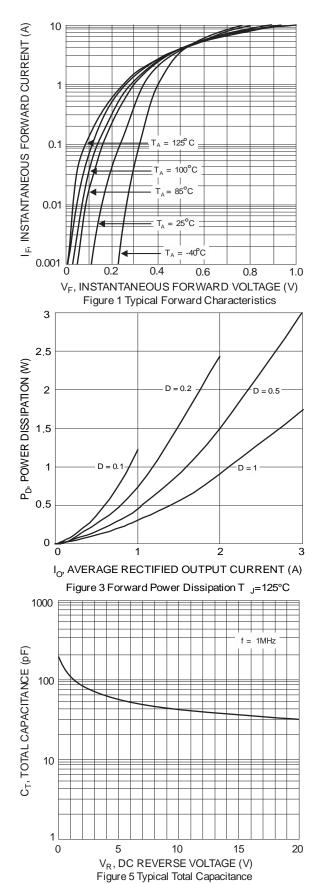
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

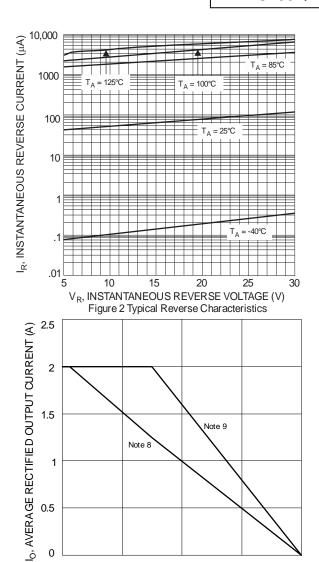
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 11)	$V_{(BR)R}$	30	_	_	V	$I_R = 1.5 \text{mA}$
Forward Voltage	V_{F}		0.36	0.42	V	I _F = 1.0A, T _A = +25°C
Tornara voltago			0.4	0.49	•	$I_F = 2.0A, T_A = +25^{\circ}C$
Leakage Current (Note 11)	I_R	_	0.15	1.0	mA	$V_R = 30V, T_A = +25^{\circ}C$
Total Capacitance	C _T	_	75	_	pF	$V_R = 10V, f = 1.0MHz$
Switching Speed t _{RR}	t _{RR}	_	17	_	ns	I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A (RG1)

Notes:

- 6. Part mounted on 50.8mm \times 50.8mm GETEK board with 25.4mm \times 25.4mm copper pad, 25% anode, 75% cathode. T_A = +25°C.
- 7. Part mounted on FR-4 board with 1.8mm \times 2.5mm cathode and 1.8mm \times 1.2mm anode, 1 oz. copper pads. $T_A = +25^{\circ}C$.
- 8. Part mounted on FR4 PCB, 2oz.
- 9. Part Mounted on 1inch sq. copper pad, 2oz.
- 10. Theoretical R_{9JS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 11. Short duration pulse test to minimize self-heating effect.







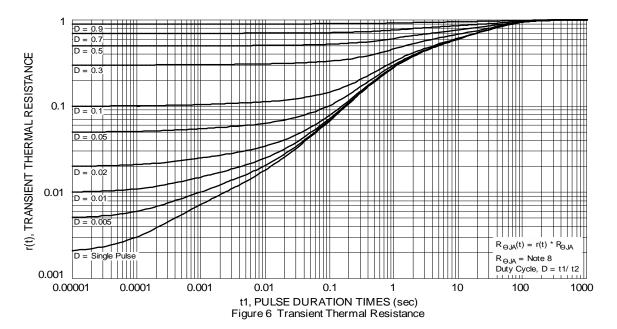
T_A, AMBIENT TEMPERATURE (°C) Figure 4 DC Forward Current Derating

75

25

125

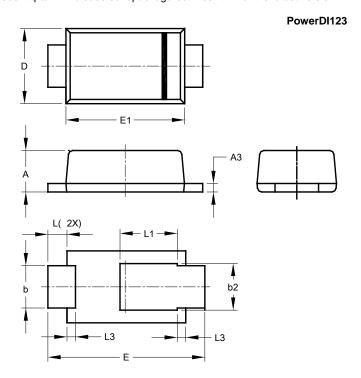






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

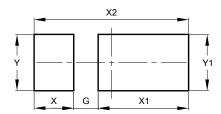


PowerDI123					
Dim	Min	Max	Тур		
Α	0.93	1.00	0.98		
А3	0.15	0.25	0.20		
b	0.85	1.25	1.00		
b2	1.025	1.125	1.10		
D	1.63	1.93	1.78		
Е	3.50	3.90	3.70		
E1	2.60	3.00	2.80		
L	0.40	0.50	0.45		
L1	1.25	1.40	1.35		
L3	0.125	0.275	0.20		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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