

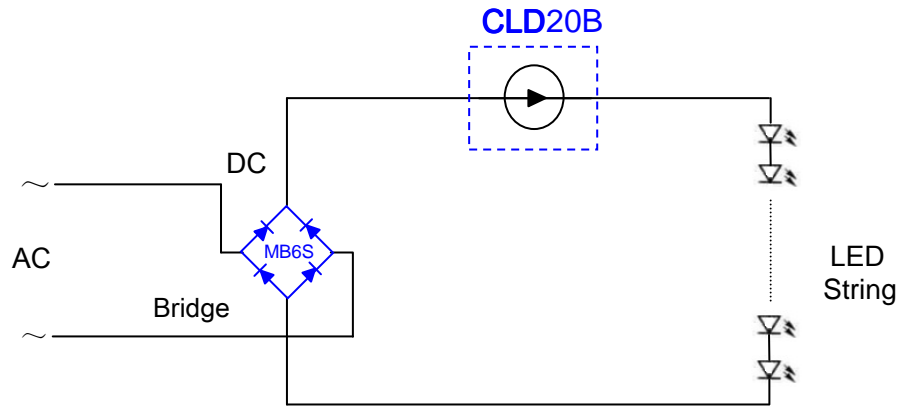
| Part number | Steady Current Ireg(SS) | | | Limiting Voltage @ 80% Ireg | Test Voltage | Peak Operating Voltage | Rating Voltage |
|-------------|-------------------------|--------------|-------------|--------------------------------|--------------|------------------------------|----------------|
| | Min (mA) | Typ. (mA) | Max (mA) | Max (V) | (V) | (V) | Max (V) |
| CLD20B | 17 | 20 | 23 | 4.5 | 10 | 45 | 25 |

The graph shows the relationship between the regulation current I_{reg} (mA) and the operation voltage (V) for the proposed circuit. The current remains near zero until approximately 50V, after which it rises sharply, reaching 60mA at about 55V. The region of low current is labeled 'VR'.



CLD20B

Application



Type Application Circuit
(20mA each LED String)

Number of LED's that can be connected is determined by:

$$\text{LEDs} = (\text{Vdc} - \text{Vak}) / \text{LED } V_F$$

Example: $V_{ac}=110V$, CLD $V_{ak}=45V$, LED $V_F=3.5V@20mA$

Using our [P/N:MB6S](#) bridge for whole wave rectifier

After rectifier, Output $V_{dc}=150V$

$(150V_{dc}-45V_{dc})/3.5V_{dc}=30$ LEDs in series.



Ordering Information

| Device | Packing |
|------------------|----------------------|
| (Part Number)-TP | Tape&Reel:3Kpcs/Reel |

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