

# **Electrical Characteristics**

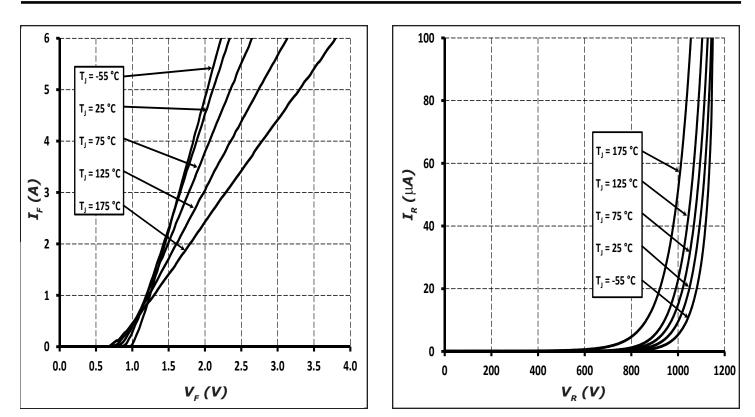
Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.5 1.8	1.7 2.4	V	$I_{F} = 2 A T_{J} = 25^{\circ}C$ $I_{F} = 2 A T_{J} = 175^{\circ}C$	Fig.1
I <sub>R</sub>	Reverse Current	3 6	15 55	μΑ	$V_{R} = 600 V T_{J} = 25^{\circ}C$ $V_{R} = 600 V T_{J} = 175^{\circ}C$	Fig. 2
Q <sub>c</sub>	Total Capacitive Charge	5.8		nC	V <sub>R</sub> = 400 V, I <sub>F</sub> = 2A d <i>i</i> /d <i>t</i> = 500 A/µS T <sub>J</sub> = 25°C	Fig. 5
С	Total Capacitance	175 10.5 8.5		pF	$ \begin{array}{l} V_{_R} = 0 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \\ V_{_R} = 200 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \\ V_{_R} = 400 \ V, \ T_{_J} = 25 \ ^\circ C, \ f = 1 \ MHz \end{array} $	Fig. 6
E <sub>c</sub>	Capacitance Stored Energy	0.8		μJ	V <sub>R</sub> = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Unit
R <sub>ejc</sub>	TO-252 Package Thermal Resistance from Junction to Case	3.8	°C/W

# **Typical Performance**









### **Typical Performance**

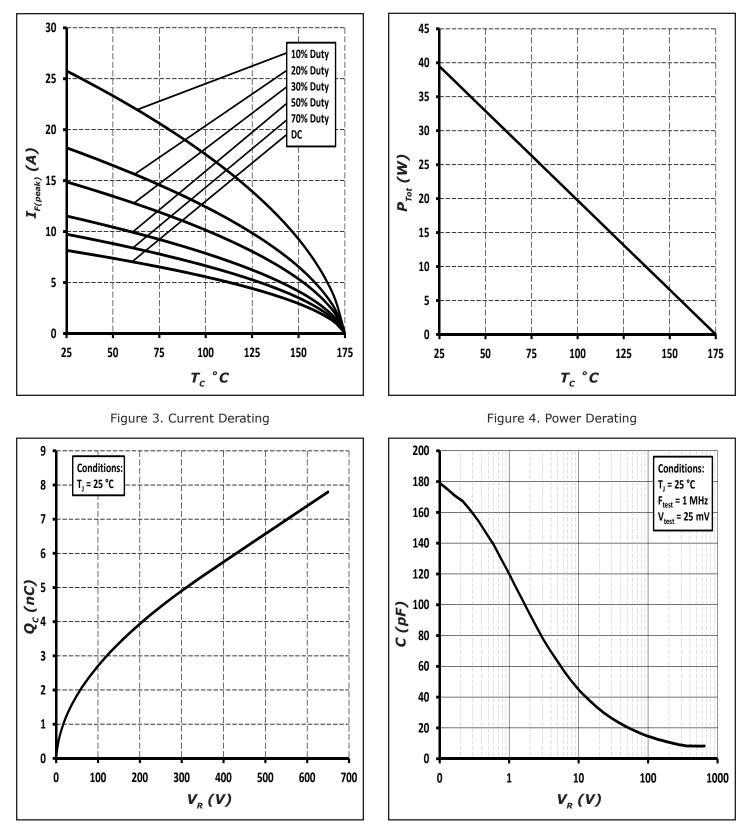
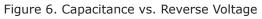


Figure 5. Total Capacitance Charge vs. Reverse Voltage





#### **Typical Performance**

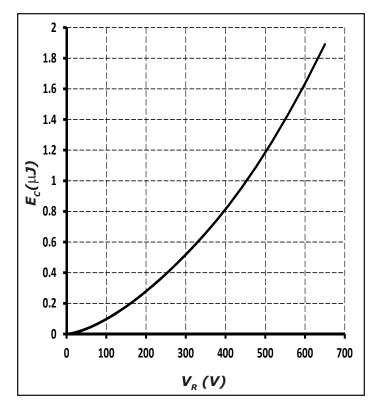


Figure 7. Capacitance Stored Energy

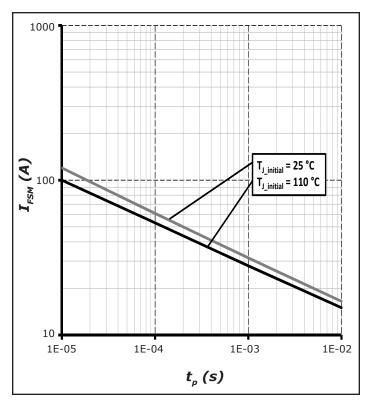


Figure 8. Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

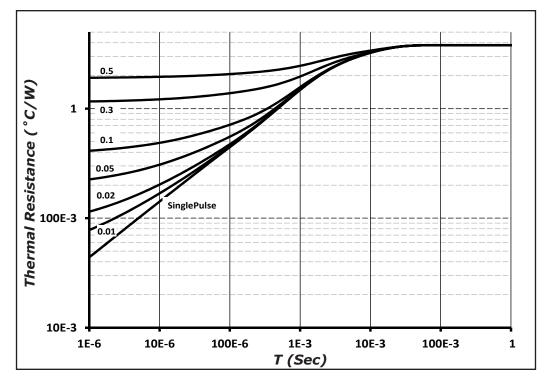
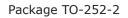


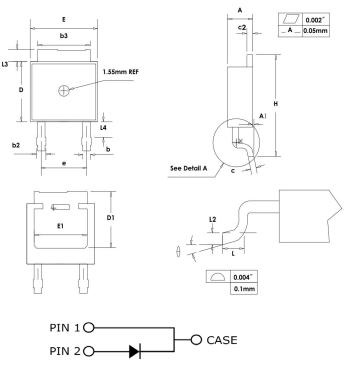
Figure 9. Transient Thermal Impedance

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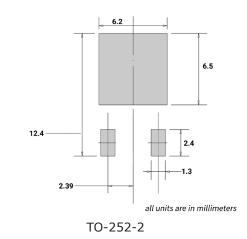
## Package Dimensions





SYMBOL	MILLIMETERS			
JINDOL	MIN	MAX		
A	2.159	2.413		
A1	0	0.13		
b	0.64	0.89		
b2	0.653	1.143		
b3	5.004	5.6		
С	0.457	0.61		
c2	0.457	0.864		
D	5.867	6.248		
D1	5.21	-		
E	6.35	6.73		
E1	4.32	-		
e	4.58 BSC			
Н	9.65	10.414		
L	1.106	1.78		
L2	0.51 BSC			
L3	0.889	1.27		
L4	0.64	1.01		
θ	0°	8°		

## **Recommended Solder Pad Layout**



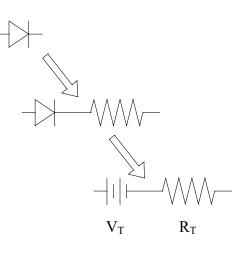
Part Number	Package	Marking
C3D02060E	TO-252-2	C3D02060

Note: Recommended soldering profiles can be found in the applications note here: http://www.wolfspeed.com/power\_app\_notes/soldering





#### Diode Model



 $Vf_{T} = V_{T} + If^{*}R_{T}$ 

١

$$V_{T} = 0.98 + (T_{J}^{*} - 1.1^{*}10^{-3})$$
  

$$R_{T} = 0.18 + (T_{J}^{*} 1.8^{*}10^{-3})$$

Note:  $T_i$  = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

#### Notes

#### **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfpseed representative or from the Product Ecology section of our website at http:// www.wolfspeed.com/Power/Tools-and-Support/Product-Ecology.

#### **REACh Compliance** .

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

#### **Related Links**

- Cree SiC Schottky diode portfolio: http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes
- Schottky diode Spice models: http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2
- SiC MOSFET and diode reference designs: http://go.pardot.com/l/101562/2015-07-31/349i

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Cree. Inc. 4600 Silicon Drive Durham, NC USA Tel: +1.919.313.5300 Fax: +1.919.313.5451 www.cree.com/power