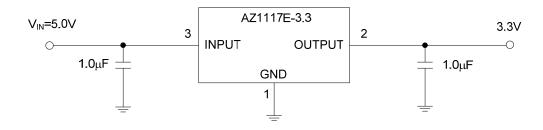


Typical Applications Circuit (Cont. Note 4)



Note 4: The AZ1117E is compatible with Low ESR ceramic capacitor. A minimum of 1.0µF input and output capacitors are required. The ESR of the output capacitors must be less than 1.5Ω . Close to the OUTPUT pin, it is not recommended to use a capacitor smaller than 0.68µF in parallel with output capacitor. When the output capacitor parallels 0.1µF capacitor, the 0.1µF capacitor must be away from the OUTPUT pin, the distance is no less than 5mm.

Pin Descriptions

Pin Number	Pin Name	Function
1	ADJ/GND	Adjustable pin or ground pin
2	OUTPUT	Regulator output pin
3	INPUT	Supply voltage pin

Absolute Maximum Ratings (Note 5)

Symbol	Parameter	Rating	Unit
V _{IN}	Power Supply Voltage	16	V
TJ	Operating Junction Temperature Range	+150	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
θ_{JA}	Thermal Resistance (Junction to Ambient) (Note 6)	65	°C/W
-	ESD (Machine Model)	200	٧
_	ESD (Human Body Model)	2000	V

Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.
 Chip is soldered to 200mm²(16mm*12.5mm) copper (top side solder mask) on 2oz.2 layers FR-4 PCB with 8*0.5mm vias.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Supply Voltage	_	13	V
TJ	Operating Junction Temperature Range	-40	+125	°C





AZ1117E

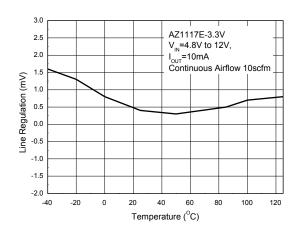
Electrical Characteristics (@V_{IN} = V_{OUT}+1.5V, C_{IN} = 1.0 μ F (Ceramic), C_{OUT} = 1.0 μ F (Ceramic), Typical T_A = +25°C, **Bold** typeface applies over -40°C \leq T_J \leq +125°C ranges, unless otherwise specified.)

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{REF}		$V_{OUT}+1.5V \le V_{IN} \le 12V$, $I_{OUT} = 10mA$		1.238	1.250	1.262	V
	Reference Voltage			98%*V _{OUT}	V _{OUT}	102%*V _{OUT}	V
		For 1.2V, $V_{OUT}+1.5V \le V_{IN} \le 12V$, $I_{OUT}=10mA$		98%*V _{OUT}	V _{OUT}	102%*V _{OUT}	V
	Output Voltage			96%*V _{OUT}	V _{OUT}	104%*V _{OUT}	V
V _{OUT}	(Fixed Versions)	For 1.5V to 5V,		99%*V _{OUT}	Vout	101%*V _{OUT}	V
		$V_{OUT}+1.5V \le V_{IN} \le 1$ $I_{OUT} = 10mA$	2V,	98%*V _{OUT}	Vout	102%*V _{OUT}	V
V_{DROP}	Dropout Voltage	I _{OUT} = 1A		_	1.1	1.3	V
I _{OUT(MAX)}	Maximum Output Current	1.5V ≤ V _{IN} -V _{OUT}		1	1.3	_	Α
V _{RLOAD}	Load Regulation	$V_{IN} = V_{OUT} + 1.5V$ $1mA \le I_{OUT} \le 1A$		_	0.2	0.6	%/A
V _{RLINE}	Line Regulation	$1.5V \le V_{IN} - V_{OUT} \le 10$ $I_{OUT} = 30 \text{mA}$	0V,	_	0.001	±0.04	%/V
lα	Quiescent Current	For Fixed Voltage Volt	ersion,	_	3.5	6	mA
-	Minimum Load Current	For ADJ Version, 1.5V ≤ V _{IN} -V _{OUT} ≤ 10V		_	2	5	mA
I _{ADJ}	Adjustable Pin Current	_		-	45	90	μA
-	Adjustable Pin Current Change	1.5V ≤ V _{IN} -V _{OUT} ≤ 10	0V	_	0.2	5	μA
PSRR	Power Supply Rejection Ratio	Ripple 1.0 Vp-p V _{IN} = V _{OUT} +2V,	f = 120Hz	_	70	_	dB
1 01414		I _{OUT} = 100mA	f = 1kHz	_	70	_	
$\frac{\Delta V_{\rm OUT} / V_{\rm OUT}}{\Delta T}$	Output Voltage Temperature Coefficient	I _{OUT} = 30mA		_	±30	_	ppm/ °C
V _{NOISE}	RMS Output Noise	10Hz ≤ f ≤ 100kHz, No Load		-	0.003	_	%
T _{OTSD}	Thermal Shutdown Temperature	-		_	+170	_	°C
THYOTSD	Thermal Shutdown Hysteresis	_		_	+20	_	°C
θЈС	Thermal Resistance (Junction to Case)	SOT-223		_	40	_	°C/W

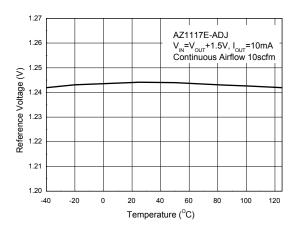


Performance Characteristics

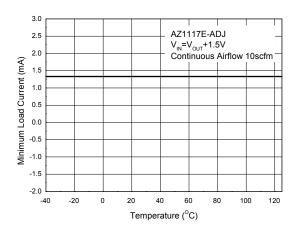
Line Regulation vs. Temperature



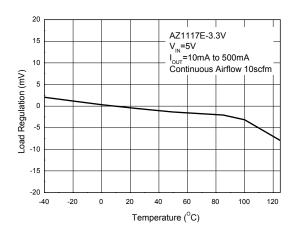
Reference Voltage vs. Temperature



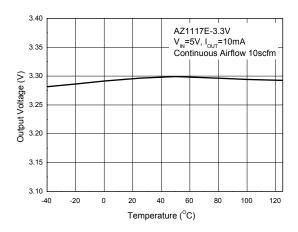
Minimum Load Current vs. Temperature



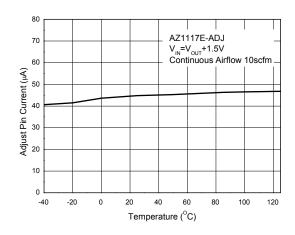
Load Regulation vs. Temperature



Output Voltage vs. Temperature



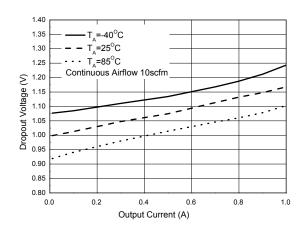
Adjust Pin Current vs. Temperature



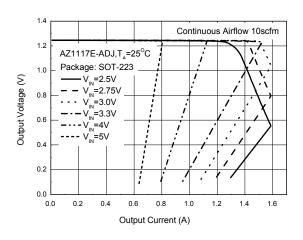


Performance Characteristics (Cont.)

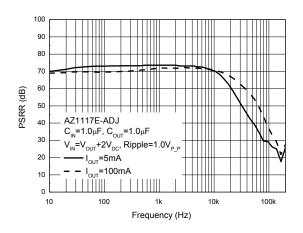
Dropout Voltage vs. Output Current



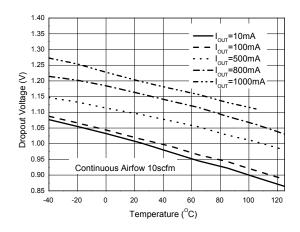
Output Voltage vs. Output Current



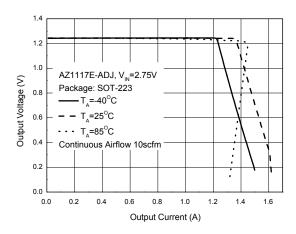
PSRR vs. Frequency



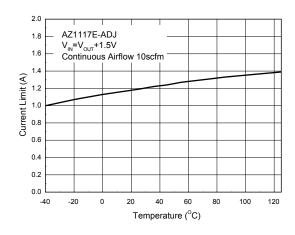
Dropout Voltage vs. Temperature



Output Voltage vs. Output Current



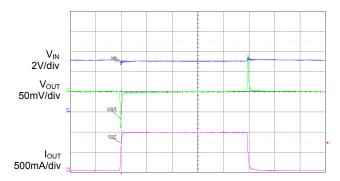
Current Limit vs. Temperature





Performance Characteristics (Cont.)

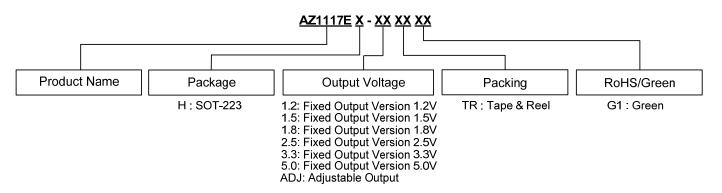
Load Transient Response $(AZ1117E\text{-}ADJ,\ V_{\text{IN}}\text{=}5V,\ V_{\text{OUT}}\text{=}3.3V,$ $C_{\text{IN}}\text{=}1.0\mu\text{F},\ C_{\text{OUT}}\text{=}1.0\mu\text{F})$



Time 100µs/div



Ordering Information

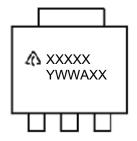


Package	Temperature Range	Part Number	Marking ID	Packing	
		AZ1117EH-1.2TRG1	GH23F	4000/Tape & Reel	
	-40 to +125°C	AZ1117EH-1.5TRG1	GH27F	4000/Tape & Reel	
		AZ1117EH-1.8TRG1	GH18G	4000/Tape & Reel	
SOT-223		AZ1117EH-2.5TRG1	GH23G	4000/Tape & Reel	
		AZ1117EH-3.3TRG1	GH27G	4000/Tape & Reel	
		AZ1117EH-5.0TRG1	GH18H	4000/Tape & Reel	
		AZ1117EH-ADJTRG1	GH23H	4000/Tape & Reel	

Marking Information

SOT-223 Series

(Top View)



First Line: Logo and Marking ID (See Ordering Information) Second Line: Date Code

Y: Year

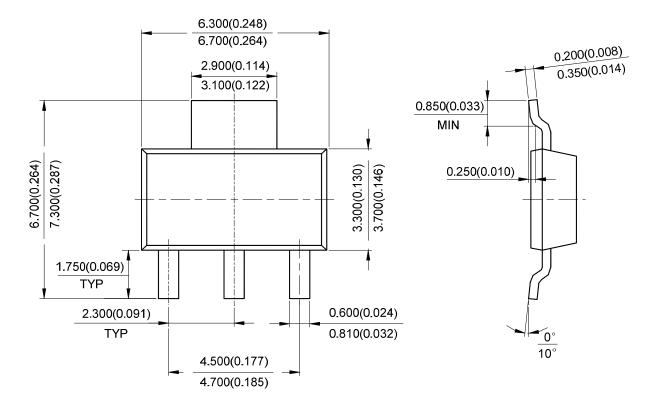
WW: Work Week of Molding A: Assembly House Code

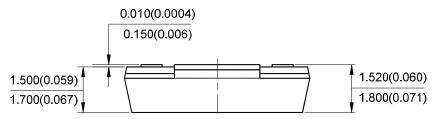
XX: 7th and 8th Digits of Batch Number



Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: SOT-223



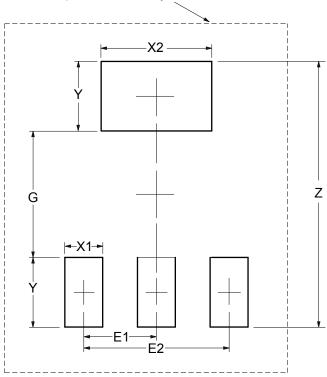




Suggested Pad Layout

(1) Package Type: SOT-223

Grid placement courtyard



Dimensions	Z	G	X1	X2	Y	E1	E2
	(mm)/(inch)						
Value	8.400/0.331	4.000/0.157	1.200/0.047	3.500/0.138	2.200/0.087	2.300/0.091	4.600/0.181



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