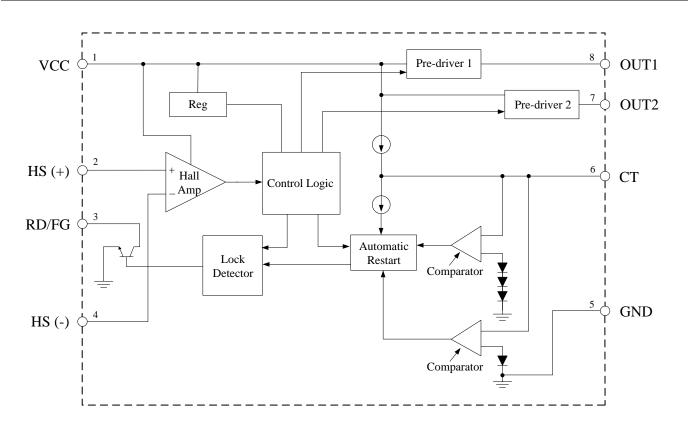


### **Pin Descriptions**

Pin Number	Pin Name					
	AM4406	AM4406F	Function			
1	VCC	VCC	Power supply			
2	HS (+)	HS (+)	Hall input (+)			
3	RD	FG	Rotation detection/Frequency generation			
4	HS (-)	HS (-)	Hall input (-)			
5	GND	GND	Ground			
6	СТ	СТ	Timing capacitor			
7	OUT2	OUT2	Driver output 2			
8	OUT1	OUT1	Driver output 1			

# **Functional Block Diagram**





### Absolute Maximum Ratings (Note 2)

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	30	V
I <sub>OUT</sub>	Output Current	70	mA
PD	Power Dissipation	550 (Note 3)	mW
T <sub>STG</sub>	Storage Temperature Range	-55 to +125	°C
ESD	ESD (Human Body Model)	3000 V	
ESD	ESD (Machine Model)	300	V

Note 2: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 3: Reduced by 5.5 mW/°C when  $T_A$  is over +25°C.

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Мах	Unit
Vcc	Supply Voltage	4	28	V
V <sub>HS</sub> (+)	Hall Input Voltage (+) (Note 4)	1.0	V <sub>CC</sub> -0.5	V
Vнs (-)	Hall Input Voltage (-) (Note 4)	1.0	V <sub>CC</sub> -0.5	V
TA	Operating Temperature	-40	+95	°C

Note 4: Hall input voltage range includes the amplitude of signal.

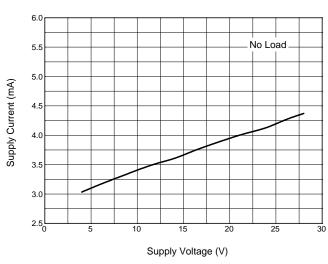


# Electrical Characteristics (V<sub>CC</sub>=12V, T<sub>A</sub>=+25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Icc	Supply Current	No load	_	3.2	5.0	mA
V <sub>HYS</sub> (+)	Hall Amplifier Input Hysteresis (+)	Zero to peak including offset and hysteresis	3	_	15	mV
V <sub>HYS</sub> (-)	Hall Amplifier Input Hysteresis (-)	Zero to peak including offset and hysteresis	-3	_	-15	mV
ICHG	CT Charge Current	V <sub>CT</sub> =1.5V	2	3.45	5.25	μA
I <sub>DHG</sub>	CT Discharge Current	V <sub>CT</sub> =1.5V	0.35	0.8	1.45	μA
R <sub>CD</sub>	CT Charge and Discharge Ratio	ICHG/ IDHG	3	4.5	8	_
V <sub>CL</sub>	CT Clamp Voltage	_	2.2	2.6	3	V
V <sub>CP</sub>	CT Comparator Voltage	-	0.4	0.6	0.8	V
V <sub>OH1</sub>	OUT1 High Level Voltage	I <sub>OUT1</sub> =10mA	10	10.5	_	V
V <sub>OH2</sub>	OUT2 High Level Voltage	I <sub>OUT2</sub> =10mA	10	10.5	_	V
V <sub>RDL</sub>	RD Output Low Level Voltage	I <sub>RD</sub> =5mA	_	0.2	0.5	V
I <sub>RD</sub>	RD Current Capacity	V <sub>RD</sub> =2V	8	18	_	mA
VFGL	FG Output Low Level Voltage	I <sub>FG</sub> =5mA	_	0.2	0.5	V
I <sub>FG</sub>	FG Current Capacity	V <sub>FG</sub> =2V	8	18	_	mA

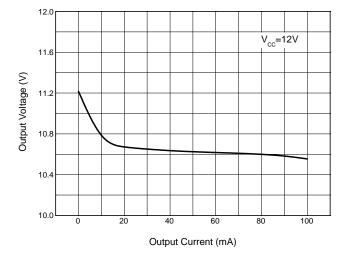


#### **Performance Characteristics**

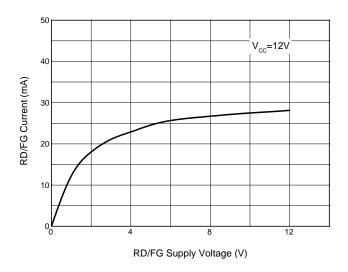


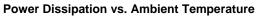
#### Supply Current vs. Supply Voltage

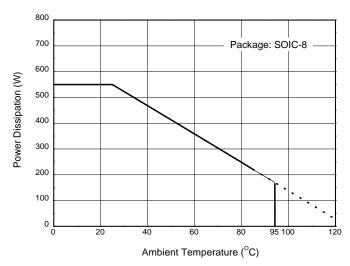
**Output Voltage vs. Output Current** 



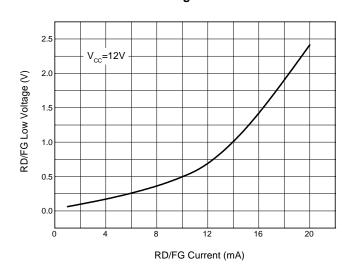
RD/FG Current vs. RD/FG Supply Voltage





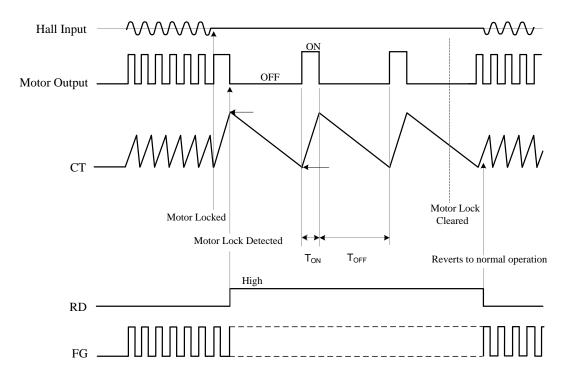


RD/FG Low Voltage vs. RD/FG Current





### **Operating Diagram**

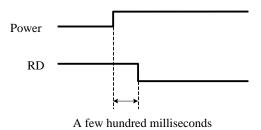


Note 5: Automatic restart is performed in the following manner. A motor lock condition is detected when the Hall signal stops switching. The output is ON when CT pin is being charged. C2 is the external capacitor of the CT pin. Output ON time and OFF time are determined by the capacitance of C2.



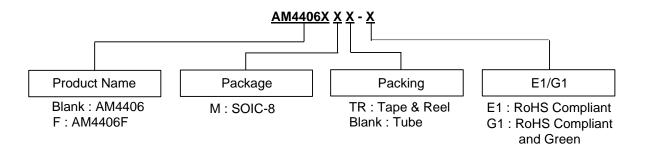
$$T_{ON} = \frac{C2^{*}(V_{CL} - V_{CP})}{I_{CHG}} (Sec.)$$
$$T_{OFF} = \frac{C2^{*}(V_{CL} - V_{CP})}{I_{DHG}} (Sec.)$$

Note 7: The RD pin may maintain HIGH level for a few hundred milliseconds when the power is turned on.





## Ordering Information

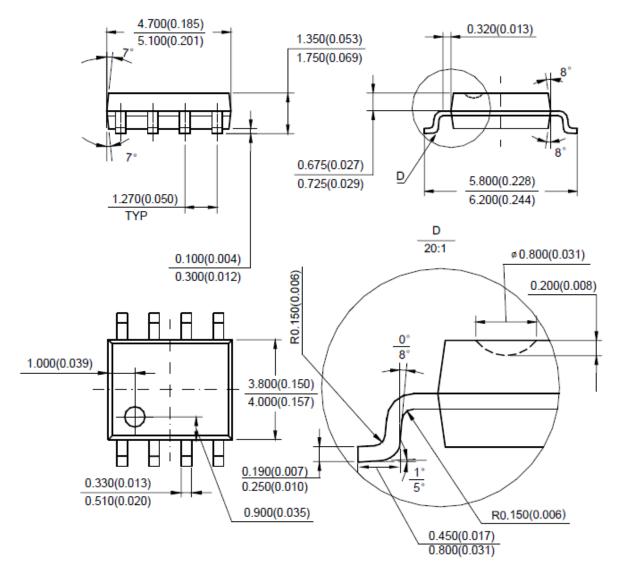


Package	Temperature Range	Part Number		Markin		
		RoHS Compliant	RoHS Compliant and Green	RoHS Compliant	RoHS Compliant and Green	Packing
SOIC-8	-40 to +95℃	AM4406M-E1	AM4406M-G1	AM4406M	AM4406M-G1	Tube
		AM4406MTR-E1	AM4406MTR-G1	AM4406M	AM4406M-G1	Tape & Reel
		_	AM4406FMTR-G1	_	AM4406FM-G1	Tape & Reel



#### Package Outline Dimensions (All dimensions in mm(inch).)

#### (1) Package Type: SOIC-8



Note: Eject hole, oriented hole and mold mark is optional.



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