

45 ns and B3KE EOL Suplemental Information

EOL Data Sheet

AC Characteristics

Table 1: Read Cycle Timing Parameters V_{DD} = 3.0-3.6V for SST39LF010/020/040

		SST39LF010-45 SST39LF020-45 SST39LF040-45		
Symbol	Parameter	Min	Max	Units
T _{RC}	Read Cycle Time	45		ns
T _{CE}	Chip Enable Access Time		45	ns
T _{AA}	Address Access Time		45	ns
T _{OE}	Output Enable Access Time		30	ns
T _{CLZ} ¹	CE# Low to Active Output	0		ns
T _{OLZ} 1	OE# Low to Active Output	0		ns
T _{CHZ} ¹	CE# High to High-Z Output		15	ns
T _{OHZ} ¹	OE# High to High-Z Output		15	ns
T _{OH} ¹	Output Hold from Address Change	0		ns

T1.2 25162

Packaging Diagram

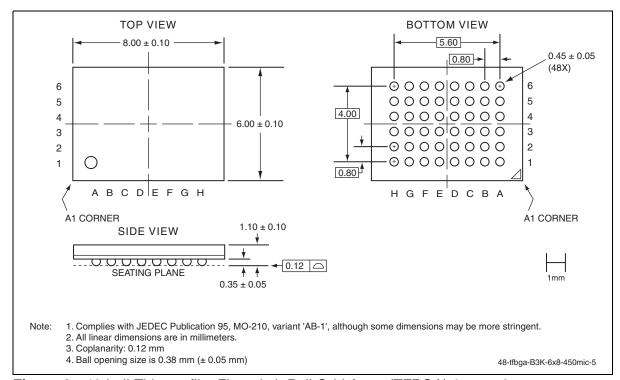


Figure 2: 48-ball Thin-profile, Fine-pitch Ball Grid Array (TFBGA) 6mm x 8mm SST Package Code: B3K

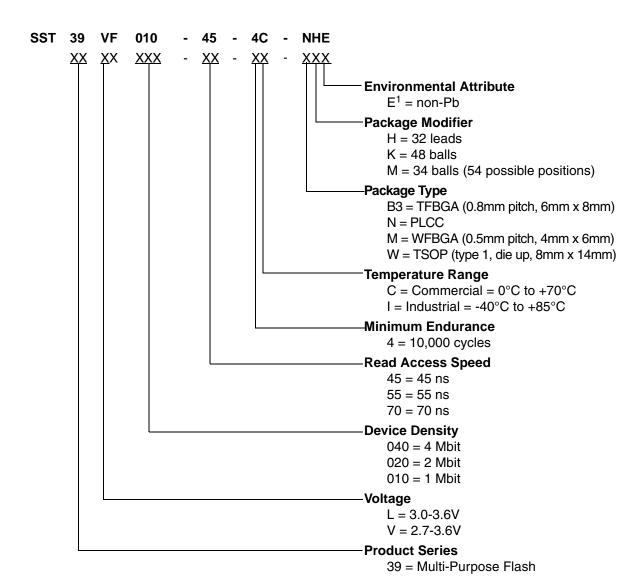
^{1.} This parameter is measured only for initial qualification and after a design or process change that could affect this parameter.



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Product Ordering Information



Environmental suffix "E" denotes non-Pb solder. SST non-Pb solder devices are RoHS compliant.



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Valid combinations for SST39LF010

SST39LF010-45-4C-NHE SST39LF010-45-4C-WHE SST39LF010-45-4C-B3KE

SST39LF010-45-4C-MME

Valid combinations for SST39VF010

SST39VF010-70-4C-B3KE SST39VF010-70-4I-B3KE

Valid combinations for SST39LF020

SST39LF020-45-4C-NHE SST39LF020-45-4C-WHE SST39LF020-45-4C-B3KE

SST39LF020-45-4C-MME

Valid combinations for SST39VF020

SST39VF020-70-4C-B3KE SST39VF020-70-4I-B3KE

Valid combinations for SST39LF040

SST39LF040-45-4C-NHE SST39LF040-45-4C-WHE SST39LF040-45-4C-B3KE

Valid combinations for SST39VF040

SST39VF040-70-4C-B3KE SST39VF040-70-4I-B3KE

Note: Valid combinations are those products in mass production or will be in mass production. Consult your SST sales representative to confirm availability of valid combinations and to determine availability of new combinations.



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Table 2: Revision History

Revision		Description	Date
Α	•	EOL document for 45 ns parts and B3KE parts	Jun 2013

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Memory sizes denote raw storage capacity; actual usable capacity may be less.

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