

- Configured, controlled, and monitored using a generic 32-bit microprocessor interface
- Provides a standard 5-signal IEEE 1149.1 JTAG test port for boundary scan test purposes

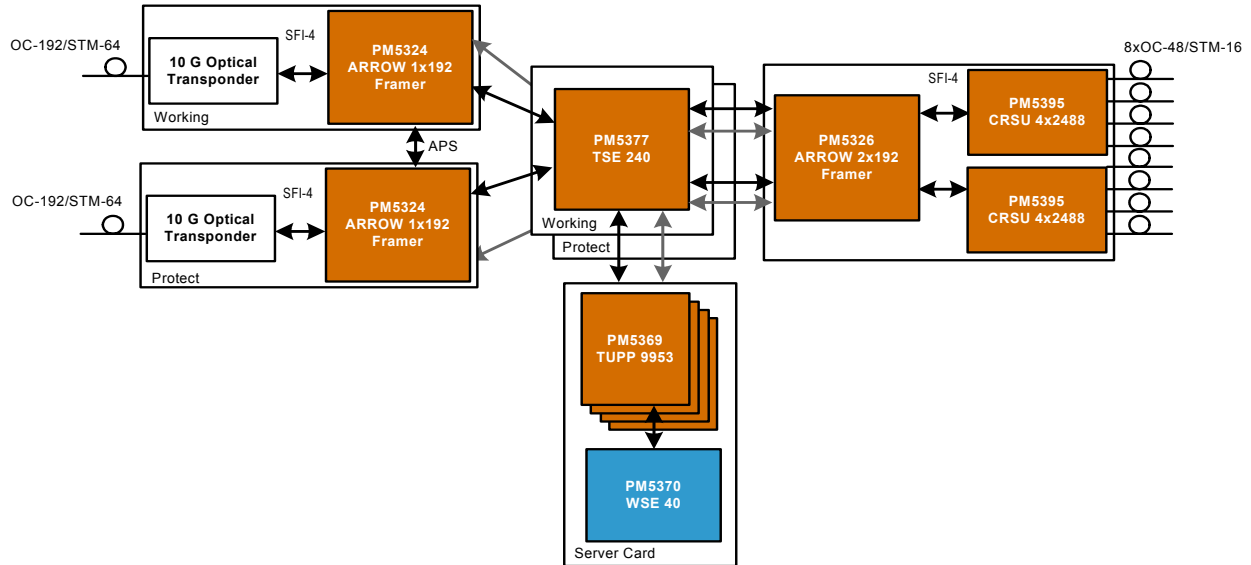
Package

- Implemented in 1.2 V core and 2.5 V I/O 0.13µm CMOS technology. Inputs are 3.3 V tolerant
- Packaged in a 672-ball FCBGA top-hat, 27 mm x 27mm

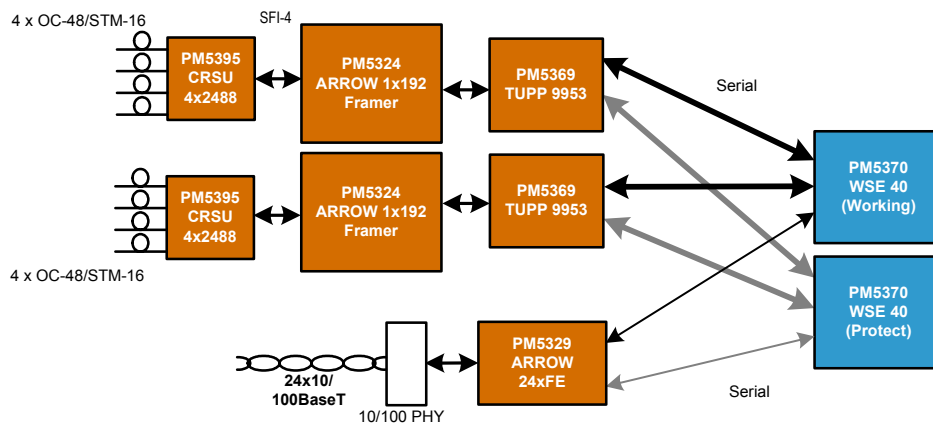
Applications

- SONET/SDH Add-Drop Multiplexer (ADM)
- SONET/SDH Digital Cross-connect (DCC)
- Multi-service Provisioning Platform (MSPP)
- Multi-service ADM (MS-ADM)
- Multi-service Switch (MSS)
- Optical Access Mux
- Terminal Multiplexers

OC-192/STM-64 Optical Cross-Connect with 40G VT/TU Cross-Connect Server Card



OC-48/STM-16 VT/TU ADM/Cross-Connect



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