

# VS-48CTQ060PbF, VS-48CTQ060-N3

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	20 A	T <sub>.1</sub> = 25 °C	0.61	V	
		40 A	1j=25 C	0.83		
		20 A	T 405 00	0.58		
		40 A	T <sub>J</sub> = 125 °C	0.75		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V Detectiv	2	mA	
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	89		
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = T <sub>J</sub> maximum		0.37	V	
Forward slope resistance	r <sub>t</sub>			8.26	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1220	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

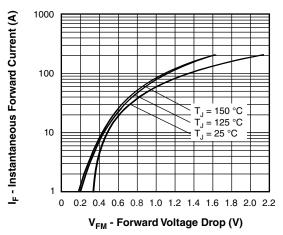
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		٥	DC operation	2.0		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>		1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
A				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque -	maximum			12 (10)	(lbf $\cdot$ in)	
Marking device			Case style TO-220AB	48CT	Q060	



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1000  $\Gamma_{\rm J} = 150$ I<sub>R</sub> - Reverse Current (mA) 100 = 125 °C 10 = 100 °C≣ T<sub>J</sub> = 75 °C  $T_J = 50 \, ^{\circ}C$  $T_1 = 25 \, ^{\circ}C$ 0.01 0.001 0 10 20 50 30 40 60 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

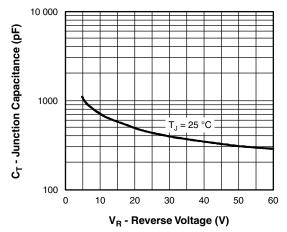


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

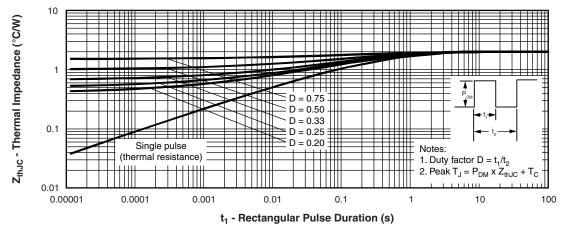


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



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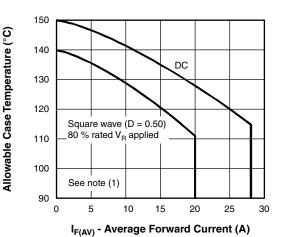


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

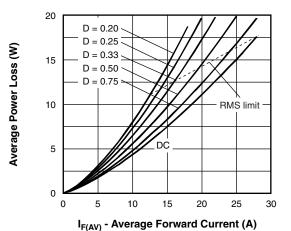


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

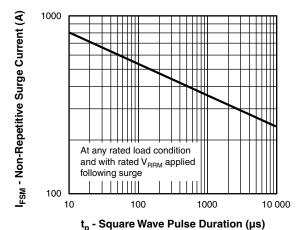


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

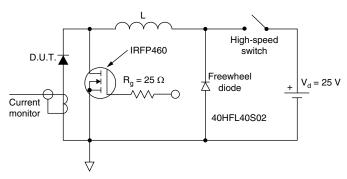


Fig. 8 - Unclamped Inductive Test Circuit

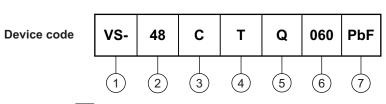
### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{B1} \times I_B$  (1 - D);  $I_B$  at  $V_{B1} = 10 \text{ V}$ 



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### **ORDERING INFORMATION TABLE**



- 1 Vishay Semiconductors product
- 2 Current rating (40 A)
- 3 Circuit configuration
  - C = Common cathode
- 4 Package
  - T = TO-220
- 5 Schottky "Q" series
- 6 Voltage rating (060 = 60 V)
- 7 Environmental digit
  - PbF = Lead (Pb)-free and RoHS compliant
  - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-48CTQ060PbF	50	1000	Antistatic plastic tube				
VS-48CTQ060-N3	50	1000	Antistatic plastic tube				

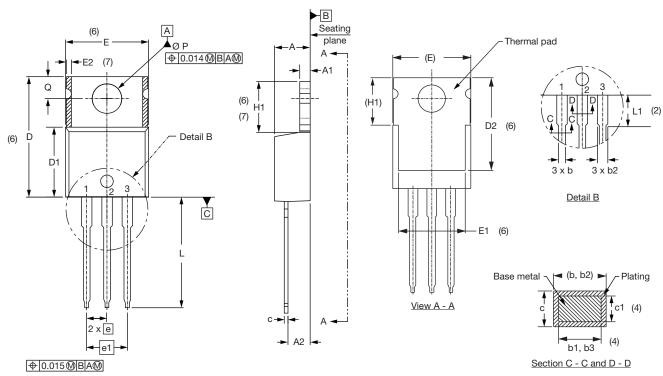
LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95222		
Part marking information -	TO-220AB PbF	www.vishay.com/doc?95225		
	TO-220AB -N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95424		



### Vishay Semiconductors

### **TO-220AB**

#### **DIMENSIONS** in millimeters and inches



### Lead assignments

### <u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIMETERS		INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip

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