

Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Paramete	Rating	Unit	
V_{KA}	Cathode Voltage		40	V
I _{KA}	Continuous Cathode Current		150	mA
I _{REF}	Reference Input Current		-0.050 to +10	mA
T_J	Operating Junction Temperature		+150	°C
T _{ST}	Storage Temperature		-55 to +150	°C
Pn	Power Dissipation (Notes 5 & 6)	SOT23	330	mW
r _D	Power Dissipation (Notes 5 & 6)	SOT25	500	mW

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V_{KA}	Cathode Voltage	V_{REF}	36	V
I _{KA}	Cathode Current	0.065	100	mA
T _A	Operating Ambient Temperature	-40	+125	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test 0	Conditions	Min	Тур	Max	Unit
	Deference Voltage	$V_{KA} = V_{REF}$	ZXRE250A	2.470	2.495	2.520	V
V_{REF}	Reference Voltage	$I_{KA} = 10mA$	ZXRE250B	2.482	2.495	2.507	V
	Deviation of Defendance Value on Ocean Full	V V	$T_A = 0 \text{ to } +70^{\circ}\text{C}$	-	6	16	mV
V_{DEV}	Deviation of Reference Voltage Over Full Temperature Range (Note 7)	$V_{KA} = V_{REF},$ $I_{KA} = 10mA$	$T_A = -40 \text{ to } +85^{\circ}\text{C}$	-	14	34	mV
	Temperature Range (Note 7)	IKA – TOTTA	$T_A = -40 \text{ to } +125^{\circ}\text{C}$	-	14	34	mV
ΔV_{REF}	Ratio of the Change in Reference		$V_{KA} = 10V \text{ to } V_{REF}$	-	-1.4	-2.7	mV/V
ΔV _{KA}	Voltage to the Change in Cathode Voltage	$I_{KA} = 10mA$	V _{KA} = 36V to 10V	-	-1	-2	mV/V
I _{REF}	Reference Input Current	$I_{KA} = 10mA, R1 = 10$	0KΩ, R2 = ∞	-	1	4	μA
	I _{REF} Deviation Over Full Temperature Range (Note 7)	$I_{KA} = 10 \text{mA}, R1 = 10 \text{mA}$	$T_A = 0 \text{ to } +70^{\circ}\text{C}$	-	0.8	1.2	μA
ΔI_{REF}			$T_A = -40 \text{ to } +85^{\circ}\text{C}$	-	0.8	2.5	μA
	range (Note 1)		$T_A = -40 \text{ to } +125^{\circ}\text{C}$	-	0.8	2.5	μA
I _{KA(MIN)}	Minimum Cathode Current for Regulation	$V_{KA} = V_{REF}$	$V_{KA} = V_{REF}$		40	65	μA
I _{KA(OFF)}	Off-State Current	$V_{KA} = 36V$, $V_{REF} = 0V$		-	0.05	0.5	μA
Z _{KA}	Dynamic Output Impedance (Note 8)	$V_{KA} = V_{REF}, f = 0Hz$		-	0.2	0.5	Ω
Δ	Thermal Resistance Junction to Ambient	SOT23		-	380	-	°C/W
θ_{JA}	Thermal Resistance Junction to Ambient	SOT25		-	250	-	°C/W

^{4.} Operation above the absolute maximum rating may cause device failure. Operation at the absolute maximum ratings, for extended periods, may reduce device reliability. Unless otherwise stated voltages specified are relative to the ANODE pin.

^{5.} T_J , max = +150°C

^{6.} Ratings apply to ambient temperature at +25°C.

^{7.} Deviation of V_{DEV} , and ΔI_{REF} are defined as the maximum variation of the values over the full temperature range. 8. Derivation of Z_{KA} on following page.

VKA



Electrical Characteristics (cont.) (@TA = +25°C, unless otherwise specified.)

The average temperature coefficient of the reference input voltage αV_{REF} is defined as:

$$\left|\alpha V_{REF}\right| = \frac{\left(\frac{V_{DEV}}{V_{REF} @ 25^{\circ}C}\right) X \cdot 10^{6}}{T2 - T1} \text{ ppm/°C}$$

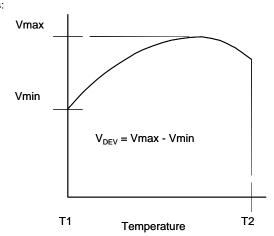
Where:

T2 - T1 = full temperature change.

αV_{REF} can be positive or negative depending on whether the slope is positive or negative.

Note: 8. The dynamic output impedance, Rz, is defined as:

$$|Z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$$



When the device is programmed with two external resistors R1 and R2, the dynamic output impedance of the overall circuit, is defined as:

$$|Z'| = \frac{\Delta V}{\Delta I} \approx |Z_{KA}| \left(1 + \frac{R1}{R2}\right)$$

Test Circuits

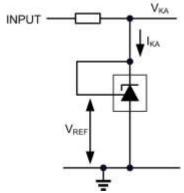


Figure 1 Test Circuit for $V_{KA} = V_{REF}$



Figure 2 Test Circuit for $V_{KA} > V_{REF}$

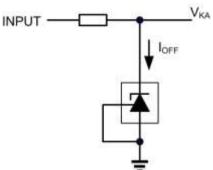
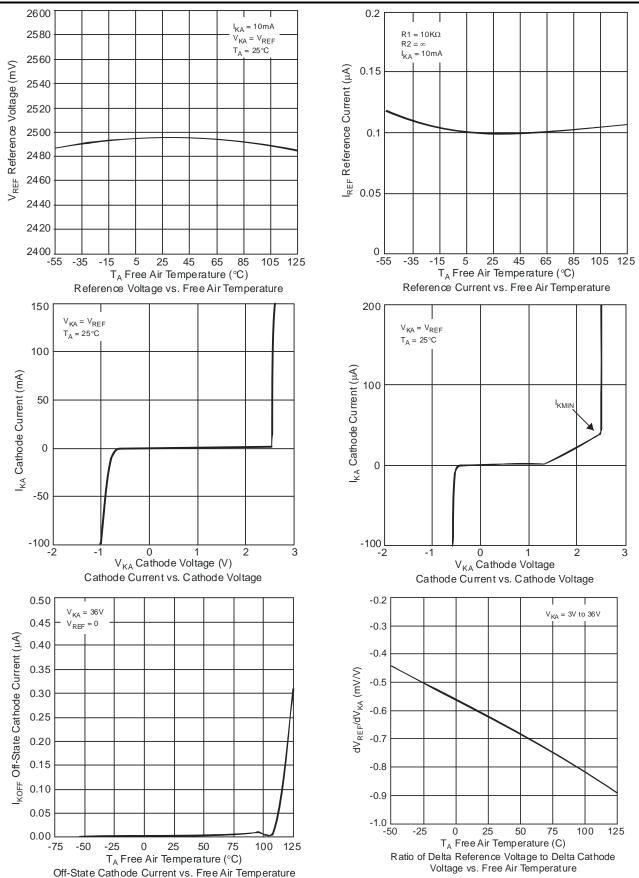


Figure 3 Test Circuit for Ioff

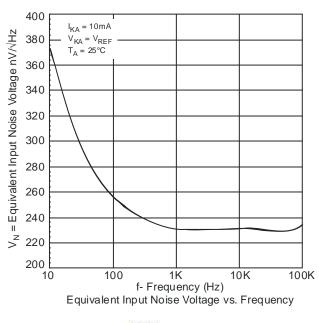


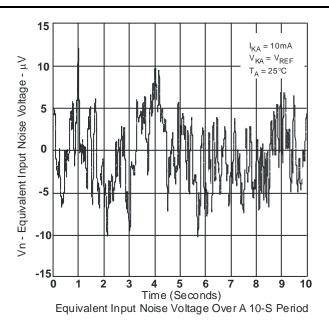
Typical Performance Characteristics





Typical Performance Characteristics (Continued)





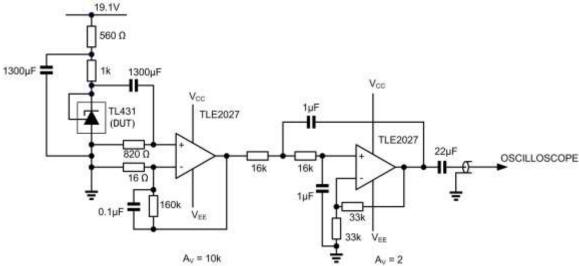
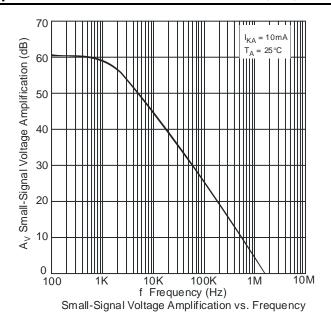
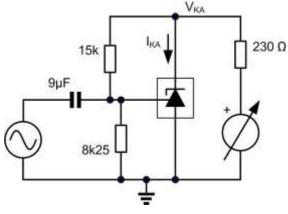


Figure 4 Test Circuit for Noise Input Voltage

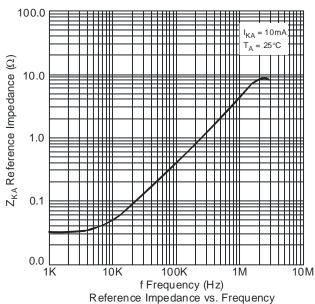


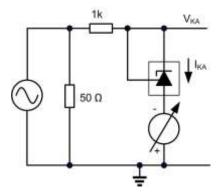
Typical Performance Characteristics (Cont.)





Test Circuit for Voltage Amplification

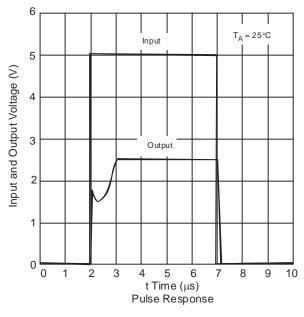


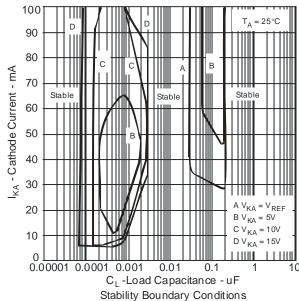


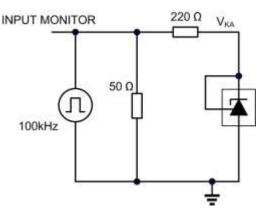
Test Circuit for Reference Impedance



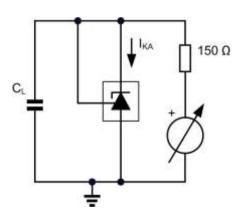
Typical Performance Characteristics (Cont.)



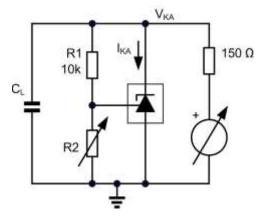




Test Circuit for Pulse Response



Test Circuit for Curve A

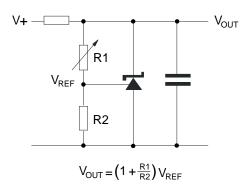


Test Circuit for Curves B, C, D

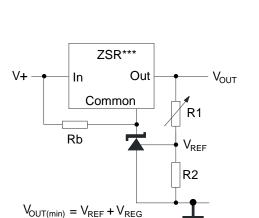
The device is stable under all conditions with a load capacitance not exceeding 50pF. The device is stable under all conditions with a load capacitance between 5nF and 20nF. The device is stable under all conditions with a load capacitance exceeding 300nF. With a cathode current not exceeding 5mA, the device is stable with any load capacitance.



Application Information



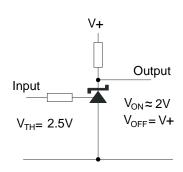
Shunt Regulator



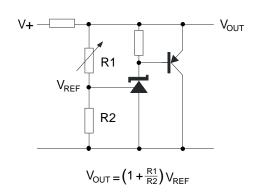
Rb - Optional to provide minimum cathode current

Output Control of a Three Terminal Fixed Regulator

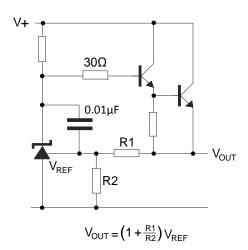
 $V_{OUT} = \left(1 + \frac{R1}{R2}\right) V_{REF}$



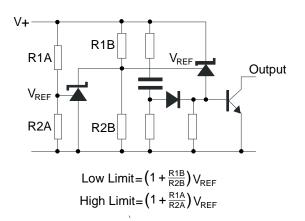
Single Supply Comparator with Temperature Compensated Threshold



Higher Current Shunt Regulator



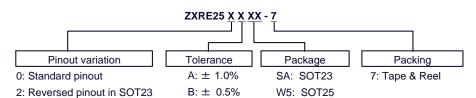
Series Regulator



Over Voltage / Under Voltage Protection Circuit



Ordering Information



Part Number	Package	Pookoging	7" Tape	and Reel	Amm	о Вох
(Note 9)	Code	Packaging	Quantity	Part Number Suffix	Quantity	Part Number Suffix
ZXRE250A(B)SA-7	SA	SOT23	3,000/Tape & Reel	-7	NA	NA
ZXRE250A(B)W5-7	W5	SOT25	3,000/Tape & Reel	-7	NA	NA
ZXRE252A(B)SA-7	SA	SOT23	3,000/Tape & Reel	-7	NA	NA

Note: 9. Suffix (B) denotes ZXRE250B (0.5% tolerance) device.

Marking Information

(1) SOT23

(Top View)

1 XX Y W X

 $\frac{XX}{Y}: \text{Identification code} \\ \frac{Y}{Y}: \text{Year } 0 \text{--} 9$

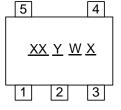
<u>W</u>: Week: A-Z: 1~26 week; a~z: 27~52 week; z represents 52 and 53 week

X: A~Z: Green 2 3

Device	Package	Identification Code
ZXRE250ASA	SOT23	DA
ZXRE250BSA	SOT23	DB
ZXRE252ASA	SOT23	FA
ZXRE252BSA	SOT23	FB

(2) SOT25

(Top View)



 $\frac{XX}{Y}: \text{Identification code} \\ \underline{Y}: \text{Year } 0\text{\sim}9$

 \underline{W} : Week : A~Z : 1~26 week; a~z : 27~52 week; z represents

52 and 53 week X: A~Z: Green

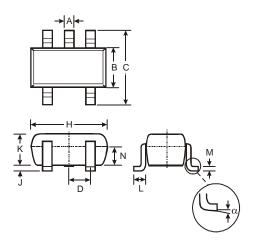
Device	Package	Identification Code
ZXRE250AW5	SOT25	DA
ZXRE250BW5	SOT25	DB



Package Outline Dimensions

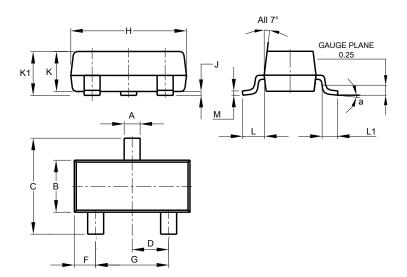
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package type: SOT25



SOT25				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D		_	0.95	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
M	0.10	0.20	0.15	
N	0.70	0.80	0.75	
α	0°	8°		
All Dimensions in mm				

(2) Package Types: SOT23



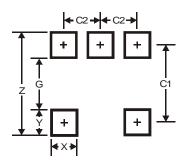
SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Ι	2.80	3.00	2.90		
7	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
٦	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
M	0.085	0.150	0.110		
а	8°				
All Dimensions in mm					



Suggested Pad Layout

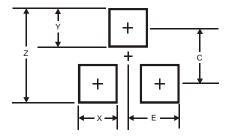
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package type: SOT25



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

(2) Package Types: SOT23



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
F	1.35



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