

TLN PulseCap™ Series



High Capacitance Tantalum Solid Electrolytic Chip Capacitors Undertab Series

CAPACITANCE AND RATED VOLTAGE RANGE (FIGURE DENOTES CASE SIZE)

Capacitance		Voltage Rating DC (V _R) to 85°C		
µF	Code	4V (G)	6.3V (J)	10V (A)
680	687			
1000	108			4(100)/6(55)
1500	158		4(100)	6(55)
2200	228		6(55)	
3300	338	6(55)		

Released ratings (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.

RATINGS & PART NUMBER REFERENCE

AVX Part No.	Case Size	Capacitance (μF)	Rated Voltage (V)	Rated Temperature (°C)	Category Voltage (V)	Category Temperature (°C)	DCL Max. (μA)	ESR Max. @ 100kHz (mΩ)	100kHz RMS Current (mA)			MSL
									25°C	85°C	125°C	
4 Volt @ 40°C												
TLN6338M004#0055	6	3300	4	40	0.8	125	132	55	2045	1840	818	3
6.3 Volt @ 40°C												
TLN4158M006#0100	4	1500	6.3	40	1.3	125	90	100	1285	1156	514	3
TLN6228M006#0055	6	2200	6.3	40	1.3	125	132	55	2045	1840	818	3
10 Volt @ 40°C												
TLN4108M010#0100	4	1000	10	40	2	125	100	100	1285	1156	514	3
TLN6108M010#0055	6	1000	10	40	2	125	100	55	2045	1840	818	3
TLN6158M010#0055	6	1500	10	40	2	125	150	55	2045	1840	818	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5V RMS with a maximum DC bias of 2.2 volts.

DCL is measured at rated voltage after 5 minutes.

ESR allowed to move up to 1.25 times catalogue limit post mounting

DCL allowed to move up to 2.00 times catalogue limit post mounting

For typical weight and composition see page 269.

NOTE: AVX reserves the right to supply higher voltage ratings or tighter tolerance part in the same case size, to the same reliability standards.

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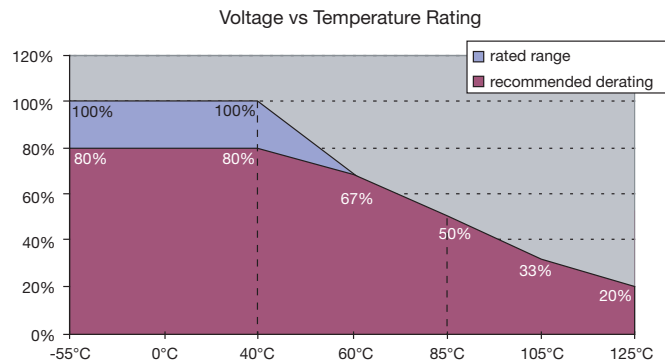


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QUALIFICATION TABLE

TEST	TLN PulseCap™ series (Temperature range -55°C to +125°C)									
	Condition			Characteristics						
Endurance	Apply rated voltage (Ur) at 40°C and / or category voltage (Uc) at 85°C for 2000 hours through a circuit impedance of ≤0.1Ω/V. Stabilize at room temperature for 1-2 hours before measuring.			Visual examination	no visible damage					
				DCL	2 x initial limit					
				ΔC/C	within +5/-30% of initial value					
				ESR	1.25 x initial limit					
Humidity	Store at 65°C and 90-95% relative humidity for 500 hours, with no applied voltage. Stabilize at room temperature and humidity for 1-2 hours before measuring.			Visual examination	no visible damage					
				DCL	2 x initial limit					
				ΔC/C	within ±10% of initial value					
				ESR	1.25 x initial limit					
Temperature Stability	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+85°C	+125°C	+20°C
	1	+20	15							
	2	-55	15	DCL	2 x IL*	n/a	2 x IL*	20 x IL*	25 x IL*	2 x IL*
	3	+20	15							
	4	+85	15	ΔC/C	n/a	+5/-20%	±10%	+20/-0%	+25/-0%	±10%
	5	+125	15							
	6	+20	15	ESR	1.25 x IL*	2.5 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL*
Surge Voltage	Apply 1.3x rated voltage (Ur) at 40°C for 1000 cycles of duration 6 min (30 sec charge, 5 min 30 sec discharge) through a charge / discharge resistance of 1000Ω			Visual examination	no visible damage					
				DCL	2 x initial limit					
				ΔC/C	within ±5% of initial value					
				ESR	1.25 x initial limit					

*Initial Limit

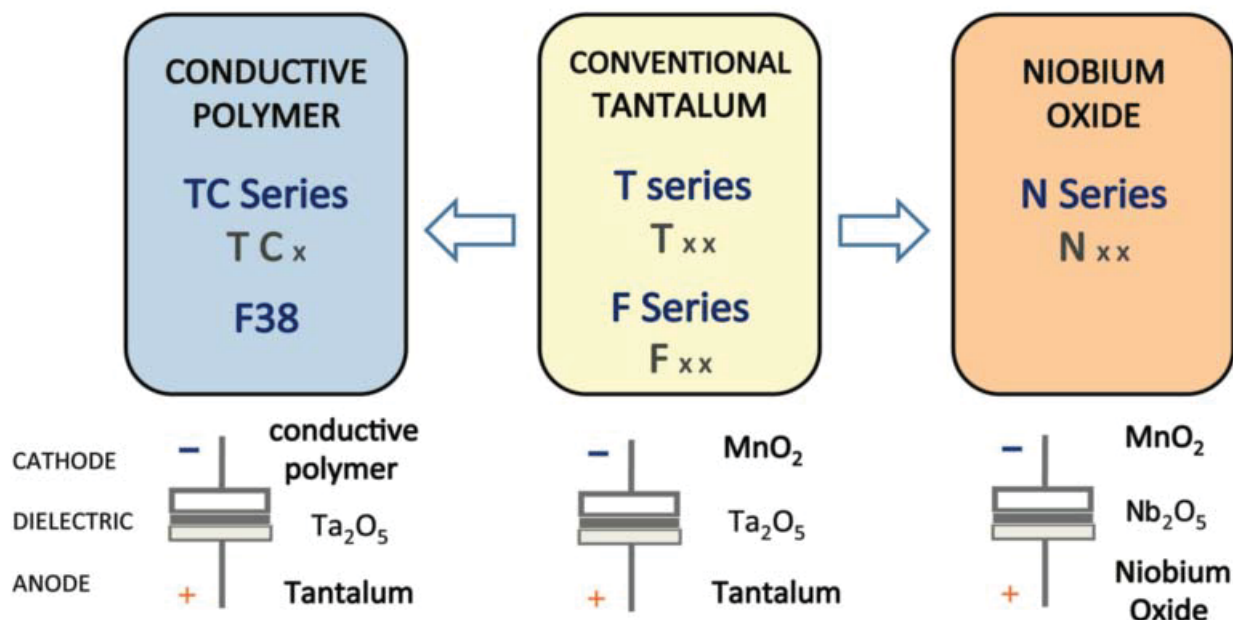


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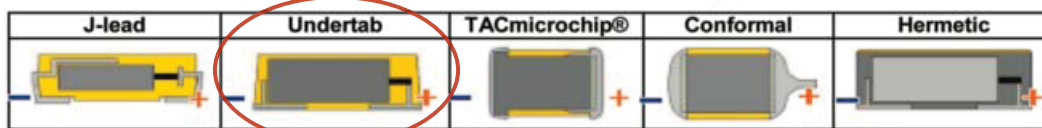


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AVX SOLID ELECTROLYTE CAPACITOR ROADMAP



Five Capacitor Construction Styles



SERIES LINE UP: CONVENTIONAL SMD MnO₂

