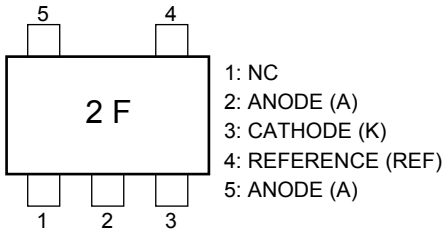
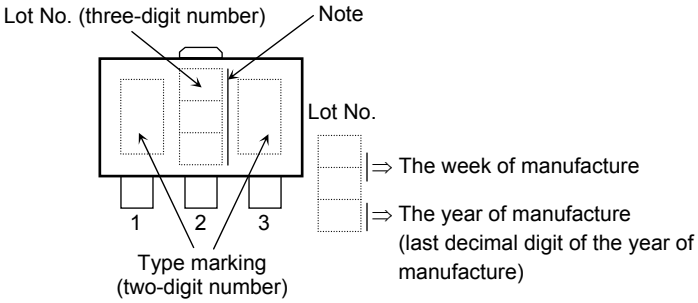
Pin Assignment/Marking

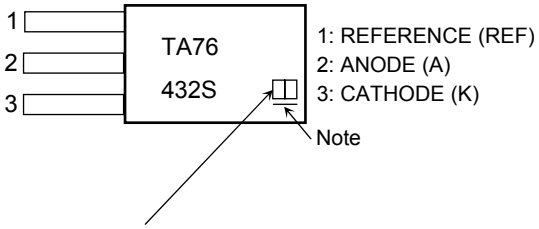
TA76432FT/TA76432FC



TA76432F/FR



TA76432S



Lot No: The last decimal digit of the year of manufacture followed by the month as letters A to L of the alphabet.
For example: Jan-2001 is coded as “1A”

No.	TA76432F	TA76432FR
1	CATHODE (K)	REFERENCE (REF)
2	ANODE (A)	ANODE (A)
3	REFERENCE (REF)	CATHODE (K)

Note: TA76432F vs. TA76432FR: reverse pin connection.

Note: A line under a Lot No. identifies the indication of product Labels.
Not underlined: [[Pb]]/INCLUDES > MCV
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

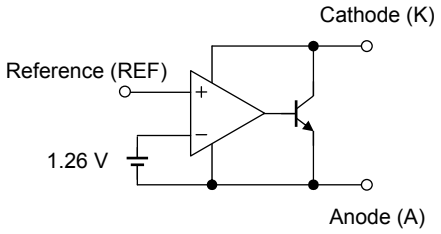
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

How to Order

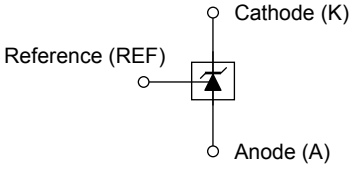
Product No.	Package Type	Packing Type and Capacity	Minimum Order
TA76432FT (TE85L)	UFV (surface-mount type)	Embossed tape: 3000/tape	1 tape
TA76432FC (TE85L)	SMV (surface-mount type)	Embossed tape: 3000/tape	1 tape
TA76432F/R	PW-MINI (SOT-89) (surface-mount type)	On cut tape (TE12L): 100/tape section	100
TA76432F/R (TE12L)		Embossed tape: 1000/tape	1 tape
TA76432S	TO-92MOD (lead type)	Loose in bag: 200/bag	1 bag
TA76432S (TPE6)		Radial tape: 2000/tape	1 tape

Note: The lead pitch for the TA76432S and TA76432S (TPE6) may vary.

Functional Block Diagram

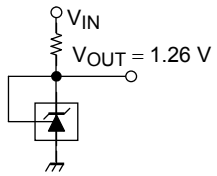


Circuit Symbol

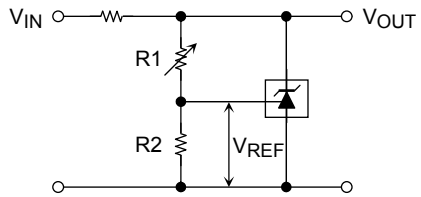


Typical Application Circuits

1.26 V Reference ($V_{KA} = V_{REF}$)



Shunt regulator ($V_{KA} > V_{REF}$)



$$V_{OUT} = V_{REF}\left(1 + \frac{R_1}{R_2}\right) + I_{REF} \cdot R1$$

Precautions during Use

(1) TA76432FT, TA76432FC, TA76432F/FR, TA76432S
These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

(2) TA76432FT, TA76432FC, TA76432F/FR, TA76432S
The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.
When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin.
Use of a laminated ceramic capacitor is recommended.

(3) Precautions when handling anode pins of TA76432FT/TA76432FC
Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Cathode voltage		V_{KA}	20	V
Cathode current		I_K	20	mA
Cathode-anode reverse current		$-I_K$	10	mA
Reference voltage		V_{REF}	7	V
Reference current		I_{REF}	50	μA
Reference-anode reverse current		$-I_{REF}$	10	mA
Power dissipation	TA76432FT	P_D	0.45 (Note 1)	W
	TA76432FC		0.2	
			0.38 (Note 2)	
	TA76432F/FR		0.5	
	TA76432S		0.8	
Thermal resistance	TA76432FT	R_{th}	277 (Note 1)	°C/W
	TA76432FC		625	
			328 (Note 2)	
	TA76432F/FR		250	
	TA76432S		156	
Operating temperature		T_{opr}	−40~85	°C
Junction temperature		T_j	150	°C
Storage temperature		T_{stg}	−55~150	°C

Note 1: Glass epoxy board mounting: 30 mm × 30 mm × 0.8 mmt (Cu pad area 35 mm^2)

Note 2: Glass epoxy board mounting: 30 mm × 30 mm × 0.8 mmt (Cu pad area 50 mm^2)

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

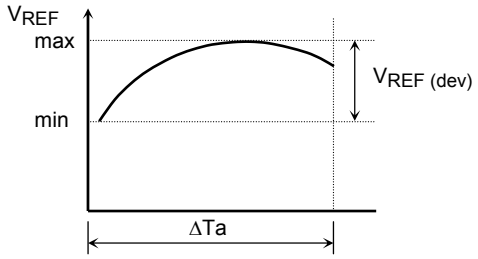
Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Cathode voltage	V_{KA}	V_{REF}	—	19	V
Cathode current	I_K	0.5	—	15	mA
Operating temperature	T_{opr}	−40	—	85	°C

Electrical Characteristics
(Unless otherwise specified, Ta = 25°C, I_K = 5 mA)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reference voltage	V_{REF}	$V_{KA} = V_{REF}$	1.242	1.26	1.278	V
Deviation of reference input voltage over temperature	$V_{REF\ (dev)}$	0°C ≦ Ta ≦ 85°C, $V_{KA} = V_{REF}$	—	3	15	mV
Ratio of change in reference input voltage to the change in cathode voltage	$\Delta V_{REF}/\Delta V$	$V_{REF} \leqq V_{KA} \leqq 5$ V	—	0.5	2.5	mV/V
		5 V ≦ V_{KA} ≦ 19 V	—	0.3	2.0	
Reference input current	I_{REF}	$V_{KA} = V_{REF}$	—	2	4	μA
Deviation of reference input current over temperature	$I_{REF\ (dev)}$	0°C ≦ Ta ≦ 85°C, $V_{KA} = V_{REF}$, R_1 = 10 kΩ, $R_2 = \infty$	—	0.3	1.2	μA
Minimum cathode current for regulation	I_{Kmin}	$V_{KA} = V_{REF}$	—	0.2	0.5	mA
Off-State cathode current	I_{Koff}	V_{KA} = 19 V, V_{REF} = 0 V	—	—	1.0	μA
Dynamic impedance	$\lvert Z_{KA} \rvert$	$V_{KA} = V_{REF}$, f ≦ 1 kHz, 0.5 mA ≦ I_K ≦ 15 mA	—	0.2	0.5	Ω

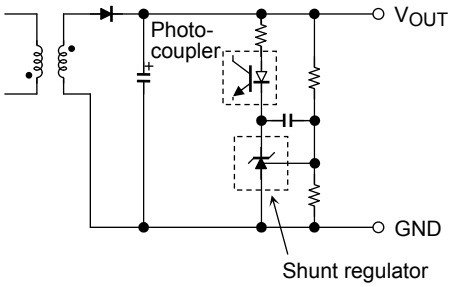
The deviation parameters $V_{REF\ (dev)}$ and $I_{REF\ (dev)}$ are defined as the maximum variation of the V_{REF} and I_{REF} over the rated temperature range.
The average temperature coefficient of the V_{REF} is defined as:



$$\left|\alpha V_{REF}\right| = \frac{\left(\frac{V_{REF\ (dev)} \times 10^6}{V_{REF}\ @25°C}\right)}{\Delta Ta} (ppm/°C)$$

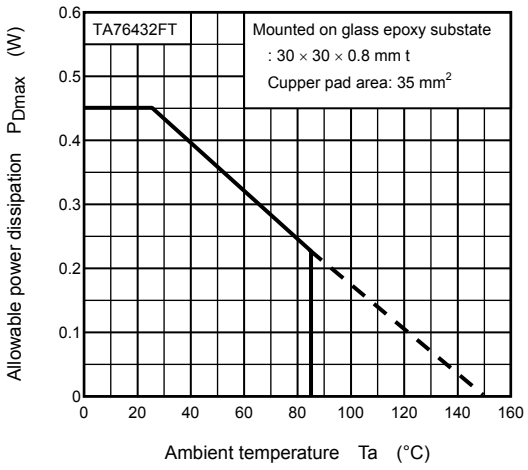
Application Circuit Example

Error amplification circuit for the switching power supply

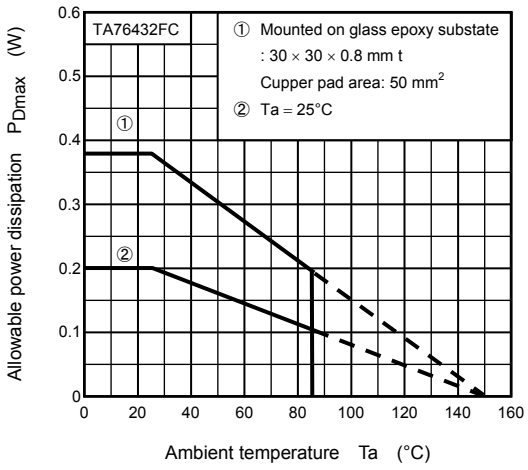


This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

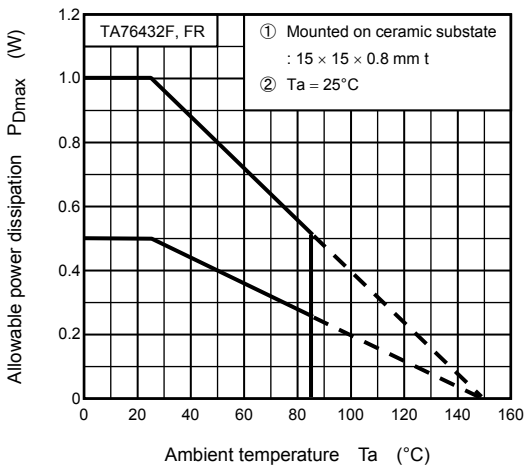
P_{Dmax} – Ta



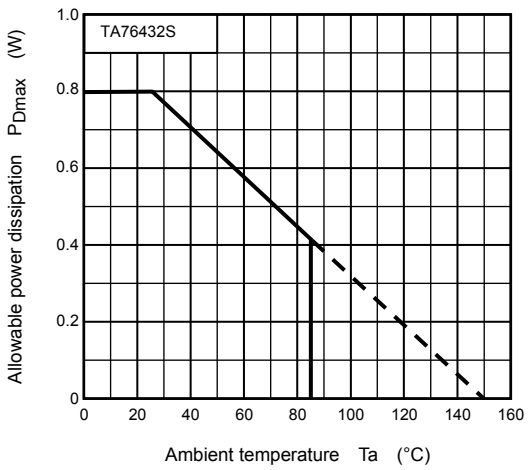
P_{Dmax} – Ta



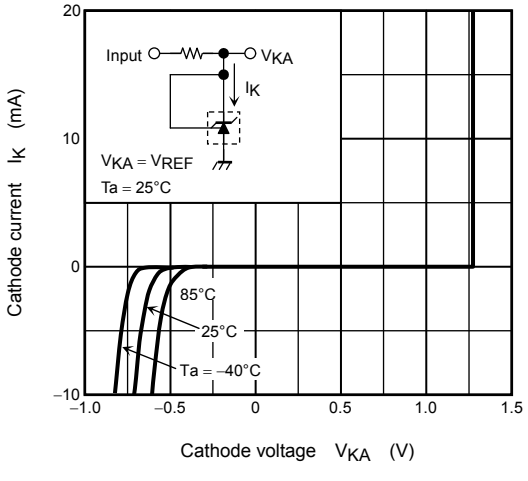
P_{Dmax} – Ta



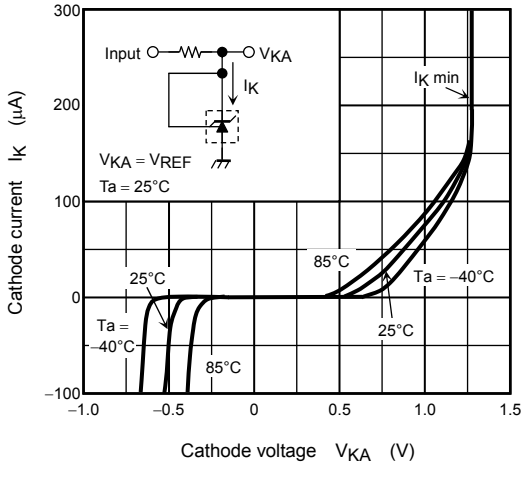
P_{Dmax} – Ta



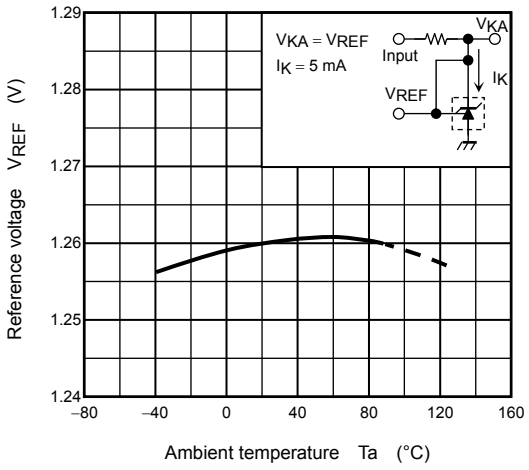
I_K – V_{KA}



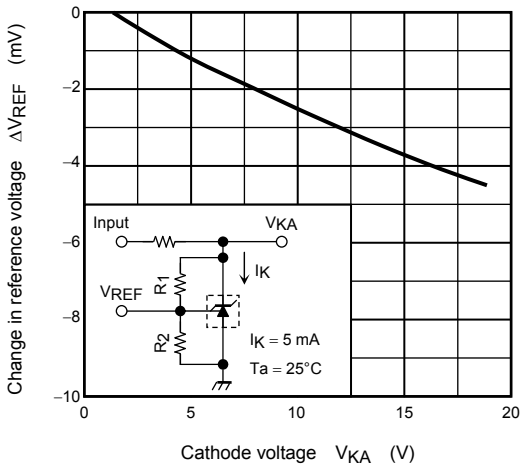
I_K – V_{KA}



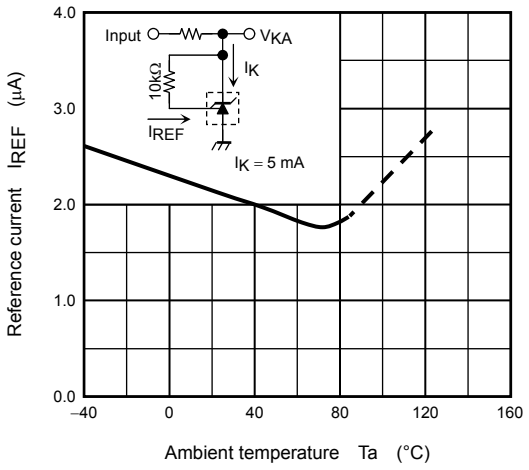
V_{REF} – Ta



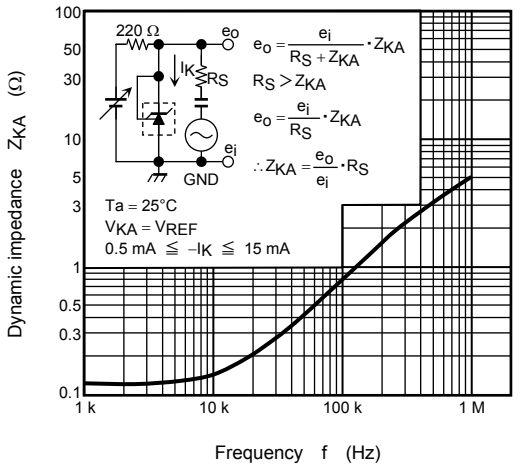
ΔV_{REF} – V_{KA}



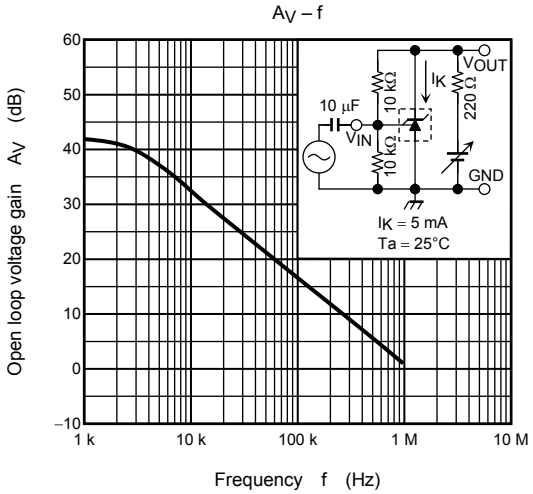
I_{REF} – Ta



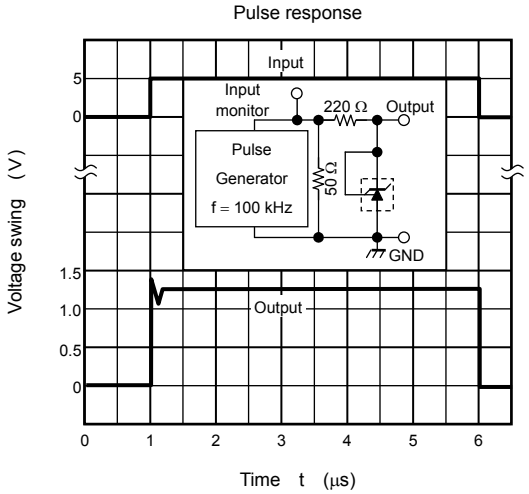
Z_{KA} – f

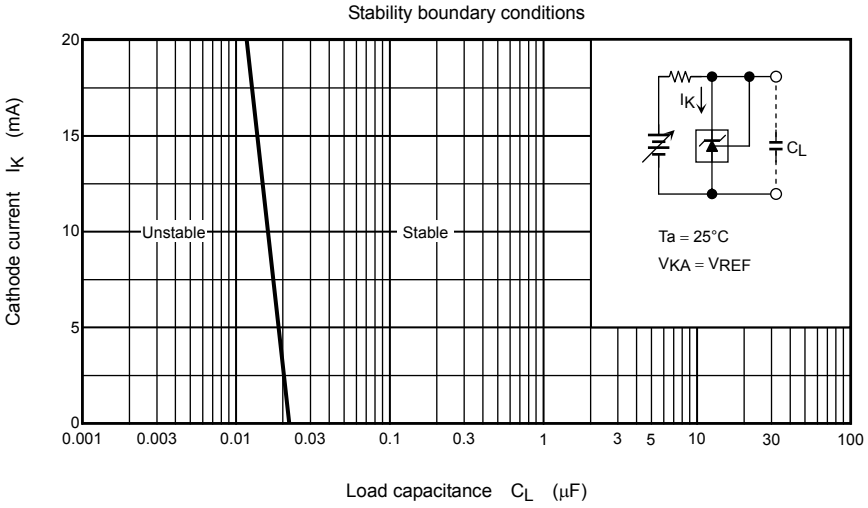


A_V – f



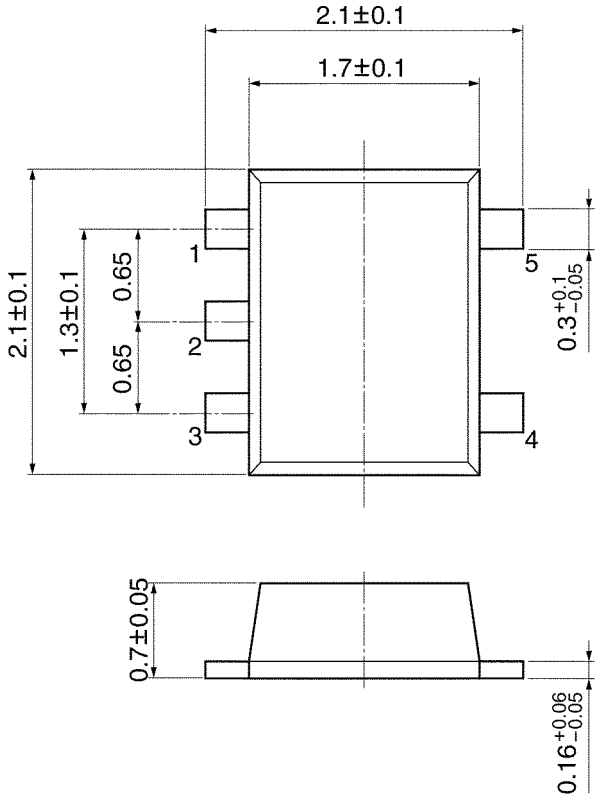
Pulse response





Package Dimensions

SSOP5-P-0.65C Unit: mm

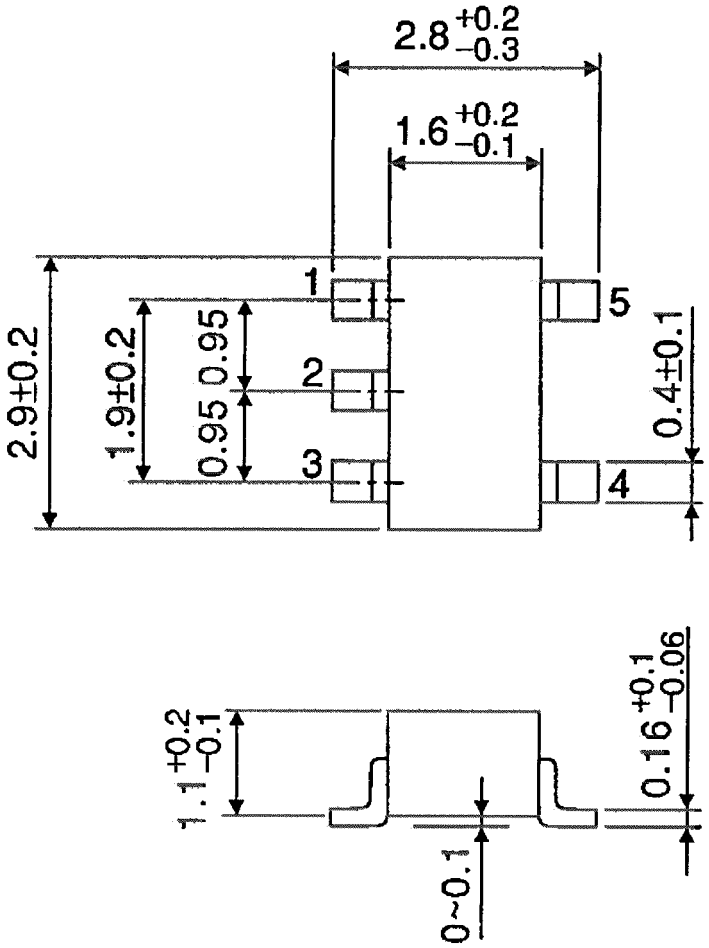


TA76432FT (UFV)

Weight: 0.007 g (typ.)

Package Dimensions

SSOP5-P-0.95 Unit : mm

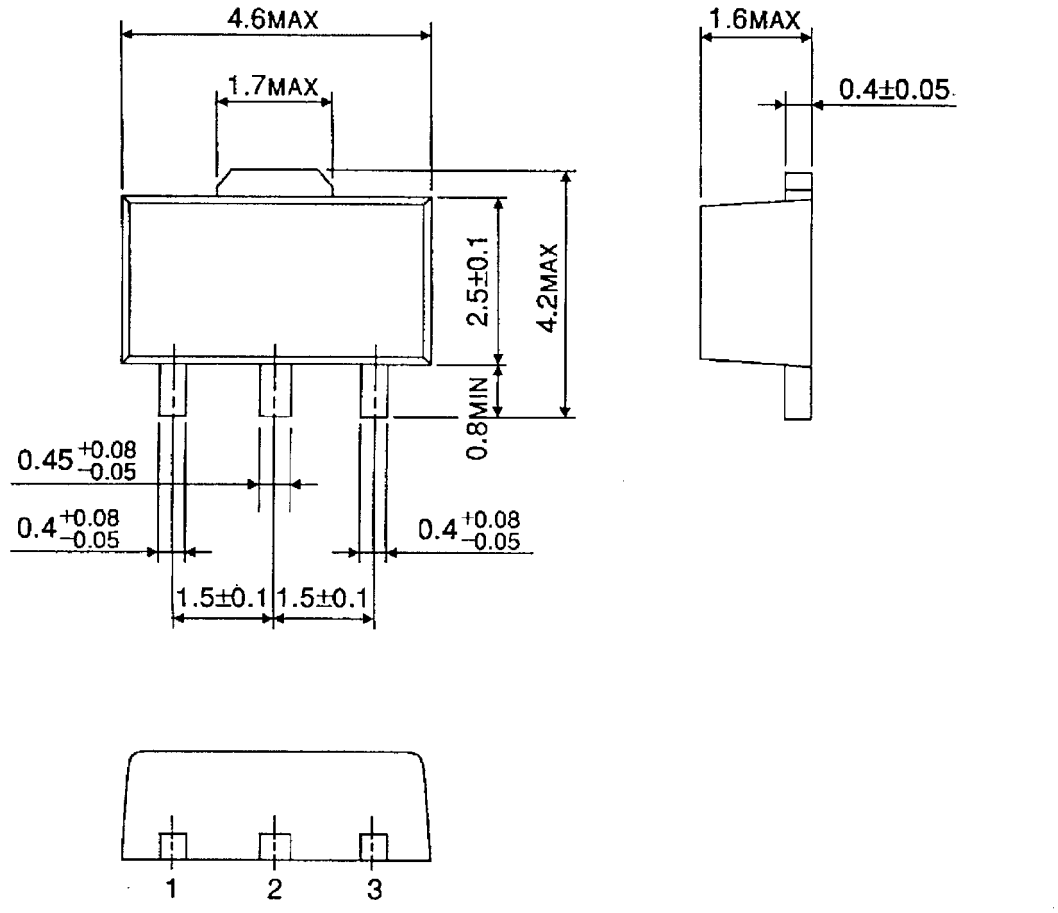


TA76432FC (SMV)

Weight: 0.014 g (typ.)

Package Dimensions

HSOP3-P-1.50 Unit : mm

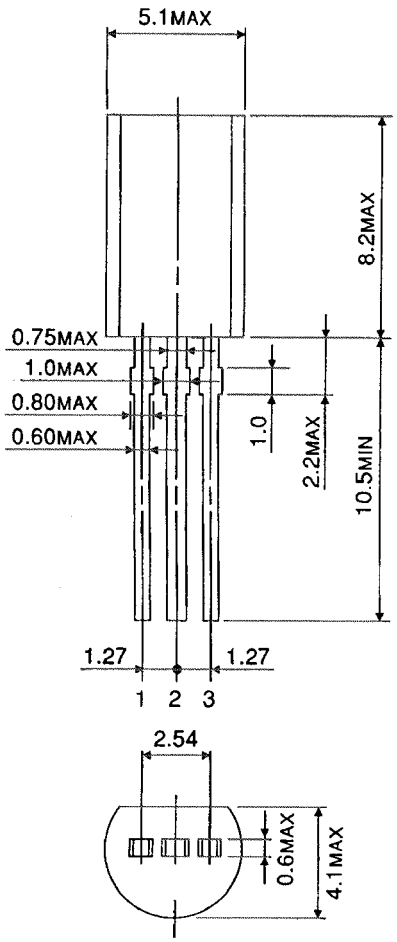


TA76432F/FR (PW-MINI)

Weight: 0.05 g (typ.)

Package Dimensions

SSIP3-P-1.27



TA76432S (TO-92MOD)

Weight: 0.36 g (typ.)

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