SUD50N04-07L

Vishay Siliconix



Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 32 V, V _{GS} = 0 V			1	μΑ
		V _{DS} = 32 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 32 V, V _{GS} = 0 V, T _J = 175 °C			150	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	65			Α
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.006	0.0074	Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C			0.012	
		V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C			0.015	
		V _{GS} = 4.5 V, I _D = 10 A		0.0085	0.011	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	20	57		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		2800		pF
Output Capacitance	C _{oss}			320		
Reversen Transfer Capacitance	C _{rss}			190		
Total Gate Charge ^c	Q_g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 50 A		50	75	nC
Gate-Source Charge ^c	Q_{gs}			10		
Gate-Drain Charge ^c	Q_{gd}			10		
Gate Resistance	R _g			2.0		Ω
Turn-On Delay Time ^c	t _{d(on)}	V_{DD} = 20 V, R_L = 0.4 Ω $I_D \cong$ 50 A, V_{GEN} = 10 V, R_G = 2.5 Ω		11	20	- ns
Rise Time ^c	t _r			20	30	
Turn-Off Delay Time ^c	t _{d(off)}			40	60	
Fall Time ^c	t _f			15	25	
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C) ^b				
Continuous Current	Is	-			43	А
Pulsed Current	I _{SM}				100	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		0.90	1.50	V
Reverse Recovery Time	t _{rr}	I _F = 30 A, di/dt = 100 A/μs		30	45	ns

Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

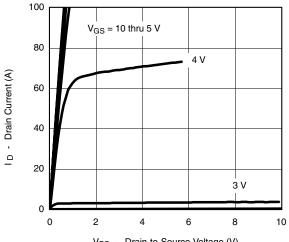
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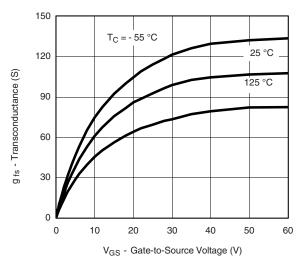
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TYPICAL CHARACTERISTICS 25 °C unless noted

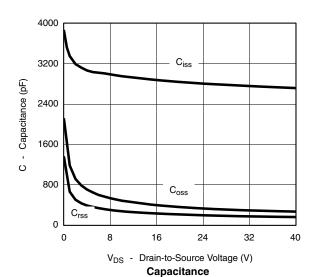


V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics

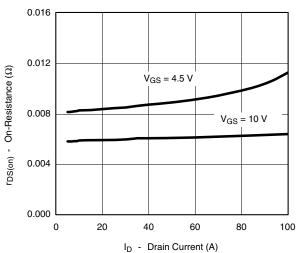


Transconductance

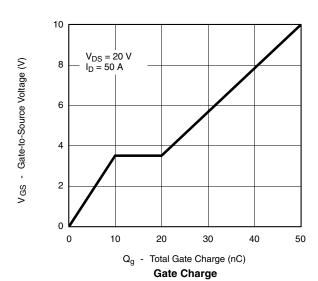


100 80 I D - Drain Current (A) 60 40 T_C = 125 °C 20 25 °C - 55 °C 0 0.0 0.5 1.0 1.5 2.0 2.5 3.0 4.0 4.5 V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics



On-Resistance vs. Drain Current



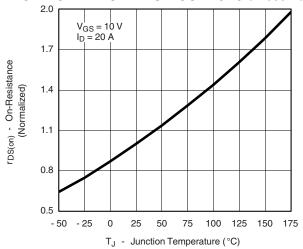
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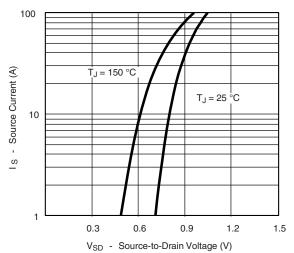
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TYPICAL CHARACTERISTICS 25 °C unless noted



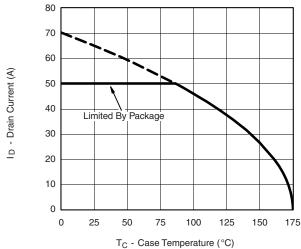


On-Resistance vs. Junction Temperature

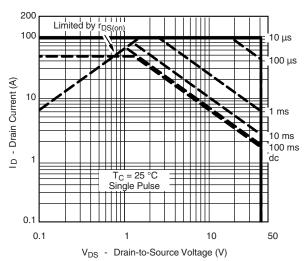


Source-Drain Diode Forward Voltage

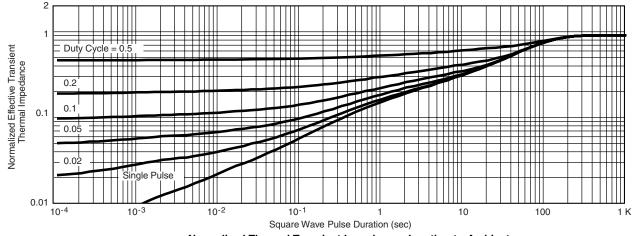
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



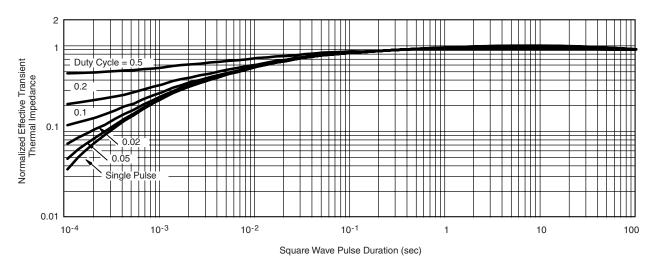
Normalized Thermal Transient Impedance, Junction-to-Ambient





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THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72344.

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